MAN-SAT149

ANTENNA SERVO, DRIVE AND CONTROL SUB-SYSTEM

VOLUME SIX

ILLUSTRATIONS

GROUPS 02, 12, 22, 32.

WARNING

VOLTAGES USED IN THIS EQUIPMENT CAN ENDANGER LIFE.

ISOLATE THE EQUIPMENT FROM POWER SUPPLIES BEFORE MAKING INTERNAL ADJUSTMENTS.

IF IT IS ESSENTIAL TO WORK ON LIVE EQUIPMENT, THE WORK MUST ONLY BE PERFORMED BY QUALIFIED PERSONNEL WHO ARE AWARE OF THE RISKS INVOLVED AND WHO HAVE TAKEN ADEQUATE PRECAUTIONS.

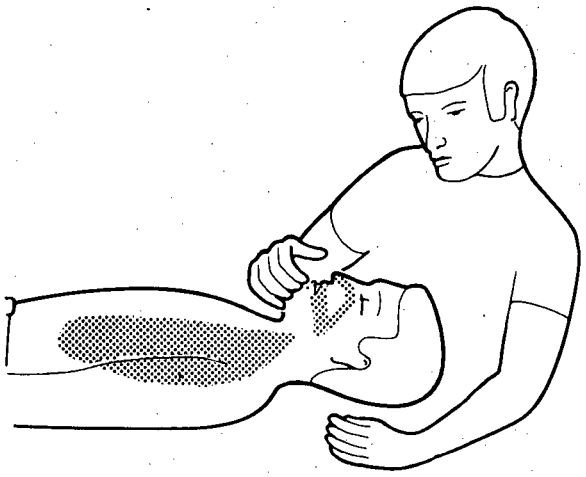
THE PROCEDURE TO BE ADOPTED IN CASES OF ELECTRICAL SHOCK IS GIVEN ON PAGE (iv).

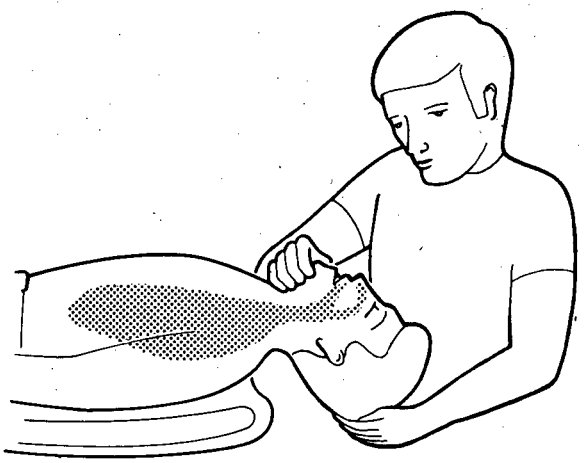
THE 'KISS OF LIFE' RESUSCITATION

WHEN BREATHING HAS STOPPED DUE TO ELECTRIC SHOCK

1. INITIAL PROCEDURE

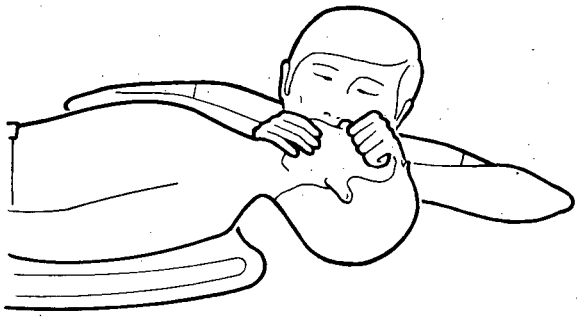
Switch off the current. If this is impossible free the person using something made of rubber, cloth or wood or a folded newspaper; use the casualty's own clothing if dry. Do not touch his skin before the current is switched off. If the victim's breathing has stopped IMMEDIATE EFFORT to restart it is essential. EVERY SECOND COUNTS.

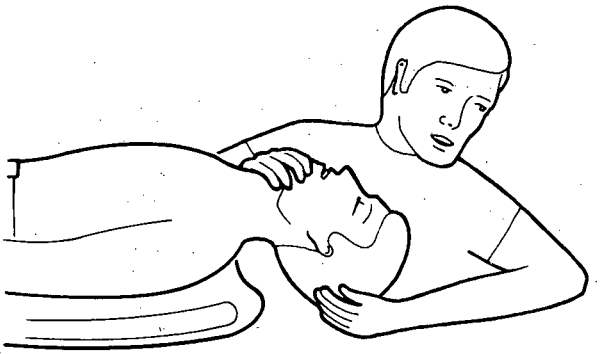




2. STARTING POSITION

Victim face up. Tilt head back and pull chin forward to open air passages.





3. INFLATION

Seal victim's nose by pinching nostrils. Open your mouth wide and inflate the victim's lungs by blowing air into his mouth.

4. EXHALATION

When you see the victim's chest rise remove your mouth to allow the air to escape from his lungs, and turn your head to one side. Continue with 3 & 4 until natural respiration returns.

Do not exceed 10-12 breaths per minute. If stomach contents are regurgitated, turn the victim's head to one side and clean out his mouth. When there are signs of natural respiration returning adjust your breathing to coincide with the victim.

HANDBOOK CONTENTS

VOLUME CONTENTS

VOLUME ONE	PART ONE	TECHNICAL DETAILS SECTIONS ONE TO SIX
VOLUME TWO	PART ONE	TECHNICAL DETAILS SECTION TWO - APPENDIX A (ANC 1720)
VOLUME THREE	PART TWO PART THREE PART FOUR	MAINTENANCE FAULT LOCATION COMPONENTS LIST
VOLUME FOUR	ILLUSTRATIONS	GROUPS 00, 10, 20, 30.
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PREFACE

A detailed preface is included in the preliminary pages of Volume One. This preface defines the figure and group-sheet coding, component identification, cross referencing and connector coding used throughout this handbook.

SUB RACKS

	10	09	08		07		06		05		04		03	02	01	
			COMBINED SUBRACKS				COMBINED SUBRACKS									
01			UV006α	S	XU402α R2	ZS	JC 218α– TL	ZS	JC 218α DC ε	ZS	JC 218α DCα	ZS				01
02							JC 218α–SC	ZS	JC 218α VC ε	ZS	JC 218α VCα	ZS				02
03						S	JC 218α–AM	ZS	JC 218α DEC	ZS	JC 218α DEC	ZS				03
04			AP 704α	S	AP 704α	S	ZT 230α-CP	ZSS	JC 244α AVR ε	ZSS	JC 244α AVR α	ZSS				04
05			UV005α	S	UV 005α	S	JC 218α RC II	ZS	JC 218α INV ε	ZS	JC 218α INVα	ZS				05
06				S		S	JC 218α RC I	ZS	JC 244α NVR ε	ZSS	JC 244α NVRα	ZSS				06
07			DV 001α	S	DV 001α	S	JC 246α CRC	ZSS	JC 244α RVR ε	ZSS	JC 244α RVRα	ZSS				07
08				S		S	JC 245α R1ADD	ZSS	JC 244α BR ε	ZSS	JC 244α BRα	ZSS				08
09			PT023α	ZS	PT 023 α	ZS	JC 244 α AC	ZSS	JC 218α SOL ε	ZS	JC 218α SOLα	ZS				09
10			ST 007α	ZS	ST 007 α	ZS	JC 218α ADC	ZS	JC 218α CR ε	ZS	JC 218α CRα	ZS				10
11	POWER SUPPLY GVR2 4586	FAN UNIT GVR2 4585					JC 215α R1 FVS III	ZS	JC 215α R1 FVS II	ZS	JC 215α R1 FVS I	ZS		TEST PANEL GVR2 4582	INPUT-OUTPUT-PANEL GVR2 4581	11
12			ST 008α	S	ST 008α	S	JC 218α SEC	ZS	JC 218α DOUT ε	ZS	JC 218α DOUTα	ZS				12
13				S		S	TEST	ZSS		ZSS		ZSS				13
14			ST 005α	S	ST 005 α	S	JC 218α ADT	ZS		ZSS		ZSS				14
15							JC 218α MED	ZS		ZSS		ZSS				15
16			ST 006α	S	ST 006 α	S	JC 218α PED	ZS		ZSS		ZSS				16
17				S		S	JC 218α SDI	ZS		ZSS		ZSS				17
18			(ST 013) GJR2 1796	ZS	(ST 013) GJR2 1796	ZS	JC 218α INV	ZS	XZ 926α R1	ZSS	XZ 926 α R1	ZSS				18
19				ZS		ZS	JC 242α-E WC	ZSS	LT 234 α	ZSS	LT 234 α	ZSS				19
20			(ST 014) GJR2 1797	ZS	(ST 014) GJR2 1797	ZS		ZSS	LT 234 α	ZSS	LT 234 α	ZSS				20
21				ZS		ZS		ZSS	LT 234 α	ZSS	LT 234 α	ZSS				21
22			JC 214 α	ZS	JC 214 α	ZS		ZSS	UT 230 α	ZS	UT 230 α	ZS				22
23			JC 214 α	ZS	JC 214 α	ZS		ZSS	UT 230 α		UT 230α	ZS				23
24			JC 214 α	ZS	JC 214 α	ZS		ZSS	XZ 926 α R1	ZSS	UT 230α	ZS				24
			ELEVATION POSITION		AZIMUTH DISPLAY		ADDER + REGISTER									

NUMBER OF DIVISION

Rack H6 : Location of Plug-in Cards and Sub-Units

Figure 02-06

Part 1

LIST OF ILLUSTRATIONS

GROUP 02

* Indicates interconnection table.

GROUP 12

Figure No. (Group-Sheet)	Title
12 07	* Interconnection Table : Card u0106 (LU230)
12 08	* Interconnection Table : Card u0107 (LU230)
12 09	* Interconnection Table : Card u0108 (LU230)
12 10	* Interconnection Table : Card u0109 (LU230)
12 11	* Interconnection Table : Card u0110 (LR240)
12 12	* Interconnection Table : Card u0111 (LR240)
12 13	* Interconnection Table : Card u0112 (LR240)
12 14	* Interconnection Table : Card u0113 (UR240)
12 15	* Interconnection Table : Card u0114 (UR240)
12 16	* Interconnection Table : Card u0115 (UR240)
12 17	* Interconnection Table : Card u0116 (UR240)
12 18	* Interconnection Table : Card u0121 (LU230)
12 19	* Interconnection Table : Card u0122 (LU230)
12 20	* Interconnection Table : Card u0123 (LU230)
12 21	* Interconnection Table : Card u0124 (LU230)
12 22	* Interconnection Table : Card u0125 (LU230)
12 23	* Interconnection Table : Card u0126 (LU230)
12 24	* Interconnection Table : Card u0127 (LU230)
12 25	* Interconnection Table : Card u0128 (LU230)
12 26	* Interconnection Table : Card u0129 (LU230)
12 27	* Interconnection Table : Card u0130 (LU230)
12 28	* Interconnection Table : Card u0131 (LR240)
12 29	* Interconnection Table : Card u0132 (LR240)
12 30	* Interconnection Table : Card u0133 (LR240)
12 31	* Interconnection Table : Card u0134 (LR240)
12 33	Selection of Code Switches for Preset
12 34	Code Switches for Tracklength
12 35	Code Switches for Number of Steps
12 36	Input/Output Sub-Rack Power Distribution
12 37	* Connectors on the Screened Side of the Input/Output Sub-Rack
12 38	* Rack Terminal Block : 0161
12 39	* Interconnection Table : 0162
12 40	* Interconnection Table : 0163
12 41	* Interconnection Table : 0164
12 42	* Interconnection Table : 0165
12 43	* Interconnection Table : 0166

Figure No. (Group-Sheet)		Title
12 44	*	Interconnection Table : 0167
12 46		Test Panel : Circuit Diagram
12 47	*	Test Panel : Interconnection Table
12 51		Code Switch Assembly : Connection Diagram

* Indicates interconnection table.

CENTRAL UNIT

12 60		Fixed Value Store - JC215a-E : Circuit Diagram
12 61		Serial Data Input, BCD→ Aiken, Parity Check - JC218a-E : Circuit Diagram
12 62		Code Switch Request - JC218a-E : Circuit Diagram
12 63		Digital Copy Control - JC218a-E : Circuit Diagram
12 64		Value Counter - JC218a-E : Circuit Diagram
12 65		Scan Control (MISC) - JC218a-E : Circuit Diagram
12 66		Axis Modifier Control (MIAM) - JC218a-E : Circuit Diagram
12 67		Tracklength → $^{1}/_{2}$ Tracklength - JC218a-E : Circuit Diagram
12 68		Decade and Step Counters - JC218a-E : Circuit Diagram
12 69		Processor Error Detector - JC218a-E : Circuit Diagram
12 70		Measurement Error Detector - JC218a-E : Circuit Diagram
12 71		Servo on Line Control Register - JC218a-E : Circuit Diagram
12 72		Register Control I - JC218a-E : Circuit Diagram
12 73		Inverter - JC218a-E : Circuit Diagram
12 74		Addend Data Output - JC218a-E : Circuit Diagram
12 75		Addend Control - JC218a-E : Circuit Diagram
12 76 SHT 1		Secant Decoder - JC218a-E : Circuit Diagram
12 76 SHT 2		Secant Decoder - JC218a-E : Circuit Diagram
12 77		Difference Output and Controls - JC218a-E : Circuit Diagram
12 78		Register Control II - JC218a-E : Circuit Diagram
12 79		Storage Register - JC244a : Circuit Diagram
12 80 SHT 1		Adder Card - JC245a-R1 : Circuit Diagram
12 80 SHT 2		Adder Card - JC245a-R1 : Circuit Diagram
12 80 SHT 3		Adder Card - JC245a-R1 : Circuit Diagram
12 80 SHT 4		Adder Card - JC245a-R1 : Circuit Diagram
12 81 SHT 1		Sequence Control Card - JC246a-E : Circuit Diagram
12 81 SHT 2		Sequence Control Card - JC246a-E : Circuit Diagram

Figure No. (Group-Sheet)		Title
12 81 SHT 3		Sequence Control - JC246a-E : Circuit Diagram
12 82		LT 234a-E : Circuit Diagram
12 83		Clock-Pulse Generator - ZT 230a-E : Circuit Diagram
12 84		Decimal Decoder for Weight Correction : Circuit Diagram
12 93 SHT 1	*	Actual Value Register, AVRα (u0404 JC244)
12 93 SHT 2	*	Actual Value Register, AVRα (u0404 JC244)
12 94	*	Inverter, INVα (u0405 JC218)
12 95 SHT 1	*	Nominal Value Register, NVRα (u0406 JC244a)
12 95 SHT 2	*	Nominal Value Register, NVRα (u0406 JC244a)
12 96 SHT 1	*	Readout Register, RVRα (u0407 JC244a)
12 96 SHT 2	*	Readout Register, RVRα (u0407 JC244a)
12 97 SHT 1	*	Buffer Register, BRα (u0408 JC244a)
12 97 SHT 2	*	Buffer Register, BRα (u0408 JC244a)
12 98	*	Store Servo On Line Control, SOLα (u0409 JC218a)
12 102 SHT 1	*	Signal Display Adapter (u0413)
12 102 SHT 2	*	Signal Display Adapter (u0413)
12 107	*	Isolating Diodes (u0418 XZ 926)
12 108		Card u0418 XZ 926a-ER1 : Component Layout
12 109	*	Selection Code Switch, DOUTα (u0419 LT234a)
12 110	*	Readout α (u0420 LT234a)
12 111	*	Secant and MACP Displays (u0421 LT234a)
12 112	*	Number of Turns (u0422 UT230a)
12 113	*	Tracklength (u0423 UT230a)
12 114	*	Measurement System Signal Inputs (u0424 UT230a)
12 115	*	Sub-Rack Terminal Block
12 116	*	Interconnection Table : Terminal Block : 0461
12 117	*	Interconnection Table : Terminal Block : 0462
12 118	*	Interconnection Table : Terminal Block : 0463
12 119	*	Interconnection Table : Terminal Block : 0464
12 120	*	Interconnection Table : Terminal Block : 0465
12 127 SHT 1	*	Actual Value Register, AVRε (u0504 JC244a)
12 127 SHT 2	*	Actual Value Register, AVRε (u0504 JC244a)
12 128	*	Inverter, INVε (u0505 JC218a-E)
12 129 SHT 1	*	Nominal Value Register, NVRε (u0506 JC244a)
12 129 SHT 2	*	Nominal Value Register, NVRε (u0506 JC244a)
12 130 SHT 1	*	Readout Register, RVRε (u0507 JC244a)

Figure No. (Group-Sheet)			Title
12	130 SHT 2	*	Readout Register, RVRε (u0507 JC244a)
12	131 SHT 1	*	Buffer Register, BRε (u0508 JC244a)
12	131 SHT 2	*	Buffer Register, BRε (u0508 JC244a)
12	132	*	Store Servo On Line Control, SOLε (u0509 JC218)
12	133	*	Code-Switch Request, CRε (u0510 JC218)
12	135	*	Difference Output and Controls, DOUTε (JC218)
12	136 SHT 1	*	Signal Display Adapter (u0513)
12	136 SHT 2	*	Signal Display Adapter (u0513)
12	141	*	Isolating Diodes : u0518 XZ 926
12	142		Card u0518 XZ 926a-ER1 : Component Layout
12	143	*	Selection Code Switch, DOUTε (u0519 LT234a)
12	144	*	Readout ε (u0520 LT234a)
12	145	*	Test Panel Lamp Driver (u0521 LT234a)
12	146	*	Mode Input (u0522 UT230)
12	147	*	Code Switch Inputs (u0523 UT230)
12	148	*	Modes and SPU Inputs (u0524 XZ 926a-R1)
12	149		Card u0524 XZ 926a-ER1 : Component Layout
12	150	*	Sub-Rack Terminal Block
12	151	*	Interconnection Table : Terminal Block : 0561
12	152	*	Interconnection Table : Terminal Block : 0562
12	153	*	Interconnection Table : Terminal Block : 0563
12	154	*	Interconnection Table : Terminal Block : 0564
12	155	*	Interconnection Table : Terminal Block : 0565
12	162 SHT 1	*	Computing Range Control and Shift, CRC (u0607 JC246a)
12	162 SHT 2	*	Computing Range Control and Shift, CRC (u0607 JC246a)
12	163 SHT 1	*	Adder and Control, ADD (u0608 JC245)
12	163 SHT 2	*	Adder and Control, ADD (u0608 JC245)
12	164	*	AC Register Buslines, BOUT, BIN (u0609 JC244a)
12	167	*	Secant Decoder, SEC (u0612 JC218a-E)
12	168 SHT 1	*	Signal Display Adapter (u0613)
12	168 SHT 2	*	Signal Display Adapter (u0613)
12	170	*	Measurement Error Detecting, MED (u0615 JC218a-E)
12	171	*	Processor Error Detecting, PED (u0616 JC218a-E)
12	173	*	Inverter, INV (u0618 JC218a-E)
12	174	*	Decimal Decoder for Weight Correction, DEC (u0619 JC242a-E)

* Indicates interconnection table.

GROUP 22

Figure No. (Group-Sheet)		Title
22 02		Actual Value Generation (Azimuth) : Circuit Diagram
22 03		MCP Readout Value Display (Azimuth) : Circuit Diagram
22 04		CCP Readout Value Display (Azimuth) : Circuit Diagram
22 08		* Error Voltage Input C, M, α, ε (u0701/02 XU402a-R2)
22 09		* Error Voltage Adjustment α (u0704 AP704)
22 10		* Discriminator α (u0705/06 UV005a)
22 11		* CMF Switch α (u0707/08 DV001a)
22 12		* Sin/Cos Voltages Output α (u0709 PT023a)
22 13		* Sin/Cos Generator α Fine Unit (u0710/11 ST007a)
22 14		* Sin/Cos Generator α Fine Unit (u0712/13 ST008a)
22 15		* Sin/Cos Generator α Medium Unit (u0714/15 ST005a)
22 16		* Sin/Cos Generator α Medium Unit (u0716/17 ST006a)
22 17		* Sin/Cos Generator α Coarse Unit (u0718/19 ST013a)
22 18		* Sin/Cos Generator α Coarse Unit (u0720/21 ST014a)
22 19		* Aiken→Decimal Decoder α (u0722 JC214a-E)
22 20	SHT 1	* Aiken→Decimal Decoder α (u0723 JC214a-E)
22 20	SHT 2	* Aiken→Decimal Decoder α (u0723 JC214a-E)
22 21		* Aiken→Decimal Decoder α (u0724 JC214a-E)
22 22		* Sub-Rack Terminal Block
22 23		* Interconnection Table : Terminal Block : 0761
22 24		* Interconnection Table : Terminal Block : 0762
22 25		* Interconnection Table : Terminal Block : 0764
22 26		* Interconnection Table : Terminal Block : 0765
22 27		* Measurement System Connector Plug b94
22 28		Aiken to Decimal Decoder JC214a-E : Circuit Diagram

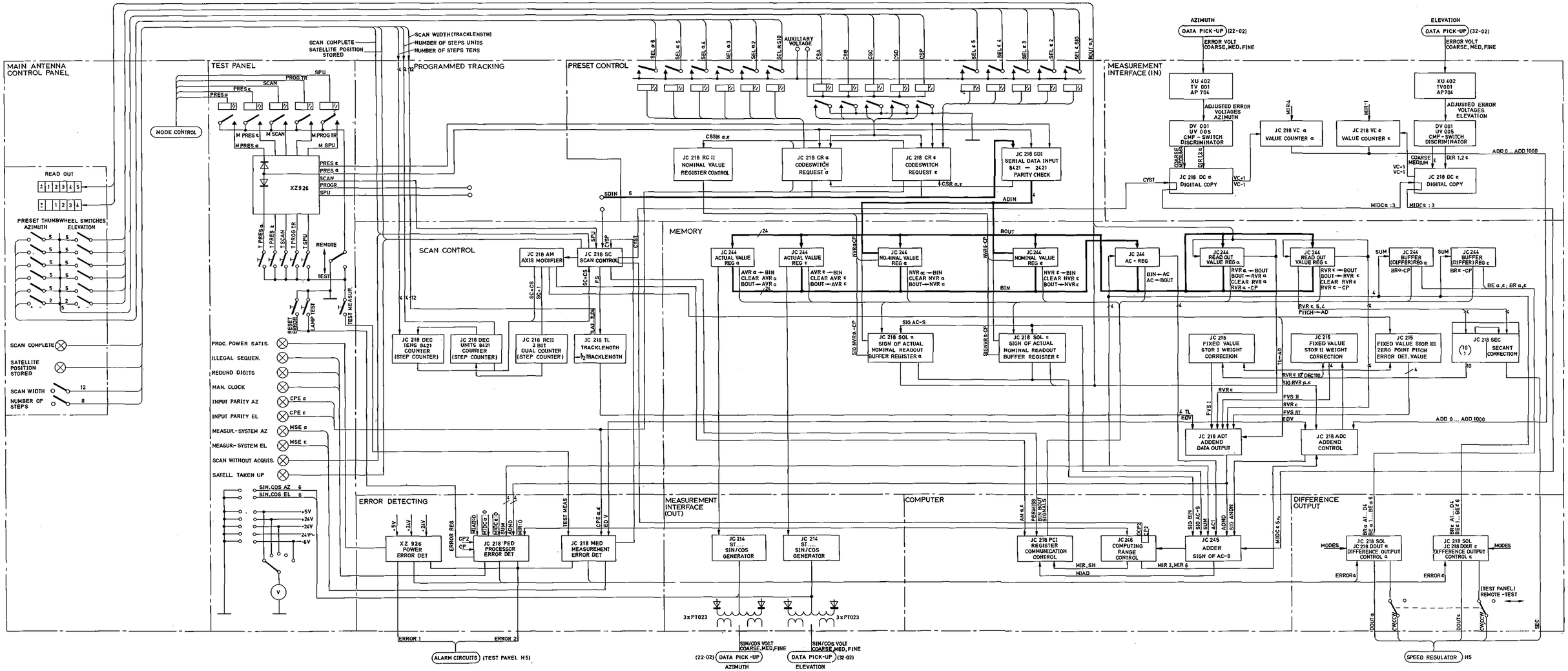
* Indicates interconnection table.

GROUP 32

32 02		Actual Value Generation (Elevation) : Circuit Diagram
32 03		MCP Readout Value Display (Elevation) : Circuit Diagram
32 04		CCP Readout Value Display (Elevation) : Circuit Diagram
32 08		* 10 kHz Oscillator (u0801/03 UV006a)
32 09		* Error Voltage Adjustment ε (u0804 AP704a)

Figure No. (Group-Sheet)		Title
32 10		* Discriminator ε (u0805/06 UV005a)
32 11		* CMF Switch ε (u0807/08 DV001a)
32 12		* Sin/Cos Voltages Output ε (u0809 PT023a)
32 13		* Sin/Cos Generator ε Fine Unit (u0810/11 ST007a)
32 14		* Sin/Cos Generator ε Fine Unit (u0812/13 ST008a)
32 15		* Sin/Cos Generator ε Medium Unit (u0814/15 ST005a)
32 16		* Sin/Cos Generator ε Medium Unit (u0816/17 ST006a)
32 17		* Sin/Cos Generator ε Coarse Unit (u0818/19 ST013a)
32 18		* Sin/Cos Generator ε Coarse Unit (u0820/21 ST014a)
32 19		* Aiken→Decimal Decoder ε (u0822 JC214a-E)
32 20	SHT 1	* Aiken→Decimal Decoder ε (u0823 JC214a-E)
32 20	SHT 2	* Aiken→Decimal Decoder ε (u0823 JC214a-E)
32 21		* Aiken→Decimal Decoder ε (u0824 JC214a-E)
32 22		* Sub-Rack Terminal Block
32 23		* Interconnection Table : Terminal Block : 0861
32 24		* Interconnection Table : Terminal Block : 0862
32 25		* Interconnection Table : Terminal Block : 0864
32 26		* Interconnection Table : Terminal Block : 0865
32 27		* Measurement System Connector Plug b90

* Indicates interconnection table.

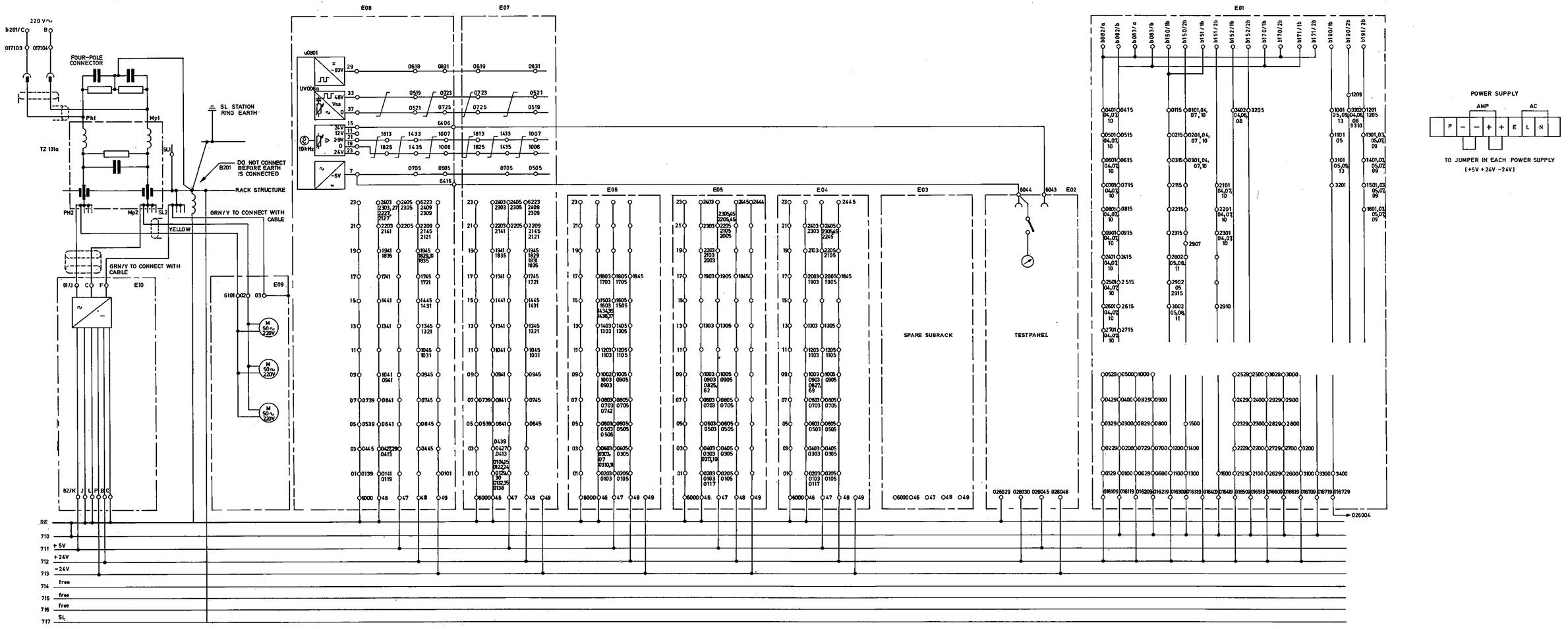


ANC 1720 : Block Diagram

Figure 02-04

Part 1

Iss. 1



Rack H6 Power Distribution: Circuit Diagram

Figure 02-05

Part 1

Iss. 1

b150		b250
a		b
SCREEN	13	SCREEN
DOUTα A2	12	DOUTα A3
DOUTα B2	11	DOUTα B3
DOUTα C2	10	DOUTα C3
DOUTα D2	9	DOUTα D3
DOUTα A4	8	
DOUTα B4	7	
DOUTα C4	6	
DOUTα D4	5	
CW	4	
CCW	3	
DOUTα A1	2	+5V
	1	GND

b151		b251
a		b
SCREEN	13	SCREEN
DOUTε A2	12	DOUTε A3
DOUTε B2	11	DOUTε B3
DOUTε C2	10	DOUTε C3
DOUTε D2	9	DOUTε D3
DOUTε A4	8	SEC20
DOUTε B4	7	SEC21
DOUTε C4	6	SEC22
DOUTε D4	5	SEC23
UP	4	SEC24
DOWN	3	SEC25
DOUTε A1	2	+5V
	1	GND

b152		b252
a		b
SCREEN	13	SCREEN
PRESα	12	ERROR1
PRESε	11	ERROR1′
SCAN	10	ERROR2
SPU	9	ERROR2′
SCIF	8	
	7	
	6	
	5	
	4	
	3	
	2	+24V
	1	GND

b102		b202
a		b
SCREEN	13	SCREEN
	12	
	11	
	10	
	9	
	8	
	7	
	6	
	5	
	4	
	3	
	2	
	1	

b170		b270
a		b
SCREEN	13	SCREEN
ROUTα A2	12	ROUTα A3
ROUTα B2	11	ROUTα B3
ROUTα C2	10	ROUTα C3
ROUTα D2	9	ROUTα D3
ROUTα A4	8	ROUTα A5
ROUTα B4	7	ROUTα B5
ROUTα C4	6	ROUTα C5
ROUTα D4	5	ROUTα D5
ROUTα A6	4	SIG RVRα+
ROUTα B6	3	SIG RVRα-
ROUTα C6	2	+18V
ROUTα D6	1	GND

b171		b271
a		b
SCREEN	13	SCREEN
ROUTε A2	12	ROUTε A3
ROUTε B2	11	ROUTε B3
ROUTε C2	10	ROUTε C3
ROUTε D2	9	ROUTε D3
ROUTε A4	8	ROUTε A5
ROUTε B4	7	ROUTε B5
ROUTε C4	6	ROUTε C5
ROUTε D4	5	ROUTε D5
	4	SIG RVRε+
	3	SIG RVRε-
	2	+18V
	1	GND

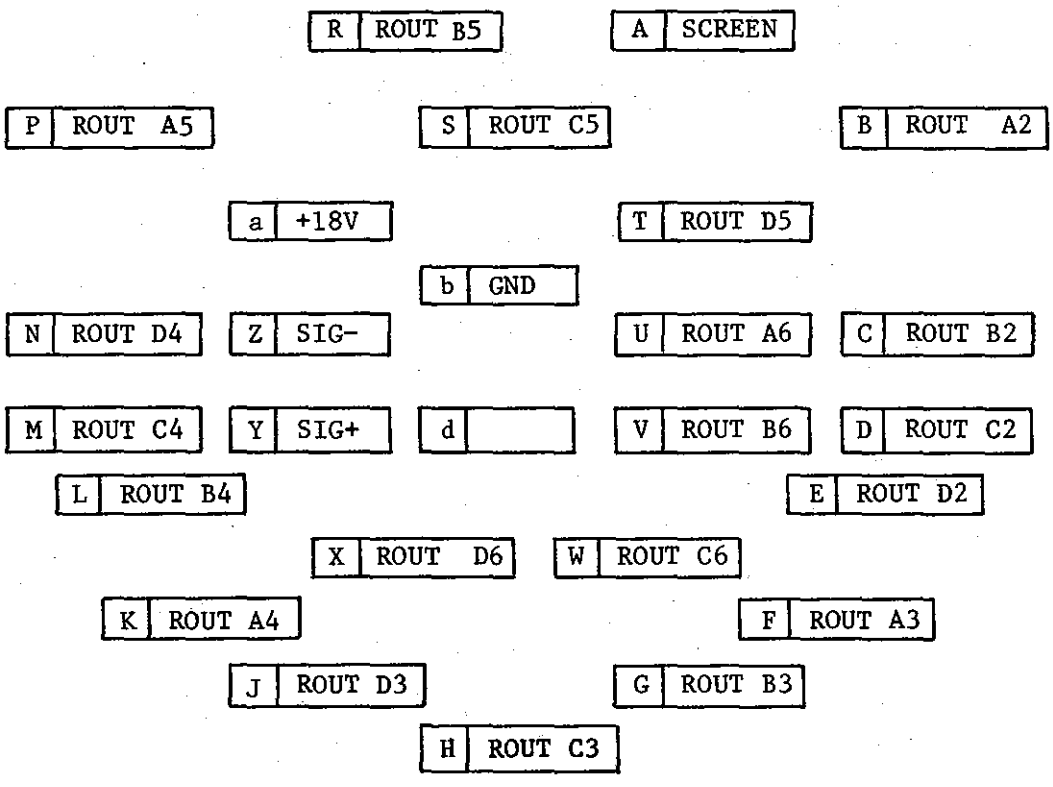
b172		b271
a		b
SCREEN	13	SCREEN
	12	
	11	
	10	
	9	
	8	
	7	
	6	
	5	
	4	
	3	
	2	
	1	

a		b
SCREEN	13	SCREEN
	12	
	11	
	10	
	9	
	8	
	7	
	6	
	5	
	4	
	3	
	2	
	1	

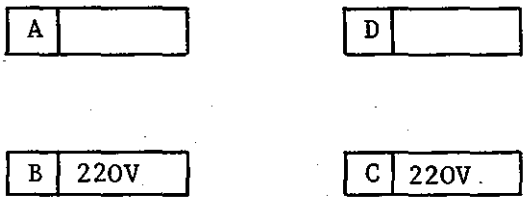
b190		b290
a		b
SCREEN	13	SCREEN
SELα 6	12	TEST
SELα 5	11	SELε 5
SELα 4	10	SELε 4
SELα 3	9	SELε 3
SELα 2	8	SELε 2
SELα SIG	7	SELε SIG
	6	
$\overline{CSA}$	5	
$\overline{CSB}$	4	
$\overline{CSC}$	3	
$\overline{CSD}$	2	+48V
$\overline{CSP}$	1	GND

b191		b192
a		b
SCREEN	13	SCREEN
NUA	12	NTA
NUB	11	NTB
NUC	10	NTC
NUD	9	NTD
TL2A	8	TL3A
TL2B	7	TL3B
TL2C	6	TL3C
TL2D	5	TL3D
TL4A	4	TPU/LOST
TL4B	3	SPWA
TL4C	2	+48V
TL4D	1	GND

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
TYPE	CONNECTORS ON THE UNSCREENED SIDE OF THE INPUT/OUTPUT SUB-RACK	GROUP 02	SHEET 07	
		PART 1		



b 082 Azimuth
b 083 Elevation
} to cabin panel



b 201 POWER

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
TYPE b201 POWER	CONNECTORS ON THE UNSCREENED SIDE OF THE INPUT/OUTPUT SUB-RACK	GROUP 02	SHEET 08	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			A	SCREEN				
01	04	02	B	ROUTα A2				
01	04	05	C	ROUTα B2				
01	04	08	D	ROUTα C2				
01	04	11	E	ROUTα D2				
01	05	02	F	ROUTα A3				
01	05	05	G	ROUTα B3				
01	05	08	H	ROUTα C3				
01	05	11	J	ROUTα D3				
01	06	02	K	ROUTα A4				
01	06	05	L	ROUTα B4				
01	06	08	M	ROUTα C4				
01	06	11	N	ROUTα D4				
01	07	02	P	ROUTα A5				
01	07	05	R	ROUTα B5				
01	07	08	S	ROUTα C5				
01	07	11	T	ROUTα D5				
01	08	02	U	ROUTα A6				
01	08	05	V	ROUTα B6				
01	08	08	W	ROUTα C6				
01	08	11	X	ROUTα D6				
01	09	02	Y	SIG RVRα				
01	09	05	Z	SIG RVRα				
			a	+12V				
			b	GND				
			d					

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b 082	
TYPE	PLUG b082	GROUP 02	SHEET 09	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			A	SCREEN				
01	24	02	B	ROUTε A2				
01	24	05	C	ROUTε B2				
01	24	08	D	ROUTε C2				
01	24	11	E	ROUTε D2				
01	25	02	F	ROUTε A3				
01	25	05	G	ROUTε B3				
01	25	08	H	ROUTε C3				
01	25	11	J	ROUTε D3				
01	26	02	K	ROUTε A4				
01	26	05	L	ROUTε B4				
01	26	08	M	ROUTε C4				
01	26	11	N	ROUTε D4				
01	27	02	P	ROUTε A5				
01	27	05	R	ROUTε B5				
01	27	08	S	ROUTε C5				
01	27	11	T	ROUTε D5				
			U					
			V					
			W					
			X					
01	09	08	Y	SIG RVRε				
01	09	11	Z	$\overline{\text{SIG RVR}}$ε				
			a	+12V				
			b	GND				
			d					

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b 083	
TYPE	PLUG b083	GROUP 02	SHEET 10	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
08	61	28	A	SCREENε				
08	61	08	B	COS VOLTFε				
08	61	18	C	COS VOLTFε				
			D					
08	61	05	E	SIN VOLTFε				
08	61	07	F	SIN VOLTMε				
08	61	09	G	SIN VOLTFε				
08	61	04	H	COS VOLTCε				
08	61	06	I	COS VOLTMε				
08	61	15	J	SIN VOLTCε				
08	61	17	K	SIN VOLTMε				
08	61	19	L	SIN VOLTFε				
08	61	14	M	COS VOLTCε				
08	61	16	N	COS VOLTMε				
			O					
			P					
			R					
			S					
			T					
			U					
			V					
08	61	25	W	SCREEN				
08	61	27	X	SCREEN				
08	61	29	Y	SCREEN				
08	61	24	Z	SCREEN				
08	61	26	a	SCREEN				
			b	ERVOLTMε		08	61	02
			c	ERVOLTCε		08	61	01
			d	SCREENε		08	61	21
01	62	23	e	SCREEN (+24V)				
			f	SCREEN		08	61	22
			g	ERVOLTMε		08	61	12
			h	ERVOLTCε		08	61	11
			i	ERVOLTFε		08	62	13
			k	ERVOLTFε		08	62	03

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b 090	
TYPE		GROUP	SHEET	
	PLUG b090	02	11	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
07	61	28	A	SCREENα				
07	61	08	B	COS VOLTFα				
07	61	18	C	COS VOLTFα				
			D					
07	61	05	E	SIN VOLTFα				
07	61	07	F	SIN VOLTMα				
07	61	09	G	SIN VOLTFα				
07	61	04	H	COS VOLTCα				
07	61	06	I	COS VOLTMα				
07	61	15	J	SIN VOLTCα				
07	61	17	K	SIN VOLTMα				
07	61	19	L	SIN VOLTFα				
07	61	14	M	COS VOLTCα				
07	61	16	N	COS VOLTMα				
			O					
			P					
			R					
			S					
			T					
			U					
			V					
07	61	25	W	SCREEN				
07	61	27	X	SCREEN				
07	61	29	Y	SCREEN				
07	61	24	Z	SCREEN				
07	61	26	a	SCREENα				
			b	ERVOLTMα		07	61	02
			c	ERVOLTCα		07	61	01
			d	SCREENα		07	61	21
07	62	23	e	SCREEN(+24V)				
			f	SCREEN		07	61	22
			g	ERVOLTMα		07	61	12
			h	ERVOLTCα		07	61	11
			i	ERVOLTFα		07	62	13
			K	ERVOLTFα		07	62	03

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b094	
TYPE		GROUP	SHEET	
	PLUG b094	02	12	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			1a					
01	29	08	2a	DOUTα A1				
01	30	04	3a	CCW				
01	30	01	4a	CW				
01	03	11	5a	DOUTα D4				
01	03	08	6a	DOUTα C4				
01	03	05	7a	DOUTα B4				
01	03	02	8a	DOUTα A4				
01	01	11	9a	DOUTα D2				
01	01	08	10a	DOUTα C2				
01	01	05	11a	DOUTα B2				
01	01	02	12a	DOUTα A2				
			13a	SCREEN				
			1b	GND				
			2b	+5V				
			3b					
			4b					
			5b					
			6b					
			7b					
			8b					
01	02	02	9b	DOUTα D3				
01	02	05	10b	DOUTα C3				
01	02	08	11b	DOUTα B3				
01	02	11	12b	DOUTα A3				
			13b	SCREEN				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b 150	
TYPE	PLUG b150	GROUP 02	SHEET 13	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			1a					
01	29	11	2a	DOUTε A1				
01	30	10	3a	DOWN				
01	30	07	4a	UP				
01	23	11	5a	DOUTε D4				
01	23	08	6a	DOUTε C4				
01	23	05	7a	DOUTε B4				
01	23	02	8a	DOUTε A4				
01	21	11	9a	DOUTε D2				
01	21	08	10a	DOUTε C2				
01	21	05	11a	DOUTε B2				
01	21	02	12a	DOUTε A2				
			13a	SCREEN				
			1b	GND				
			2b	+5V				
01	29	04	3b	SEC 25				
01	28	01	4b	SEC 24				
01	28	10	5b	SEC 23				
01	28	07	6b	SEC 22				
01	28	04	7b	SEC 21				
01	28	01	8b	SEC 20				
01	22	11	9b	DOUTε D3				
01	22	08	10b	DOUTε C3				
01	22	05	11b	DOUTε B3				
01	22	02	12b	DOUTε A3				
			13b	SCREEN				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b151	
TYPE	PLUG b151	GROUP 02	SHEET 14	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			1a					
			2a					
			3a					
			4a					
			5a					
			6a					
			7a					
01	32	07	8a	SCIF				
			9a	SPU		01	34	07
			10a	SCAN		01	34	05
			11a	PRESε		01	34	03
			12a	PRESα		01	34	01
			13a	SCREEN				
			1b	GND				
			2b	+24V				
			3b					
			4b					
			5b					
			6b					
			7b					
			8b					
01	11	15	9b	ERROR2				
01	11	13	10b	ERROR2				
01	11	11	11b	ERROR1				
01	11	09	12b	ERROR1				
			13b	SCREEN				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b 152	
TYPE	PLUG b152	GROUP 02	SHEET 15	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
01	08	11	1a	ROUTα D6				
01	08	08	2a	ROUTα C6				
01	08	05	3a	ROUTα B6				
01	08	02	4a	ROUTα A6				
01	06	11	5a	ROUTα D4				
01	06	08	6a	ROUTα C4				
01	06	05	7a	ROUTα B4				
01	06	02	8a	ROUTα A4				
01	04	11	9a	ROUTα D2				
01	04	08	10a	ROUTα C2				
01	04	05	11a	ROUTα B2				
01	04	02	12a	ROUTα A2				
			13a	SCREEN				
			1b	GND				
			2b	+12V				
01	09	05	3b	$\overline{\text{SIGRVR}\alpha}$				
01	09	02	4b	SIGRVRα				
01	07	11	5b	ROUTα D5				
01	07	08	6b	ROUTα C5				
01	07	05	7b	ROUTα B5				
01	07	02	8b	ROUTα A5				
01	05	11	9b	ROUTα D3				
01	05	08	10b	ROUTα C3				
01	05	05	11b	ROUTα B3				
01	05	02	12b	ROUTα A3				
			13b	SCREEN				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b 170	
TYPE		GROUP	SHEET	
	PLUG b170	02	16	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			1a					
			2a					
			3a					
			4a					
01	26	11	5a	ROUTε D4				
01	26	08	6a	ROUTε C4				
01	26	05	7a	ROUTε B4				
01	26	02	8a	ROUTε A4				
01	24	11	9a	ROUTε D2				
01	24	08	10a	ROUTε C2				
01	24	05	11a	ROUTε B2				
01	24	02	12a	ROUTε A2				
			13a	SCREEN				
			1b	GND				
			2b	+12V				
01	09	08	3b	$\overline{\text{SIG RVR}}$ε				
01	09	11	4b	SIG RVRε				
01	27	11	5b	ROUTε D5				
01	27	08	6b	ROUTε C5				
01	27	05	7b	ROUTε B5				
01	27	02	8b	ROUTε A5				
01	25	11	9b	ROUTε D3				
01	25	08	10b	ROUTε C3				
01	25	05	11b	ROUTε B3				
01	25	02	12b	ROUTε A3				
			13b	SCREEN				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b 171	
TYPE	PLUG b171	GROUP 02	SHEET 17	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			1a	$\overline{\text{CSP}}$		01	33	09
			2a	$\overline{\text{CSD}}$		01	33	07
			3a	$\overline{\text{CSC}}$		01	33	05
			4a	$\overline{\text{CSB}}$		01	33	03
			5a	$\overline{\text{CSA}}$		01	33	01
			6a					
01	11	07	7a	SELα SIG				
01	11	03	8a	SELα 2				
01	10	15	9a	SELα 3				
01	10	11	10a	SELα 4				
01	10	07	11a	SELα 5				
01	10	03	12a	SELα 6				
			13a	SCREEN				
			1b	GND				
			2b	+48V				
			3b					
			4b					
			5b					
			6b					
01	32	03	7b	SELε SIG				
01	31	15	8b	SELε 2				
01	31	11	9b	SELε 3				
01	31	07	10b	SELε 4				
01	31	03	11b	SELε 5				
			12b	TEST				
			13b	SCREEN				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b 190	
TYPE	PLUG b190	GROUP 02	SHEET 18	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			1a	TL4D			01	16	10
			2a	TL4C			01	16	08
			3a	TL4B			01	16	06
			4a	TL4A			01	16	04
			5a	TL2D			01	15	04
			6a	TL2C			01	15	02
			7a	TL2B			01	14	10
			8a	TL2A			01	14	08
			9a	NUD			01	13	08
			10a	NUC			01	13	06
			11a	NUB			01	13	04
			12a	NUA			01	13	02
			13a	SCREEN					
			1b	GND					
			2b	+48V					
01	12	07	3b	SPWA					
01	12	03	4b	TPU/LOST					
			5b	TL3D			01	16	02
			6b	TL3C			01	15	10
			7b	TL3B			01	15	08
			8b	TL3A			01	15	06
			9b	NTD			01	14	06
			10b	NTC			01	14	04
			11b	NTB			01	14	02
			12b	NTA			01	13	10
			13b	SCREEN					

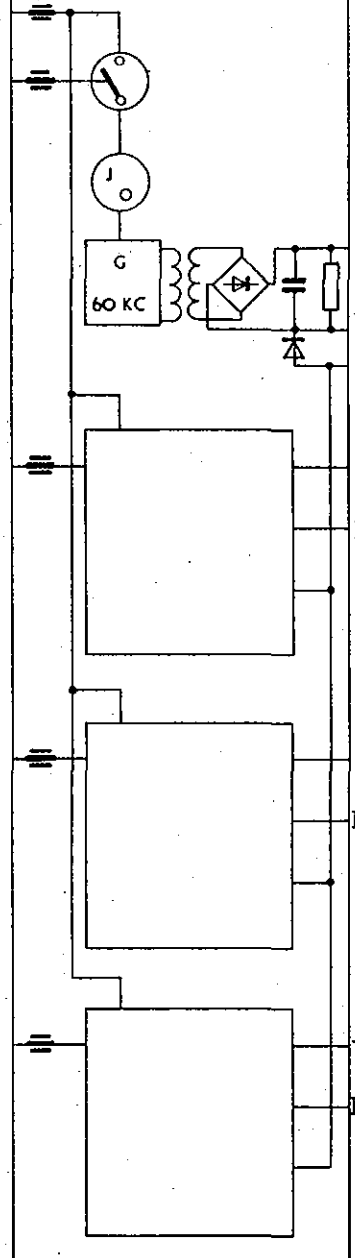
INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	b191	
TYPE	PLUG b191	GROUP 02	SHEET 19	
		PART 1		

Plug	Terminal	Designation
81/J	Live	220 V
81/L	Neutral	Mp
81/F	Earth	SL
82/K	+O/P	ASA 1000/5 : 5 V
82/J	-O/P	ASA 1000/5 V : RE
82/H	+AMP	free (ASA 1000/5)
82/G	-AMP	free (ASA 1000/5)
82/L	-O/P	ASC 50/24 RE
82/N	+AMP	ASC 50/24 free
82/M	-AMP	ASC 50/24 free
82/P	-O/P	ASC 50/24 -24 V
82/B	+O/P	ASC 500/24 +24 V
82/C	-O/P	ASC 500/24 RE
82/D	+AMP	ASC 500/24 free
82/E	-AMP	ASC 500/24 free
82/P	Earth	ASC 500/24 Earth

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
TYPE		GROUP 02	SHEET 20	
JB/LA 288 300	Connection Table for Power Supply E10			PART 1

Iss. 1

LOCATION			PIN	SIGNAL
01	61	09	29	+24V
01	61	00	21	DOUTα A2
01	61	01	23	DOUTα B2
01	61	02	25	DOUTα C2
01	61	03	27	DOUTα D2
01	61	19	00	GND

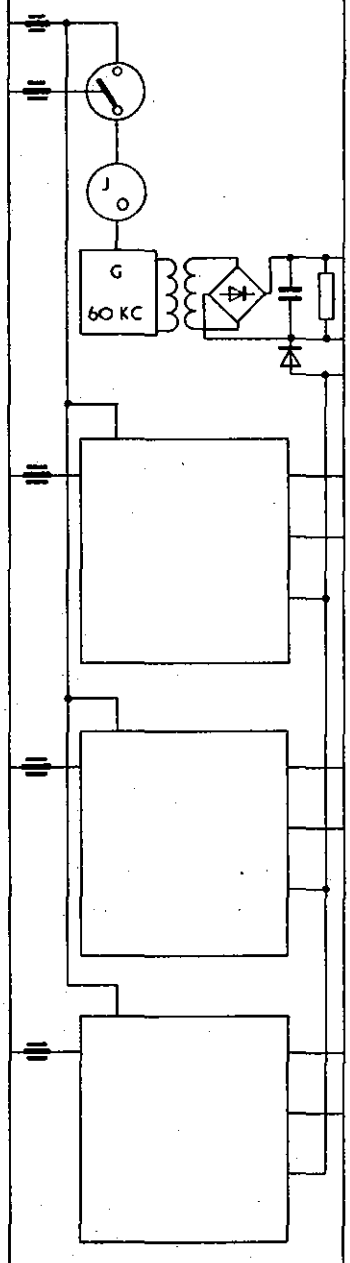


SIGNAL	PIN	LOCATION		
+5V	1	b150		2b
DOUTα A2	2	b150		12a
GND	15	b150		1b
+5V	4	b150		2b
DOUTα B2	5	b150		11a
+5V	7	b150		2b
DOUTα C2	8	b150		10a
+5V	10	b150		2b
DOUTα D2	11	b150		9a

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	01	
TYPE		GROUP	SHEET	
LU 230	CARD u0101	12	02	
		PART 1		

Iss. 1

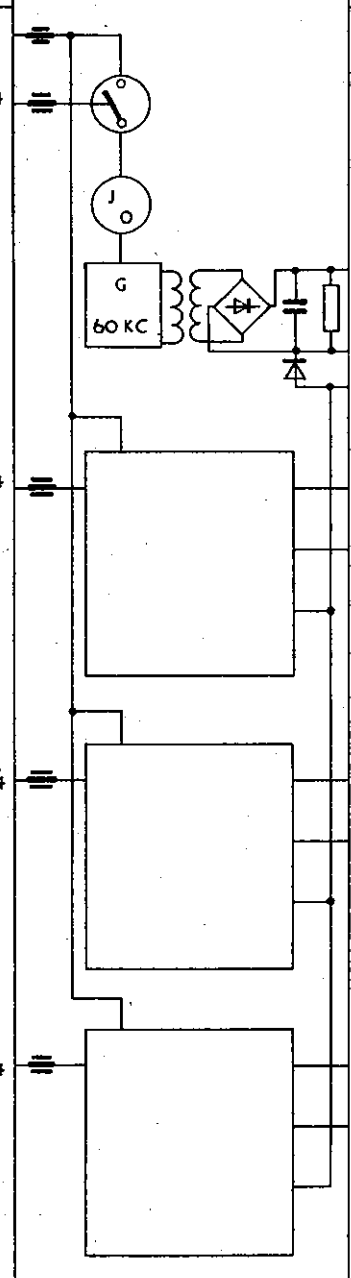
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	61	09	29	+24V					
01	61	10	21	DOUTα A3					
					+5V	1	b150		2b
					DOUTα A3	2	b150		12b
					GND	15	b150		1b
01	61	11	23	DOUTα B3	+5V	4	b150		2b
					DOUTα B3	5	b150		11b
01	61	12	25	DOUTα C3	+5V	7	b150		2b
					DOUTα C3	8	b150		1b
01	61	13	27	DOUTα D3	+5V	10	b150		2b
					DOUTα D3	11	b150		9b
01	61	19	00	GND					



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	02	
TYPE		GROUP	SHEET	
LU 230	CARD u0102	12	03	
		PART 1		

Iss. 1

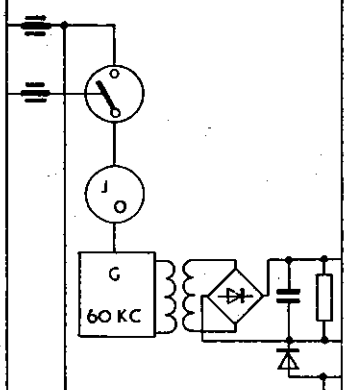
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	61	09	29	+24V					
01	61	20	21	DOUTα A4					
					+5V	1	b150		2b
					DOUTα A4	2	b150		8a
					GND	15	b150		1b
01	61	21	23	DOUTα B4	+5V	4	b150		2b
					DOUTα B4	5	b150		7a
01	61	22	25	DOUTα C4	+5V	7	b150		2b
					DOUTα C4	8	b150		6a
01	61	23	27	DOUTα D4	+5	10	b150		2b
					DOUTα D4	11	b150		5a
01	61	19	00	GND					



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	03	
TYPE		GROUP	SHEET	
LU 230	CARD u0103	12	04	
				PART 1

Iss. 1

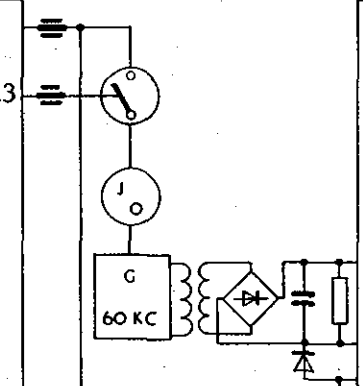
LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	61	09	29	+24V						
01	61	04	21	ROUTα A2						
						+12V	1	b170 b082		b2 a
						ROUTα A2	2	b170 b082		a17 B
						GND	15	b170 b082		b1 b
01	61	05	23	ROUTα B2		+12V	4	b170 b082		b2 a
						ROUTα B2	5	b170 b082		a10 C
01	61	06	25	ROUTα C2		+12V	7	b170 b082		b2 a
						ROUTα C2	8	b170 h082		a10 D
01	61	07	27	ROUTα D3		+12V	10	b170 b082		b2 a
						ROUTα D2	11	b170 b082		a9 E
01	61	19	00	GND						



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	04	
TYPE		GROUP	SHEET	
LU 230	CARD u0104	12	05	
				PART 1

Iss. 1

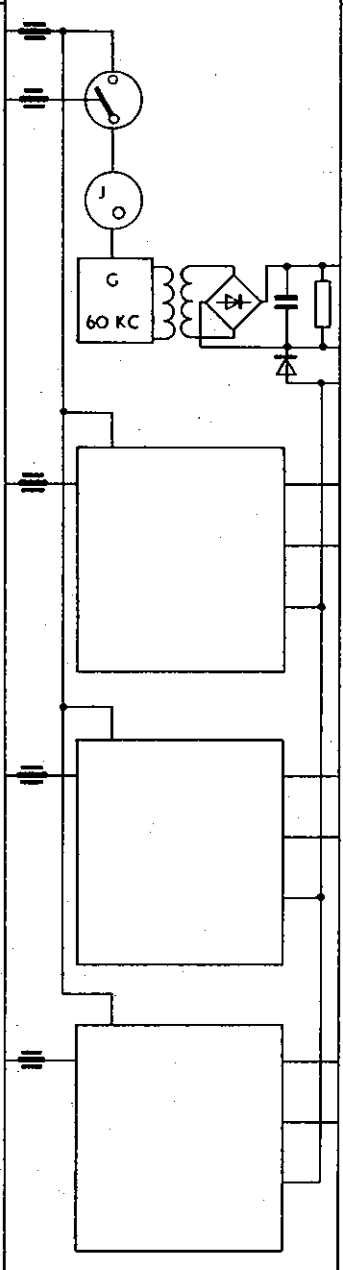
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	61	09	29	+24V					
01	61	14	21	ROUTα A3					
					+12V	1	b170 b082		2b a
					ROUTα A3	2	b170 b082		12b F
					GND	15	b170 b082		1b b
01	61	15	23	ROUTα B3	+12V	4	b170 b082		2b a
					ROUTα B3	5	b170 b082		11b G
01	61	16	25	ROUTα C3	+12V	7	b170 b082		2b a
					ROUTα C3	8	b170 b082		10b H
01	61	17	27	ROUTα D3	+12V	10	b170 b082		2b a
					ROUTα D3	11	b170 b082		9b J
01	61	19	00	GND					



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	05	
TYPE		GROUP	SHEET	
LU 230	CARD u0105	12	06	
		PART 1		

Iss. 1

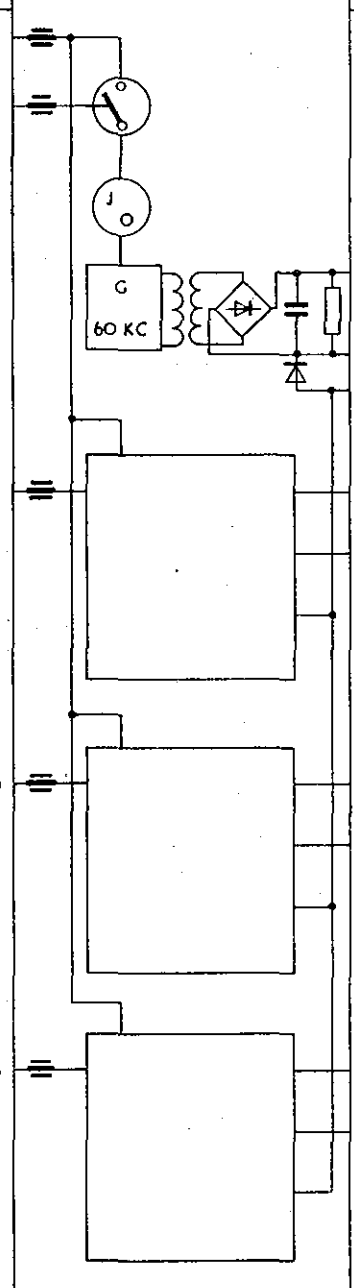
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	62	09	29	+24V					
01	62	00	21	ROUTα A4					
					+12V	1	b170 b082		2b a
					ROUTα A4	2	b170 b082		8a K
					GND	15	b170 b082		1b b
01	62	01	23	ROUTα B4	+12V	4	b170 b082		2b a
					ROUTα B4	5	b170 b082		7a L
01	62	02	25	ROUTα C4	+12V	7	b170 b082		2b a
					ROUTα C4	8	b170 b082		ba M
01	62	03	27	ROUTα D4	+12V	10	b170 b082		2b a
					ROUTα D4	11	b170 b082		5a N
01	62	19	00	GND					



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	06	
TYPE		GROUP	SHEET	
LU 230	CARD u0106	12	07	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL
01	62	09	29	+24V
01	62	10	21	ROUTα A5
01	62	11	23	ROUTα B5
01	62	12	25	ROUTα C5
01	62	13	27	ROUTα D5
01	62	19	00	GND

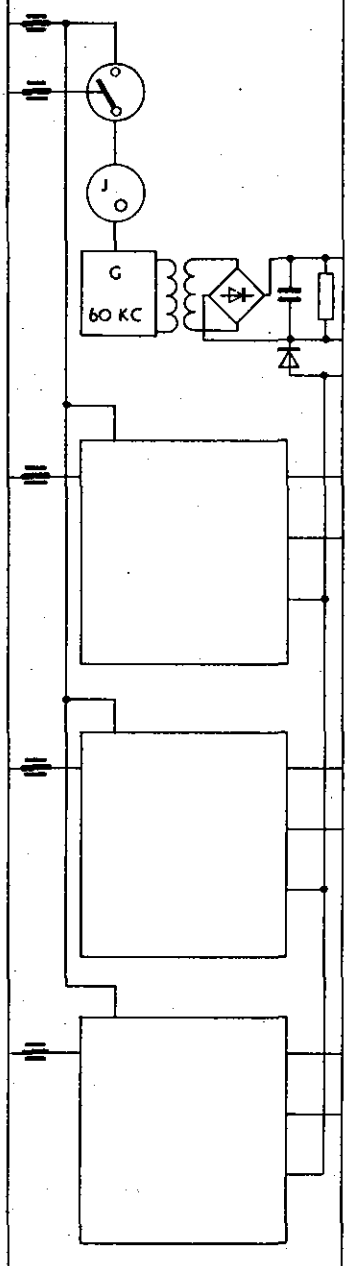


SIGNAL	PIN	LOCATION		
12V	1	b170		2b
		b082		a
ROUTα A5	2	b170		8b
		b082		P
GND	15	b170		1b
		b082		b
+12V	4	b170		2b
		b082		a
ROUTα B5	5	b170		7b
		b082		R
+12V	7	b170		2b
		b082		a
ROUTα C5	8	b170		6b
		b082		S
+12V	10	b170		2b
		b082		a
ROUTα D5	11	b170		5b
		b082		T

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	07	
TYPE		GROUP	SHEET	
LU 230	CARD u0107	12	08	
		PART 1		

Iss. 1

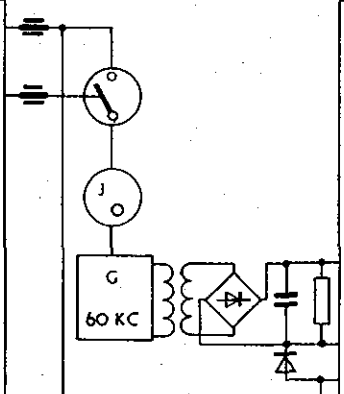
LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	62	09	29							
01	62	20	21	ROUTα A6						
						+12V	1	b170 b082		2b a
						ROUTα A6	2	b170 b082		4a U
						GND	15	b170 b082		1b b
01	62	21	23	ROUTα B6		+12V	4	b170 b082		2b a
						ROUTα A6	5	b170 b082		3a V
01	62	22	25	ROUTα C6		+12V	7	b170 b082		2b a
						ROUTα A6	8	b170 b082		2a W
01	62	23	27	ROUTα D6		+12V	10	b170 b082		2b a
						ROUTα A6	11	b170 b082		1a X
01	62	19	00	GND						



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	08	
TYPE		GROUP	SHEET	
LU 230	CARD u0108	12	09	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	62	09	29	+24V					
01	62	04	21	SIG RVRα					
					+12V	1	b170 b082		2b a
					SIG RVRα	2	b170 b082		4b Y
					GND	15	b170 b082		1b b
01	62	09	23	$\overline{\text{SIG RVRα}}$	+12V	4	b170 b082		2b a
					$\overline{\text{SIG RVRα}}$	5	b170 b082		3b Z
01	62	06	25	SIG RVRε	+12V	7	b171 b83		2b a
					SIG RVRε	8	b171 b83		4b Y
01	62	07	27	$\overline{\text{SIG RVRε}}$	+12V	10	b171 b83		2b a
					$\overline{\text{SIG RVRε}}$	11	b171 b83		3b Z
01	62	19	00	GND					



INTERCONNECTION TABLE	RACK 01	LOC'N 09	NOTES
TYPE	GROUP 12	SHEET 10	
LU 230 CARD u0109	PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	62	14	21	SELα 6		GND	1	b190		1b
						SELα 6	3	b190		12a
01	62	15	23	SELα 5		GND	5	b190		1b
						SELα 15	7	b190		11a
01	62	16	25	SELα 4		GND	9	b190		1b
						SELα 4	11	b190		10a
01	62	17	27	SELα 3		GND	13	b190		1b
01	62	09	00	+24V		SELα 3	15	b190		9a

INTERCONNECTION TABLE		RACK 01	LOC'N 10	NOTES
TYPE		GROUP 12	SHEET 11	
LR 240	CARD u0110	PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	63	00	21	SELα 2		GND	1	b190		1b
						SELα 2	3	b190		8a
01	63	01	23	SELα SIG		GND	5	b190		1b
						SELα SIG	7	b190		7a
01	63	02	25	ERROR1		ERROR1	9	b152		12b
						ERROR1'	11	b152		11b
01	63	03	27	ERROR2		ERROR2	13	b152		10b
01	63	09	00	+24V		ERROR2'	15	b152		9b

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	11	
TYPE		GROUP	SHEET	
LR 240	CARD u0111	12	12	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	63	10	21	TPU/LOST		+48V	1	b191		2b
						TPU/LOST	3	b191		4b
01	63	11	23	SPWA		+48V	5	b191		2b
						SPWA	7	b191		3b
01	63	12	25	TEST		+48V	9	b190		2b
						TEST	11	b190		12b
			27				13			
01	63	09	00	+24V			15			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	12	
TYPE		GROUP	SHEET	
LR 240	CARD u0112	12	13	
		PART 1		

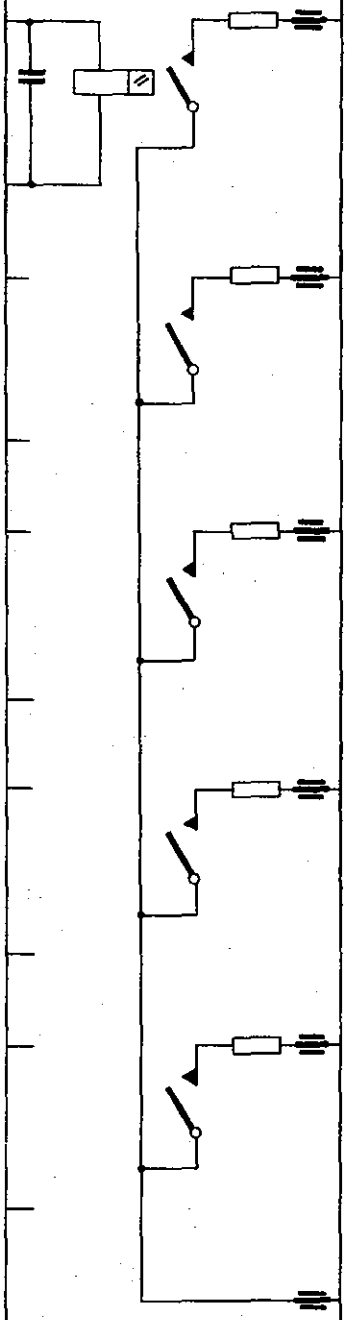
Iss. 1

LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION			
b	191	2b	1	+48V		NUA	21	01	63	04
b	191	12a	2	NUA						
b	191	2b	3	+48V		NUB	23	01	63	05
b	191	11a	4	NUB						
b	191	2b	5	+48V		NUC	25	01	63	06
b	191	10a	6	NUC						
b	191	2b	7	+48V		NUD	27	01	63	07
b	191	9a	8	NUD						
b	191	2b	9	+48V		NTA	29	01	63	08
b	191	12b	10	NTA						
						GND	00	01	63	19

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	13	
TYPE		GROUP	SHEET	
UR 240	CARD u0113	12	14	
		PART 1		

Iss. 1

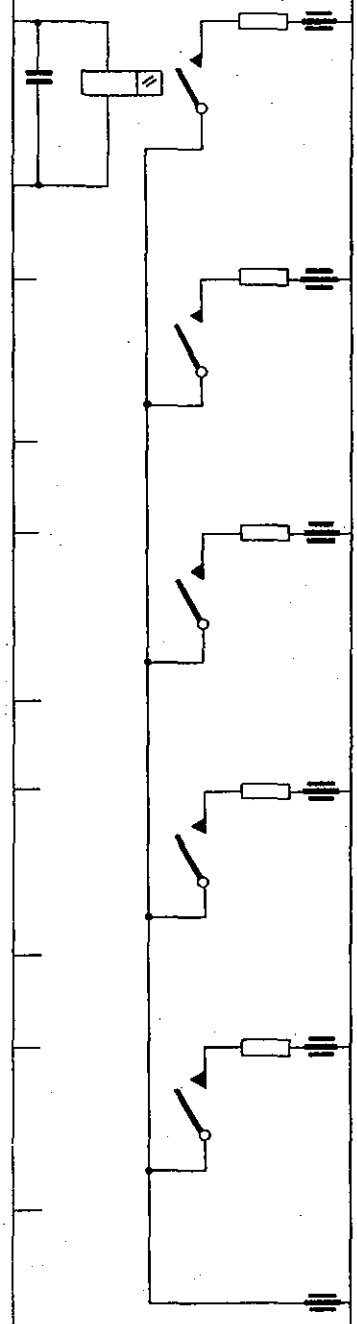
LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
b	191	2b	1	+48V		NTB	21	01	63	14
b	191	11b	2	NTB						
b	191	2b	3	+48V		NTC	23	01	63	15
b	191	10b	4	NTC						
b	191	2b	5	+48V		NTD	25	01	63	16
b	191	1b	6	NTD						
b	191	2b	7	+48V		TL 2A	27	01	63	17
b	191	8a	8	TL 2A						
b	191	2b	9	+48V		TL 2B	29	01	63	18
b	191	7a	10	TL 2B						
						GND	00	01	63	19



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	14	
TYPE		GROUP	SHEET	
UR 240	CARD u0114	12	15	
		PART 1		

Iss. 1

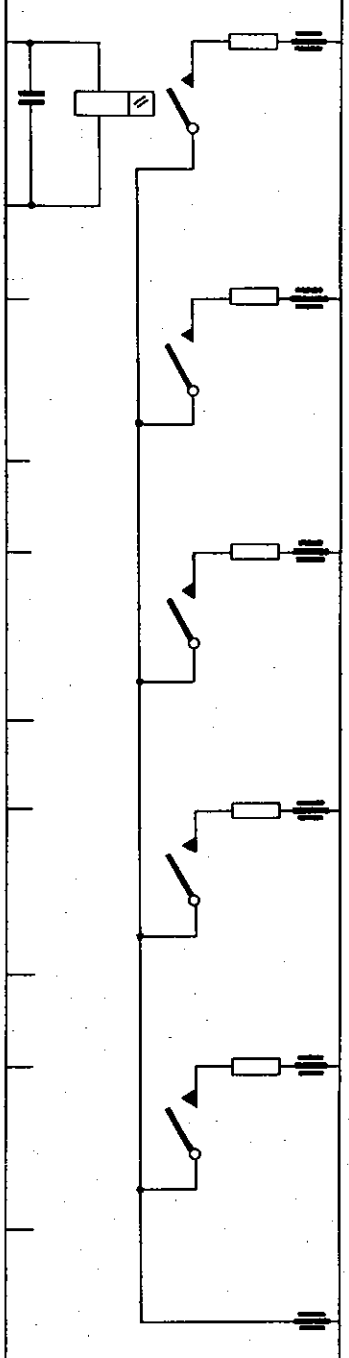
LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
b	191	2b	1	+48V		TL 2C	21	01	63	24
b	191	6a	2	TL 2C						
b	191	2b	3	+48V		TL 2D	23	01	63	25
b	191	5a	4	TL 2D						
b	191	2b	5	+48V		TL 3A	25	01	63	26
b	191	8b	6	TL 3A						
b	191	2b	7	+48V		TL 3B	27	01	63	27
b	191	7b	8	TL 3B						
b	191	2b	9	+48V		TL 3C	29	01	63	28
b	191	6b	10	TL 3C						
						GND	00	01	63	19



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	15	
TYPE		GROUP	SHEET	
UR 240	CARD u0115	12	16	
		PART 1		

Iss. 1

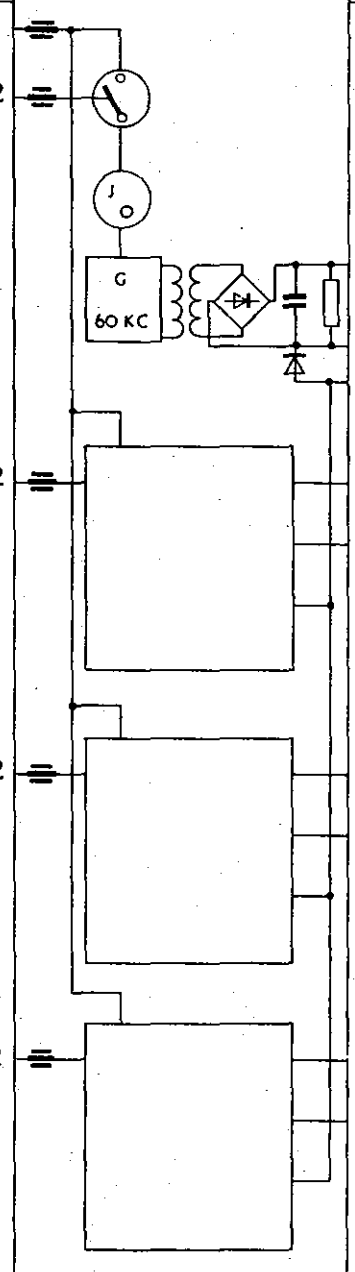
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
b	191	2b	1	+48V	TL 3D	21	01	64	00
b	191	5b	2	TL 3D					
b	191	2b	3	+48V	TL 4A	23	01	64	01
b	191	4a	4	TL 4A					
b	191	2b	5	+48V	TL 4B	25	01	64	02
b	191	3a	6	TL 4B					
b	191	2b	7	+48V	TL 4C	27	01	64	03
b	191	2a	8	TL 4C					
b	191	2b	9	+48V	TL 4D	29	01	64	04
b	191	1a	10	TL 4D					
					GND	00	01	64	19



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	16	
TYPE		GROUP	SHEET	
UR 240	CARD u0116	12	17	
		PART 1		

Iss. 1

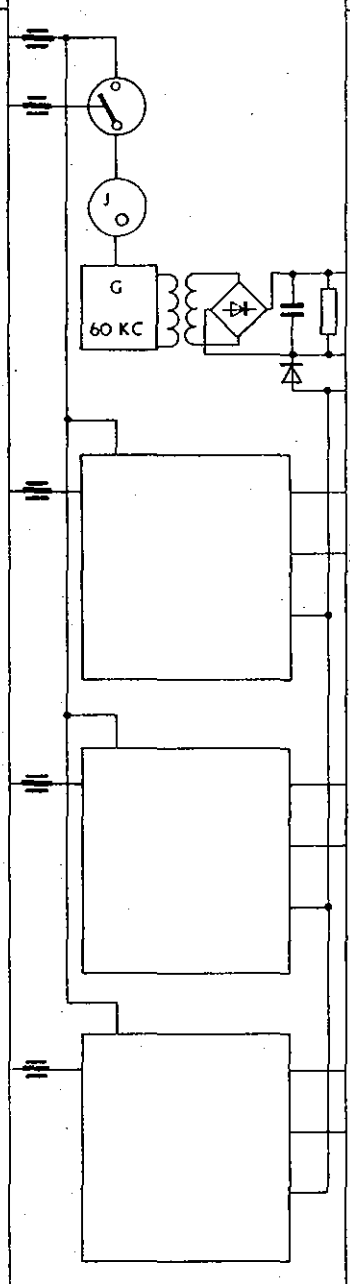
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	65	09	29	+24V					
01	65	00	21	DOUTε A2					
					+5V	1	b151		2b
					DOUTε A2	2	b151		
					GND	15	b151		1b
01	65	01	23	DOUTε B2					
					+5V	4	b151		2b
					DOUTε B2	5	b151		
01	65	02	25	DOUTε C2					
					+5V	7	b151		2b
					DOUTε C2	8	b151		
01	65	03	27	DOUTε D2					
					+5V	10	b151		2b
					DOUTε D2	11	b151		
01	65	19	00	GND					



INTERCONNECTION TABLE		RACK 01	LOC'N 21	NOTES
TYPE LU 230	CARD u0121	GROUP 12	SHEET 18	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL
01	65	09	29	+24V
01	65	10	21	DOUTε A3
01	65	11	23	DOUTε B3
01	65	12	25	DOUTε C3
01	65	13	27	DOUTε D3
01	65	19	00	GND

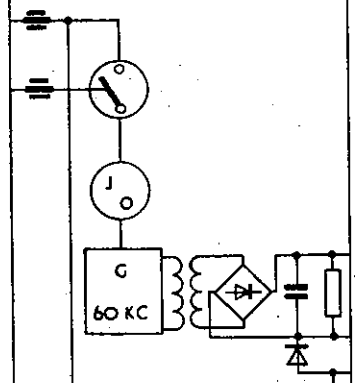


SIGNAL	PIN	LOCATION		
+5V	1	b151		2b
DOUTε A3	2	b151		12b
GND	15	b151		1b
+5V	4	b151		2b
DOUTε B3	5	b151		11b
+5V	7	b151		2b
DOUTε C3	8	b151		10b
+5V	10	b151		2b
DOUTε D3	11	b151		9b

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	22	
TYPE		GROUP	SHEET	
LU 230	CARD u0122	12	19	
		PART 1		

Iss. 1

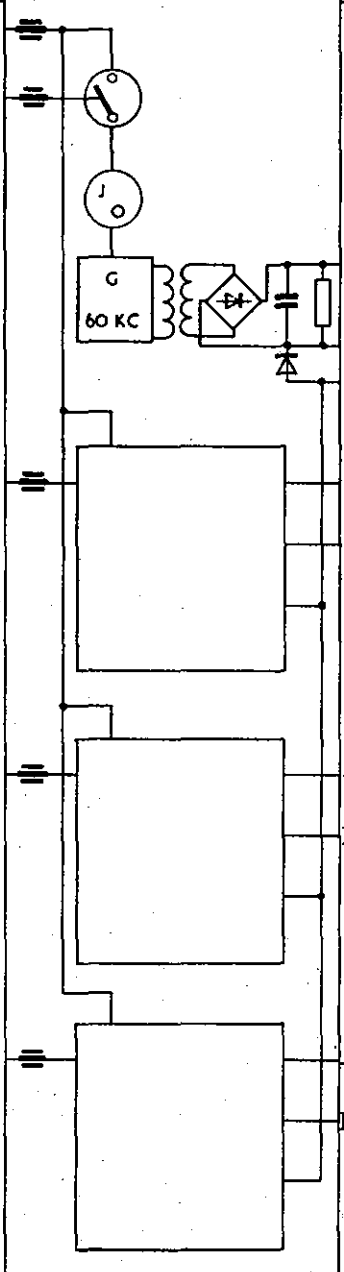
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	65	09	29	+24V					
01	65	20	21	DOUTε A4					
					+5V	1	b151		2b
					DOUTε A4	2	b151		8a
					GND	15	b151		1b
01	65	21	23	DOUTε B4					
					+5V	4	b151		2b
					DOUTε B4	5	b151		7a
01	65	22	25	DOUTε C4					
					+5V	7	b151		2b
					DOUTε C4	8	b151		6a
01	65	23	27	DOUTε D4					
					+5V	10	b151		2b
					DOUTε D4	11	b151		5a
01	65	19	00	GND					



INTERCONNECTION TABLE		RACK 01	LOC'N 23	NOTES
TYPE LU 230	CARD u0123	GROUP 12	SHEET 20	
		PART 1		

Iss. 1

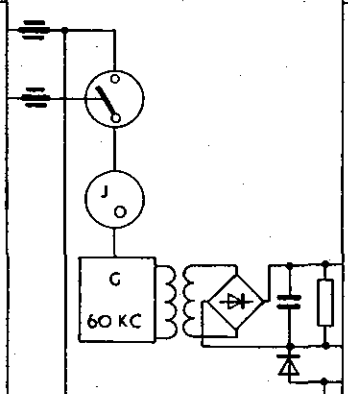
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	65	09	29	+24V					
01	65	04	21	ROUTε A2					
					+12V	1	b171 b83		2b a
					ROUTε A2	2	b171 b83		12a B
					GND	15	b171 b83		1b b
01	65	05	23	ROUTε B2					
					+12V	4	b171 b83		2b a
					ROUTε B2	5	b171 b83		11a C
01	65	06	25	ROUTε C2					
					+12V	7	b171 b83		2b a
					ROUTε C2	8	b171 b83		10a D
01	65	07	27	ROUTε D2					
					+12V	10	b171 b83		2b a
					ROUTε D2	11	b171 b83		9a E
01	65	19	00	GND					



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	24	
TYPE		GROUP	SHEET	
LU 230	CARD u0124	12	21	
		PART 1		

Iss. 1

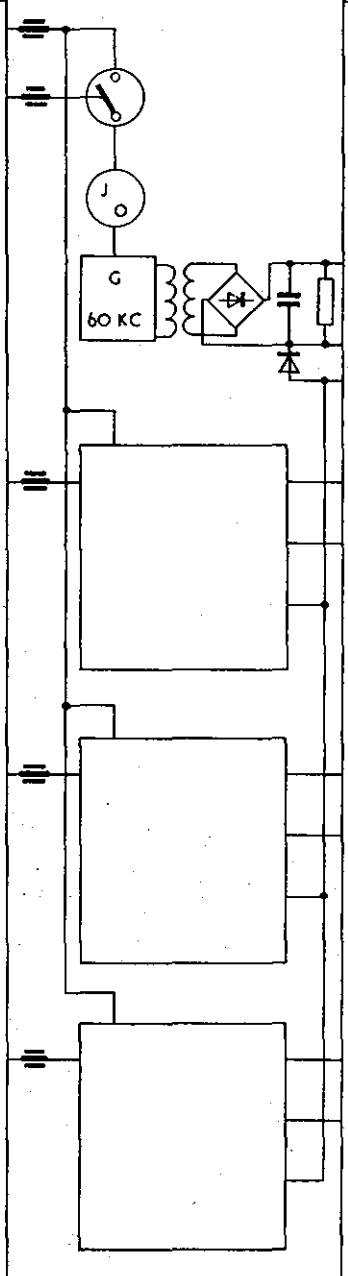
LOCATION			PIN	SIGNAL	SIGNAL	PIN	LOCATION		
01	65	09	29	+24V					
01	65	14	21	ROUTε A3					
					+12V	1	b171 b83		2b a
					ROUTε A3	2	b171 b83		12b F
					GND	15	b171 b83		1b b
01	65	15	23	ROUTε B3	+12V	4	b171 b83		2b a
					ROUTε B3	5	b171 b83		11b G
01	65	16	25	ROUTε C3	+12V	7	b171 b83		2b a
					ROUTε C3	8	b171 b83		10b H
01	65	17	27	ROUTε D3	+12V	10	b171 b83		2b a
					ROUTε D3	11	b171 b83		9b J
01	65	19	00	GND					



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	25	
TYPE		GROUP	SHEET	
LU 230	CARD u0125	12	22	
		PART 1		

Iss. 1

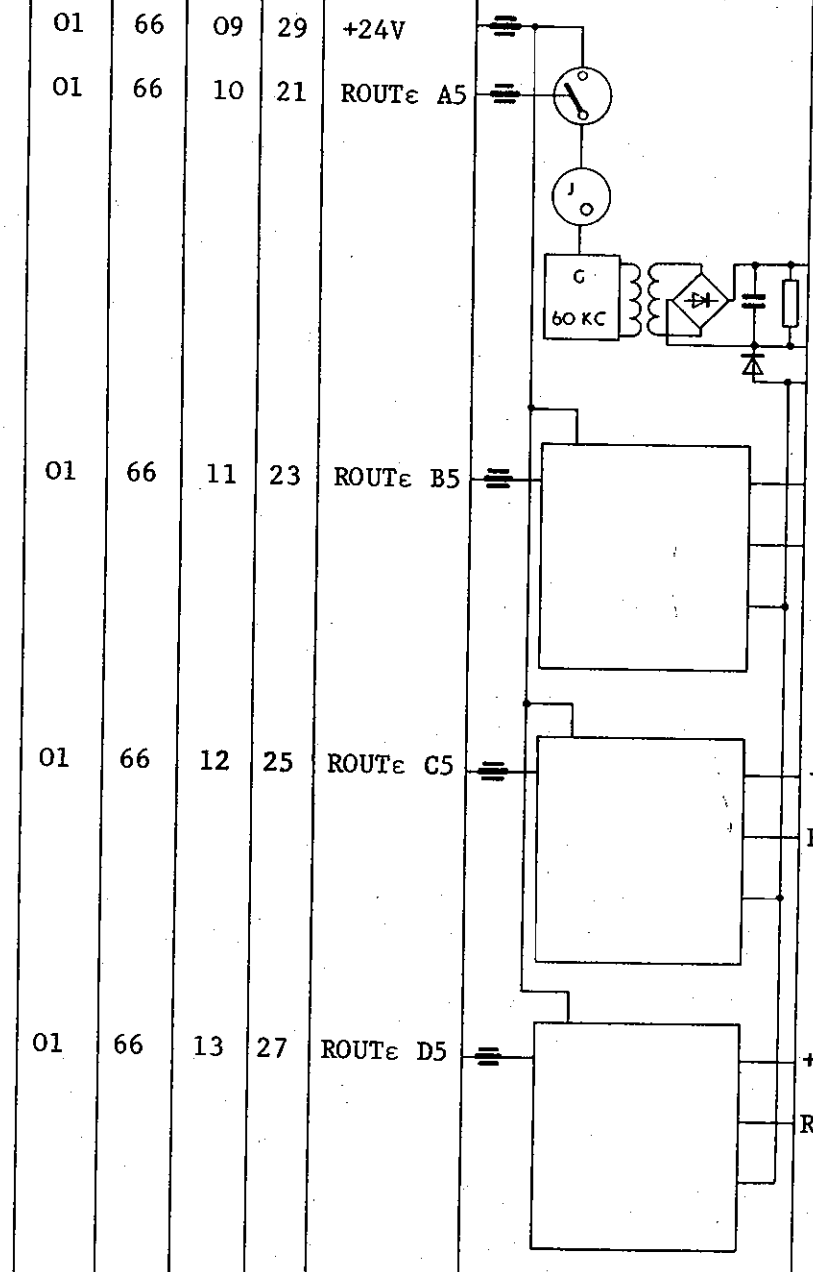
LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	66	09	29	+24V						
01	66	00	21	ROUTε A4						
						+12V	1	b171 b83		2b a
						ROUTε A4	2	b171 b83		8a K
						GND	15	b171 b83		1b b
01	66	01	23	ROUTε B4		+12V	4	b171 b83		2b a
						ROUTε B4	5	b171 b83		7a L
01	66	02	25	ROUTε C4		+12V	7	b171 b83		2b a
						ROUTε C4	8	b171 b83		6a M
01	66	03	27	ROUTε D4		+12V	10	b171 b83		2b a
						ROUTε D4	11	b171 b83		5a N
01	66	19	00	GND						



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	26	
TYPE		GROUP	SHEET	
LU 230	CARD u0126	12	23	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL
01	66	09	29	+24V
01	66	10	21	ROUTε A5
01	66	11	23	ROUTε B5
01	66	12	25	ROUTε C5
01	66	13	27	ROUTε D5
01	66	19	00	GND

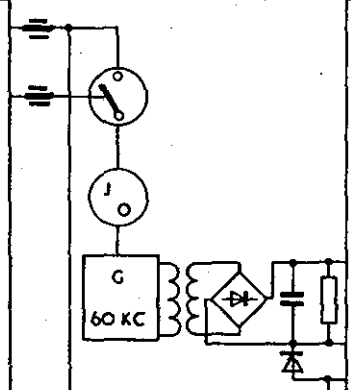


SIGNAL	PIN	LOCATION		
+12V	1	b171 b83	2b a	
ROUTε A5	2	b171 b83	8b P	
GND	15	b171 b83	1b b	
+12V	4	b171 b83	2b a	
ROUTε B5	5	b171 b83	7b R	
+12V	7	b171 b83	2b a	
ROUTε C5	8	b171 b83	6b S	
+12V	10	b171 b83	2b a	
ROUTε D5	11	b171 b83	5b T	

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	27	
TYPE		GROUP	SHEET	
LU 230	CARD u0127	12	24	
		PART 1		

Iss. 1

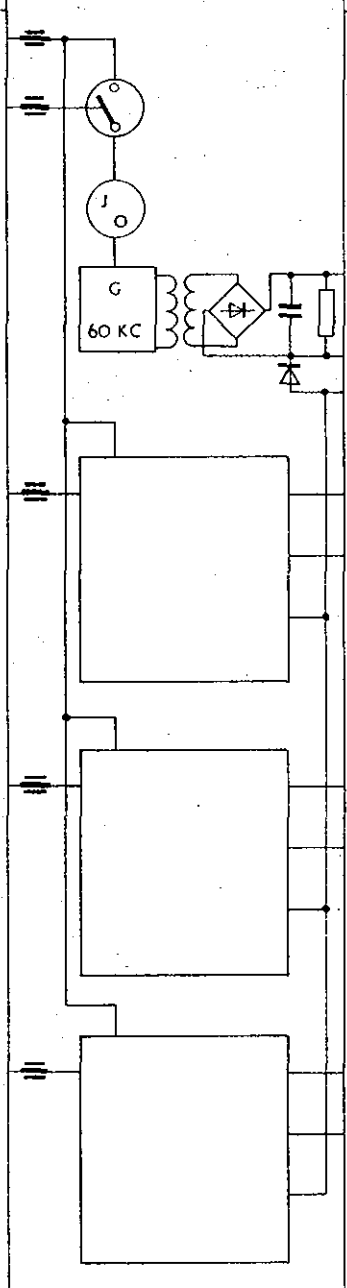
LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	66	09	29	+24V						
01	66	04	21	SEC 20						
						SEC 20	1	b151		8b
						GND	2	b151		1b
							15			
01	66	05	23	SEC 21		SEC 21	4	b151		7b
						GND	5	b151		1b
01	66	06	25	SEC 22		SEC 22	7	b151		6b
						GND	8	b151		1b
01	66	07	27	SEC 23		SEC 23	10	b151		5b
						GND	11	b151		1b
01	66	19	00	GND						



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	28	
TYPE		GROUP	SHEET	
LU 230	CARD u0128	12	25	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL
01	66	09	29	+24V
01	66	14	21	SEC 24
01	66	15	23	SEC 25
01	66	16	25	DOUTα A1
01	66	17	27	DOUTε A1
01	66	19	00	GND



SIGNAL	PIN	LOCATION		
SEC 24	1	b151		4b
GND	2	b151		1b
GND	15	b151		1b
SEC 25	4	b151		3b
GND	5	b151		1b
+5V	7	b150		2b
DOUTα A1	8	b150		2a
+5V	10	b151		2b
DOUTε A1	11	b151		2a

INTERCONNECTION TABLE

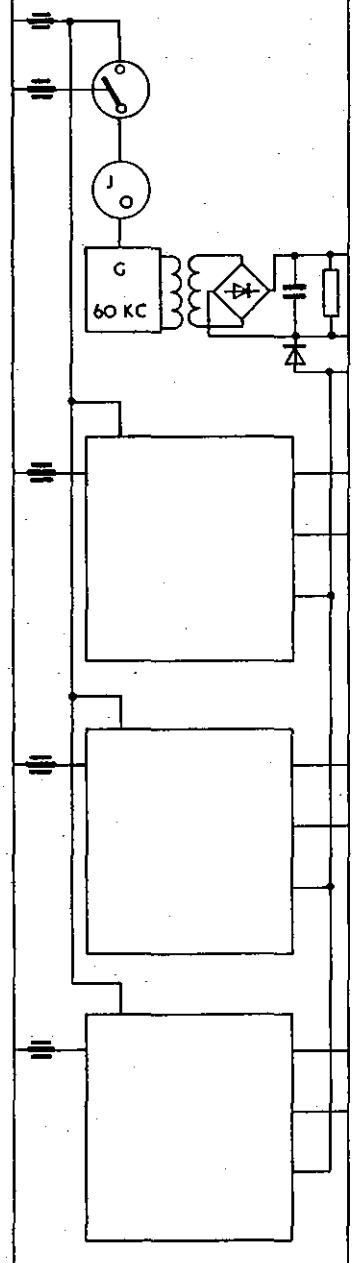
RACK	LOC'N	NOTES
01	29	
GROUP	SHEET	
12	26	

TYPE	
LU 230	CARD u0129

PART 1

Iss. 1

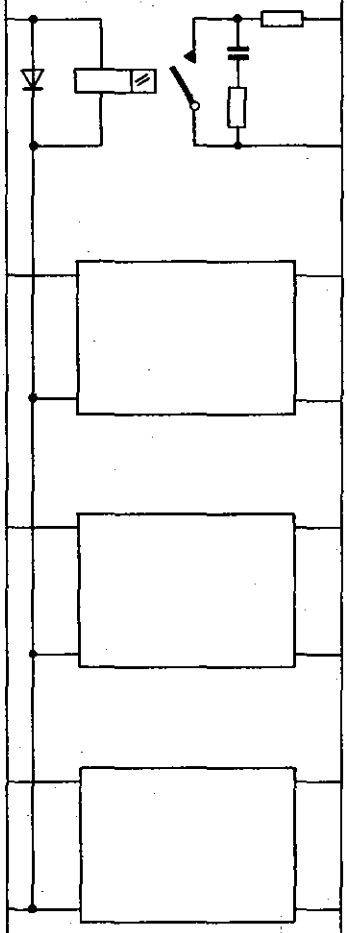
LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	66	09	29	+24V						
01	66	20	21	CW						
						CW	1	b150		4a
						GND	2	b150		1b
01	66	21	23	CCW		CCW	4	b150		3a
						GND	5	b150		1b
01	66	22	25	UP		UP	7	b151		4a
						GND	8	b151		1b
01	66	23	27	DOWN		DOWN	10	b151		3a
						GND	11	b151		1b
01	66	19	00	GND						



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	30	
TYPE		GROUP	SHEET	
LU 230	CARD u0130	12	27	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION			
01	67	00	21	SELε 2		GND	1	b190		1b
						SELε 2	3	b190		8b
01	67	01	23	SELε 3		GND	5	b190		1b
						SELε 3	7	b190		9b
01	67	02	25	SELε 4		GND	9	b190		1b
						SELε 4	11	b190		10b
01	67	03	27	SELε 5		GND	13	b190		1b
01	67	09	00	+24V		SELε 5	15	b190		11b



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	31	
TYPE		GROUP	SHEET	
LR 240	CARD .u0131	12	28	
		PART 1		

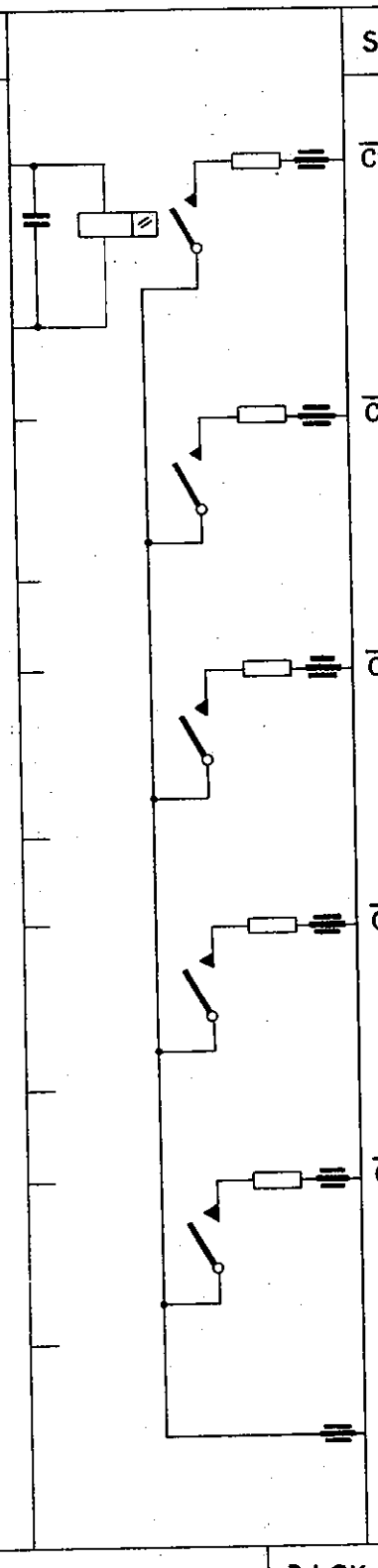
Iss. 1

LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
01	67	04	21	SELεSIG		GND	1	b190		1b
						SELεSIG	3	b190		7b
01	67	05	23	SCIF		+24V	5	b152		2b
						SCIF	7	b152		8a
			25				9			
							11			
			27				13			
01	67	09	00				15			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	32	
TYPE		GROUP	SHEET	
LR 240	CARD u0132	12	29	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION			
b	190	5a	1	$\overline{CSA}$		$\overline{CSA}$	21	01	67	20
b	190	2b	2	+48V							
b	190	4a	3	$\overline{CSB}$		$\overline{CSB}$	23	01	67	21
b	190	2b	4	+48V							
b	190	3a	5	$\overline{CSC}$		$\overline{CSC}$	25	01	67	22
b	190	2b	6	+48V							
b	190	2a	7	$\overline{CSD}$		$\overline{CSD}$	27	01	67	23
b	190	2b	8	+48V							
b	190	1a	9	$\overline{CSP}$		$\overline{CSP}$	29	01	67	24
b	190	2b	10	+48V							
						GND	00	01	67	19	



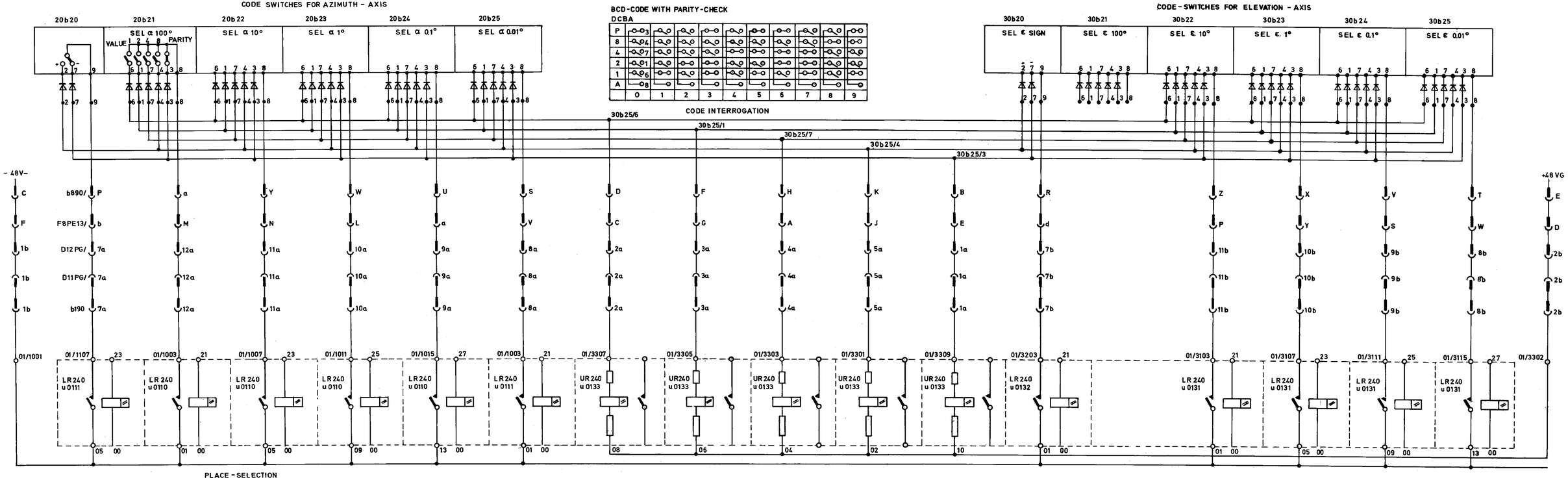
INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	33	
TYPE		GROUP	SHEET	
UR 240	CARD u0133	12	30	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		SIGNAL	PIN	LOCATION		
b	152	12a	1	PRESα		PRESα	21	01	67	10
b	152	1b	2	GND						
b	152	11a	3	PRESε		PRESε	23	01	67	11
b	152	1b	4	GND						
b	152	10a	5	SCAN		SCAN	25	01	67	12
b	152	1b	6	GND						
b	152	9a	7	SPU		SPU	27	01	67	13
			8	GND						
			10			REM GND	00	01	67	29

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	34	
TYPE		GROUP	SHEET	
UR 240	CARD u0134	12	31	
		PART 1		

Iss. 1

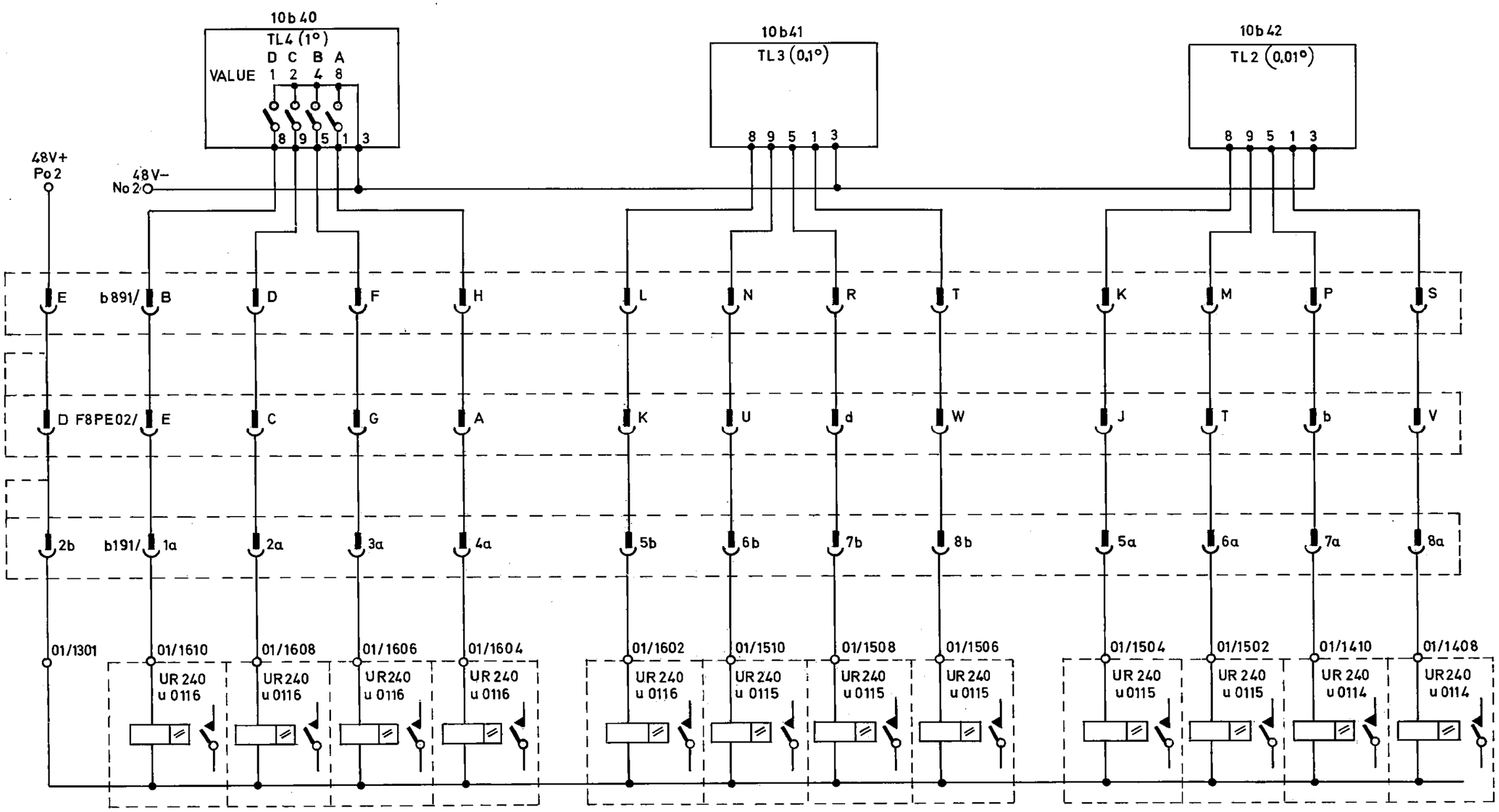


Selection of Code Switches for Preset

Figure 12-33

Part 1

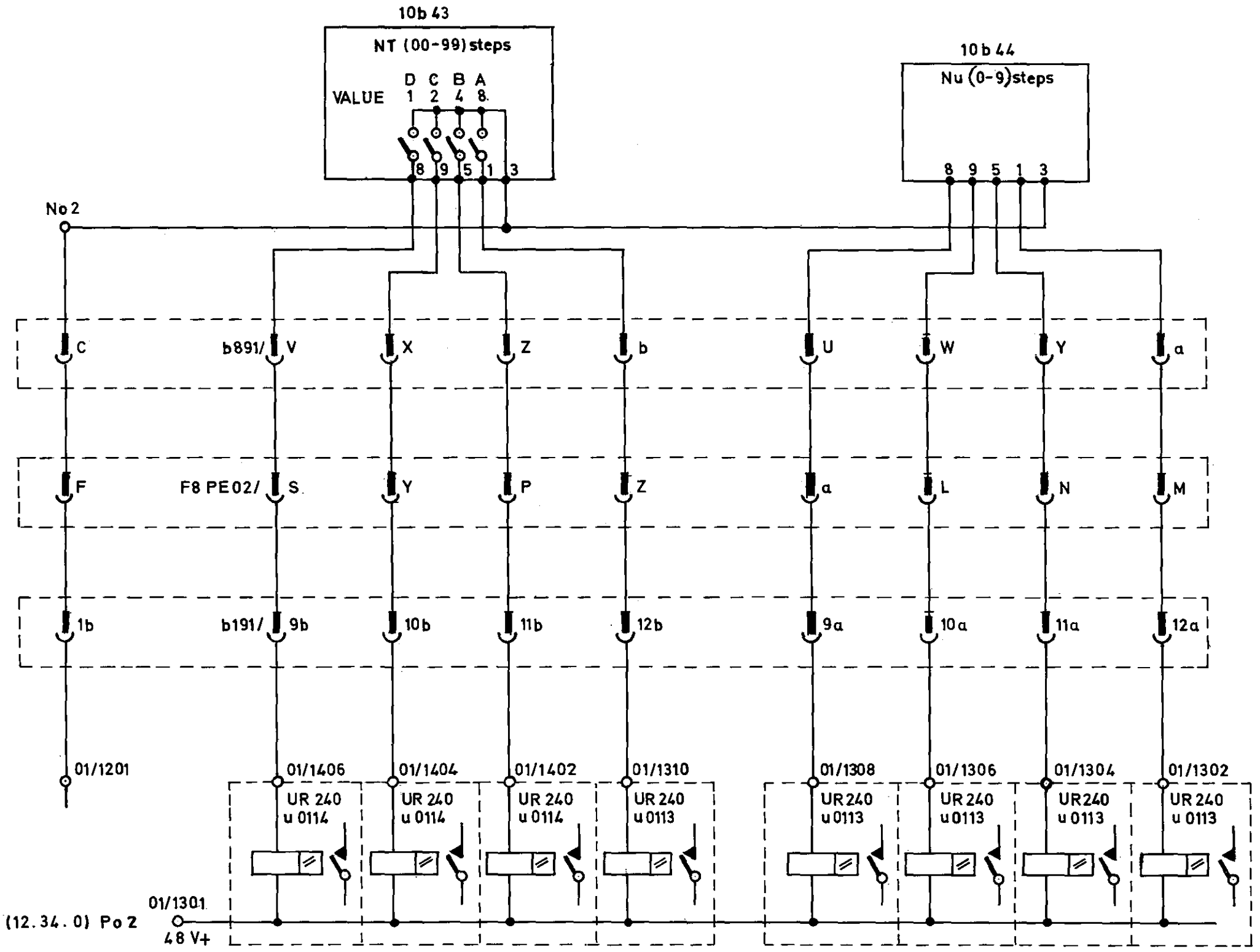
CODE SWITCHES FOR TRACKLENGTH (TL)



Code Switches for Tracklength

Figure 12 - 34

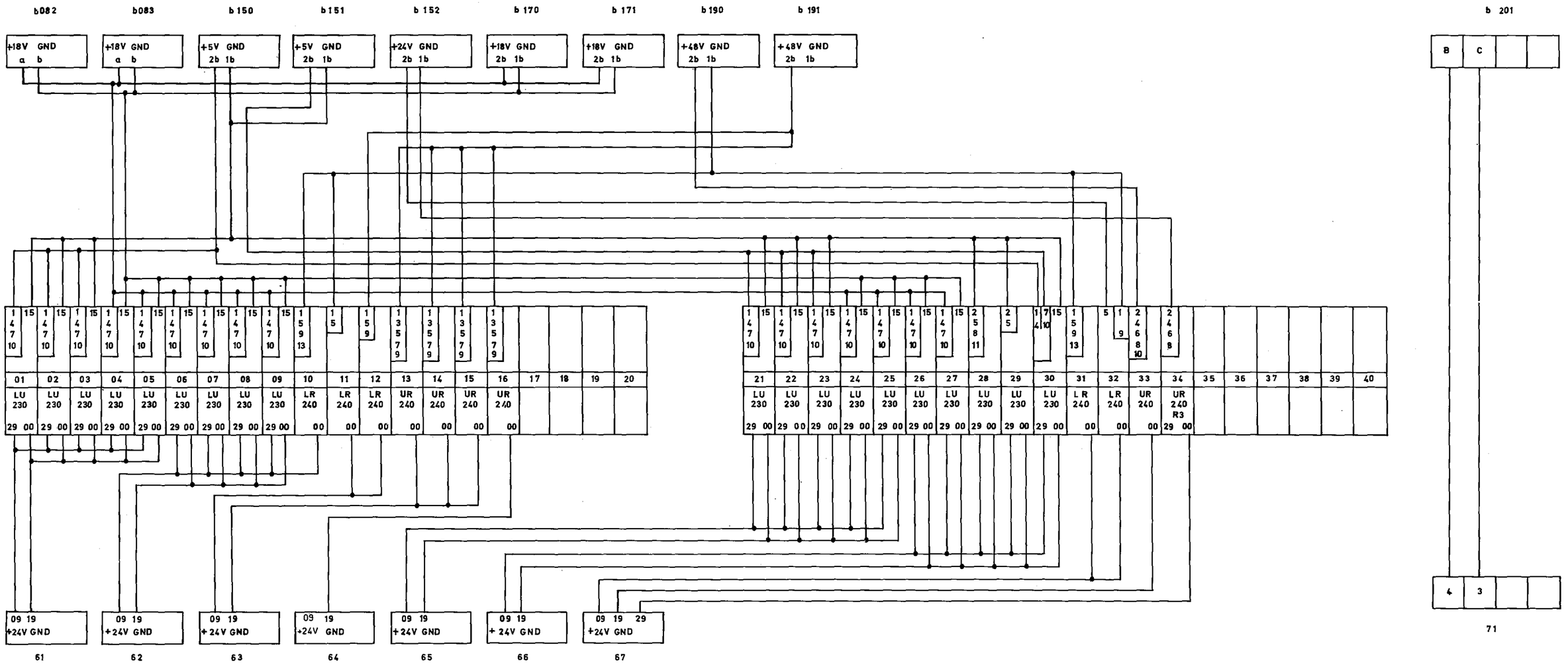
Part 1



Code Switches for Number of Steps

Figure 12–35

Part 1



Input / Output Sub-rack Power Distribution

Figure 12-36

Part 1

Iss. 1

		0	1	2	3	4	5	6	7	8	9
61	0	DOUTα A2	DOUTα B2	DOUTα C2	DOUTα D2	ROUT A2	ROUT B2	ROUT C2	ROUT D2		+24V
	1	DOUTα A3	DOUTα B3	DOUTα C3	DOUTα D3	ROUT A3	ROUT B3	ROUT C3	ROUT D3		GND
	2	DOUTα A4	DOUTα B4	DOUTα C4	DOUTα D4						
62	0	ROUTα A4	ROUTα B4	ROUTα C4	ROUTα D4	SIG RVRα	SIG RVRα	SIG RVRε	SIG RVRε		+24
	1	ROUTα A5	ROUTα B5	ROUTα C5	ROUTα D5	SELα6	SELα5	SELα4	SELα3		GND
	2	ROUTα A6	ROUTα B6	ROUTα C6	ROUTα D6						
63	0	SELα2	SELαSIG	ERROR1	ERROR2	NUA	NUB	NUC	NUD	NTA	+24V
	1	TPU/LOST	SPWA	TEST		NTB	NTC	NTD	TL2A	TL2B	GND
	2					TL2C	TL2D	TL3A	TL3B	TL3C	
64	0	TL3D	TL4A	TL4B	TL4C	TL4D					+24V
	1										GND
	2										
65	0	DOUTε A2	DOUTε B2	DOUTε C2	DOUTε D2	ROUTε A2	ROUTε B2	ROUTε C2	ROUTε D2		+24
	1	DOUTε A3	DOUTε B3	DOUTε C3	DOUTε D3	ROUTε A3	ROUTε B3	ROUTε C3	ROUTε D3		GND
	2	DOUTε A4	DOUTε B4	DOUTε C4	DOUTε D4						
66	0	ROUTε A4	ROUTε B4	ROUTε C4	ROUTε D4	SEC20	SEC21	SEC22	SEC23		+24V
	1	ROUTε A5	ROUTε B5	ROUTε C5	ROUTε D5	SEC24	SEC25	DOUTα A1			GND
	2	TCW	TCCW		TDOWN						
67	0	SELε2	SELε3	SELε4	SELε5	SELεSIG	SCIF				+24V
	1	PRES	PRES	SCAN	SPU						GND
	2	CSA	CSB	CSC	CSD	CSP					REM GND

	1	2	3	4
71			220V	220V

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
TYPE	CONNECTORS ON THE SCREENED SIDE OF THE INPUT/OUTPUT SUB-RACK	GROUP 12	SHEET 37	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	61	09	00	DOUTα A2			01	01	21
04	61	19	01	DOUTα B2			01	01	23
04	61	29	02	DOUTα C2			01	01	25
04	61	39	03	DOUTα D2			01	01	27
04	62	09	04	ROUTα A2			01	04	21
04	62	19	05	ROUTα B2			01	04	23
04	62	29	06	ROUTα C2			01	04	25
04	62	39	07	ROUTα D2			01	04	27
			08						
			09	+24V			01	01	29
							01	02	29
							01	03	29
							01	04	29
							01	05	29
04	61	08	10	DOUTα A3			01	02	21
04	61	18	11	DOUTα B3			01	02	23
04	61	28	12	DOUTα C3			01	02	25
04	61	38	13	DOUTα D3			01	02	27
04	62	08	14	ROUTα A3			01	05	21
04	62	18	15	ROUTα B3			01	05	23
04	62	28	16	ROUTα C3			01	05	25
04	62	38	17	ROUTα D3			01	05	27
			18						
			19	GND			01	01	00
							01	02	00
							01	03	00
							01	04	00
							01	05	00
04	61	07	20	DOUTα A4			01	03	21
04	61	17	21	DOUTα B4			01	03	23
04	61	27	22	DOUTα C4			01	03	25
04	61	37	23	DOUTα D4			01	03	27
			24						
			25						
			26						
			27						
			28						
			29						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	61	
TYPE	TERMINAL BLOCK 0161	GROUP 12	SHEET 38	
		PART 1		

Iss.1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	62	07	00	ROUTα A4			01	06	21
04	62	17	01	ROUTα B4			01	06	23
04	62	27	02	ROUTα C4			01	06	25
04	62	37	03	ROUTα D4			01	06	27
04	62	04	04	SIG RVRα			01	09	21
04	62	14	05	$\overline{\text{SIG RVRα}}$			01	09	23
05	61	24	06	SIG RVRε			01	09	25
05	61	34	07	$\overline{\text{SIG RVRε}}$			01	09	27
			08						
			09	+24V			01	06	29
							01	07	29
							01	08	29
							01	09	29
							01	10	00
04	62	06	10	ROUTα A5			01	07	21
04	62	16	11	ROUTα B5			01	07	23
04	62	26	12	ROUTα C5			01	07	25
04	62	36	13	ROUTα D5			01	07	27
04	61	05	14	SELα 6			01	10	21
04	61	15	15	SELα 5			01	10	23
04	61	25	16	SELα 4			01	10	25
04	61	35	17	SELα 3			01	10	27
			18						
			19	GND			01	06	00
							01	07	00
							01	08	00
							01	09	00
							01	10	00
04	62	05	20	ROUTα A6			01	08	21
04	62	15	21	ROUTα B6			01	08	23
04	62	25	22	ROUTα C6			01	08	25
04	62	35	23	ROUTα D6			01	08	27
			24						
			25						
			26						
			27						
			28						
			29						
			30						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	62	
TYPE	TERMINAL BLOCK 0162	GROUP	SHEET	
		12	39	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	61	04	00	SELα 2			01	11	21
04	61	14	01	SELα SIG			01	11	23
04	61	24	02	ERROR1			01	11	25
04	61	34	03	ERROR2			01	11	27
01	13	21	04	NUA			04	63	09
01	13	23	05	NUB			04	63	19
01	13	25	06	NUC			04	63	29
01	13	27	07	NUD					
01	13	29	08	NTA			04	63	08
			09	+24V			01	11	00
							01	12	00
04	61	33	10	TPU/LOST			01	12	21
04	61	02	11	SPWA			01	12	23
02	60	47	12	TEST GND			01	12	25
			13						
01	14	21	14	NTB			04	63	18
01	14	23	15	NTC			04	63	28
01	14	25	16	NTD			04	63	38
01	14	27	17	TL2A			04	63	07
01	14	29	18	TL2B			04	63	17
			19	GND			01	13	00
							01	14	00
							01	15	00
			20						
			21						
			22						
			23						
01	15	21	24	TL2C			04	63	27
01	15	23	25	TL2D			04	63	37
01	15	25	26	TL3A			04	63	06
01	15	27	27	TL3B			04	63	16
01	15	29	28	TL3C			04	63	26
			29						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	63	
TYPE		GROUP	SHEET	
	TERMINAL BLOCK 0163	12	40	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
01	16	21	00	TL3D			04	63	36
01	16	23	01	TL4A			04	63	05
01	16	25	02	TL4B			04	63	15
01	16	27	03	TL4C			04	63	25
01	16	29	04	TL4D			04	63	35
			05						
			06						
			07						
			08						
			09	+24V					
			10						
			11						
			12						
			13						
			14						
			15						
			16						
			17						
			18						
			19	GND			01	16	00
			20						
			21						
			22						
			23						
			24						
			25						
			26						
			27						
			28						
			29						
			30						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	64	
TYPE	TERMINAL BLOCK 0164	GROUP 12	SHEET 41	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	61	09	00	DOUTε A2			01	21	21
05	61	19	01	DOUTε B2			01	21	23
05	61	29	02	DOUTε C2			01	21	25
05	61	39	03	DOUTε D2			01	21	27
05	61	03	04	ROUTε A2			01	24	21
05	61	13	05	ROUTε B2			01	24	23
05	61	23	06	ROUTε C2			01	24	25
05	61	33	07	ROUTε D2			01	24	27
			08						
			09	+24V			01	21	29
							01	22	29
							01	23	29
							01	24	29
							01	25	29
05	61	08	10	DOUT A3			01	22	21
05	61	18	11	DOUTε B3			01	22	23
05	61	28	12	DOUTε C3			01	22	25
05	61	38	13	DOUTε D3			01	22	27
05	61	02	14	ROUTε A3			01	25	21
05	61	12	15	ROUTε B3			01	25	23
05	61	22	16	ROUTε C3			01	25	25
05	61	32	17	ROUTε D3			01	25	27
			18						
			19	GND			01	21	00
							01	22	00
							01	23	00
							01	24	00
							01	25	00
05	61	07	20	DOUTε A4			01	23	21
05	61	17	21	DOUTε B4			01	23	23
05	61	27	22	DOUTε C4			01	23	25
05	61	37	23	DOUTε D4			01	23	27
			24						
			25						
			26						
			27						
			28						
			29						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	65	
TYPE	TERMINAL BLOCK 0165	GROUP 12	SHEET 42	
		PART 1		

Iss.1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	61	01	00	ROUTε A4			01	26	21
05	61	11	01	ROUTε B4			01	26	23
05	61	21	02	ROUTε C4			01	26	25
05	61	31	03	ROUTε D4			01	26	27
05	62	01	04	SEC20			01	28	21
05	62	11	05	SEC21			01	28	23
05	62	21	06	SEC22			01	28	25
05	62	31	07	SEC23			01	28	27
			08						
			09	+24V			01	26	29
							01	27	29
							01	28	29
							01	29	29
							01	30	29
05	61	00	10	ROUTε A5			01	27	21
05	61	10	11	ROUTε B5			01	27	23
05	61	20	12	ROUTε C5			01	27	25
05	61	30	13	ROUTε D5			01	27	27
05	62	00	14	SEC24			01	29	21
05	62	10	15	SEC25			01	29	23
04	61	06	16	DOUTα A1			01	29	25
05	61	06	17	DOUTε A1			01	29	27
			18						
			19	GND			01	26	00
							01	27	00
							01	28	00
							01	29	00
							01	30	00
02	60	25	20	TCW			01	30	21
02	60	26	21	TCCW			01	30	23
02	60	27	22	TUP			01	30	25
02	60	28	23	TDOWN			01	30	27
			24						
			25						
			26						
			27						
			28						
			29						

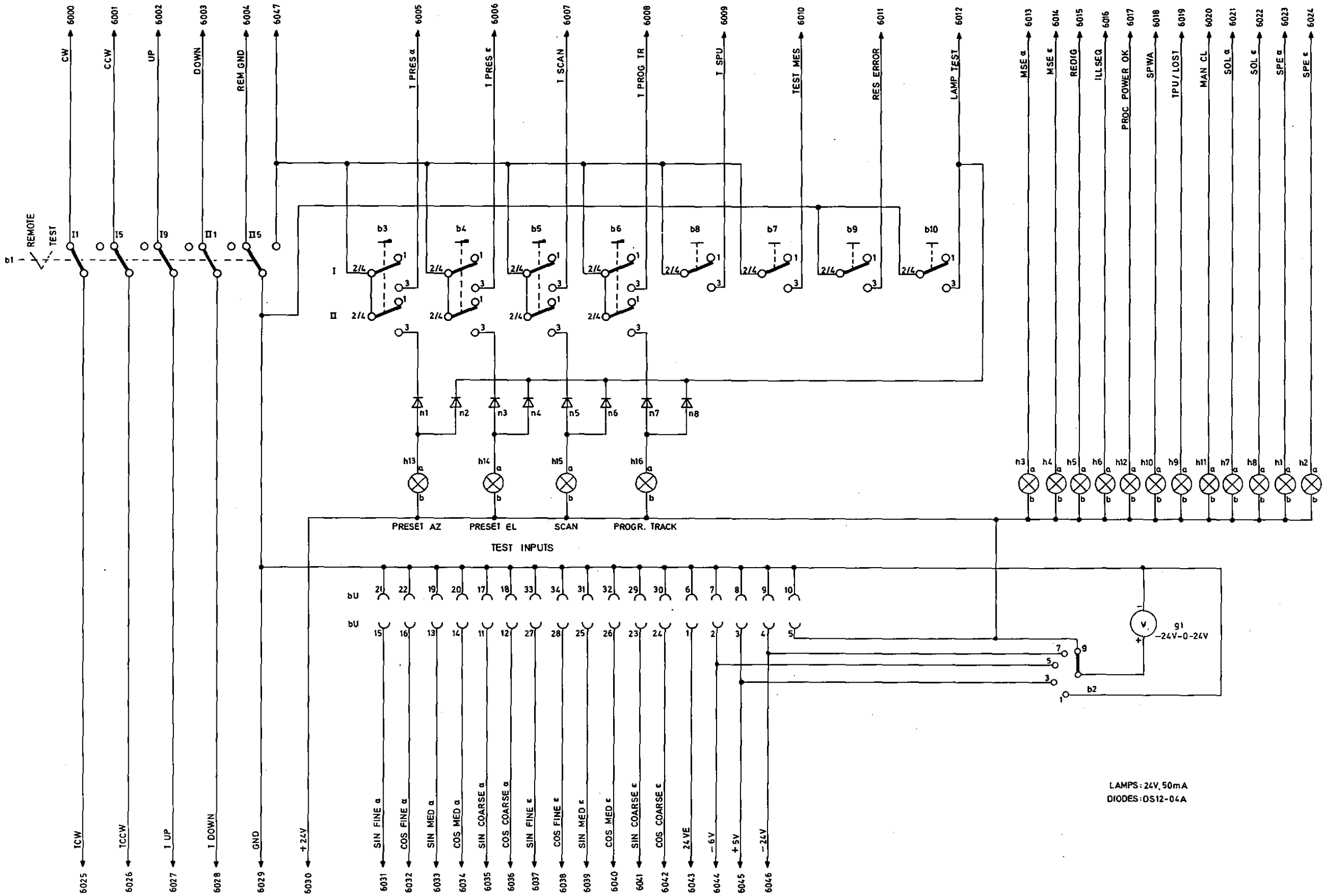
INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	66	
TYPE		GROUP	SHEET	
	TERMINAL BLOCK 0166	12	43	
		PART 1		

Iss.1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	61	35	00	SELε 2			01	31	21
05	61	25	01	SELε 3			01	31	23
05	61	15	02	SELε 4			01	31	25
05	61	05	03	SELε 5			01	31	27
05	61	14	04	SELεSIG			01	32	21
04	61	13	05	SC1F			01	32	23
			06						
			07						
			08						
			09	+24V			01	31	00
							01	32	00
01	34	21	10	PRESα			05	63	09
01	34	23	11	PRESε			05	63	19
01	34	25	12	SCAN			05	63	29
01	34	27	13	SPU			05	63	39
			14						
			15						
			16						
			17						
			18						
			19	GND			01	33	00
01	33	21	20	$\overline{\text{CSA}}$			05	63	07
01	23	23	21	$\overline{\text{CSB}}$			05	63	17
01	23	25	22	$\overline{\text{CSC}}$			05	63	27
01	23	27	23	$\overline{\text{CSD}}$			05	63	37
01	23	29	24	$\overline{\text{CSP}}$			05	63	06
			25						
			26						
			27						
			28						
02	60	04	29	REM GND			01	34	00

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		01	67	
TYPE	TERMINAL BLOCK 0167	GROUP 12	SHEET 44	
		PART 1		

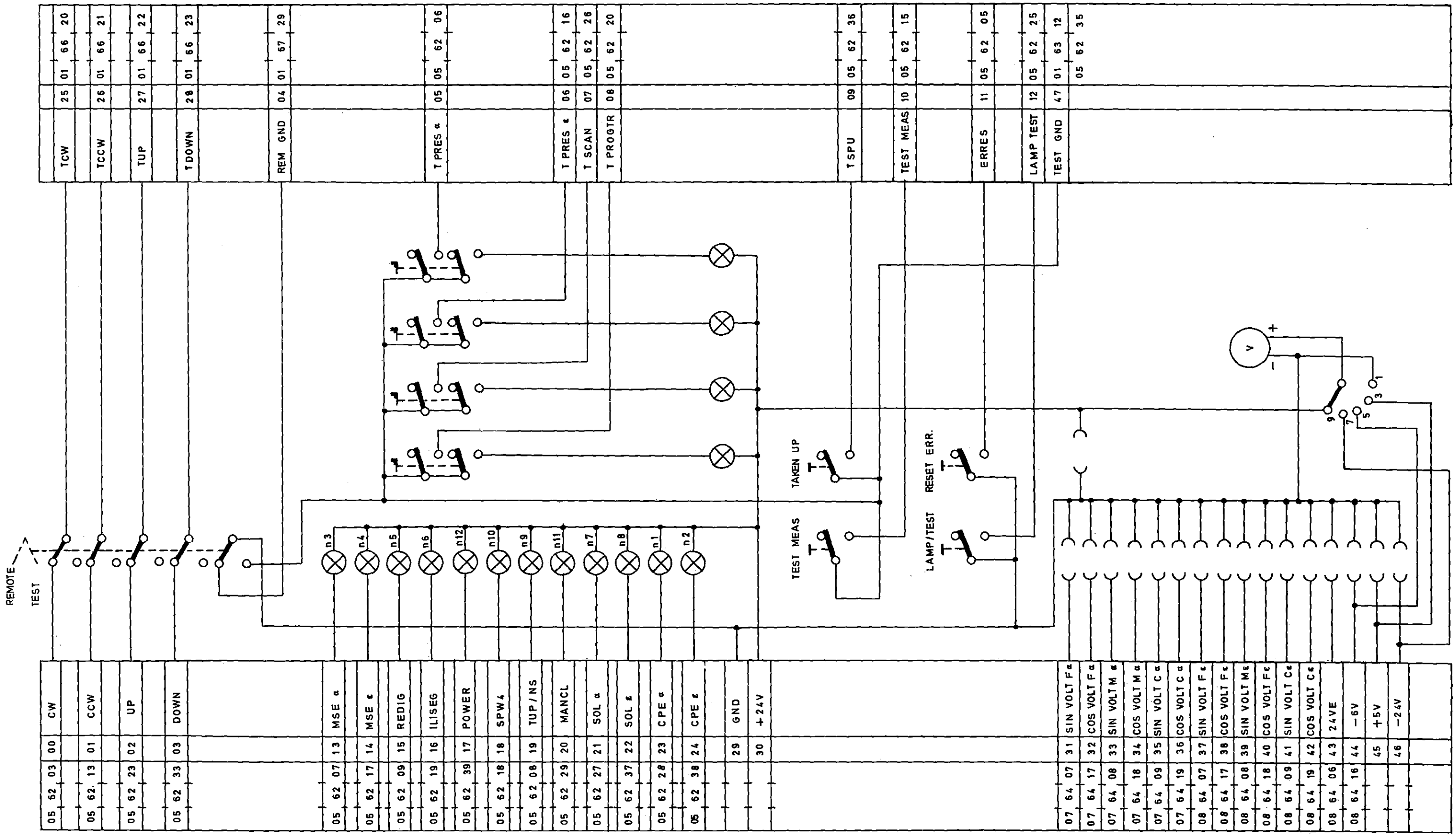
Iss. 1



Testpanel: Circuit Diagram

Figure 12-46

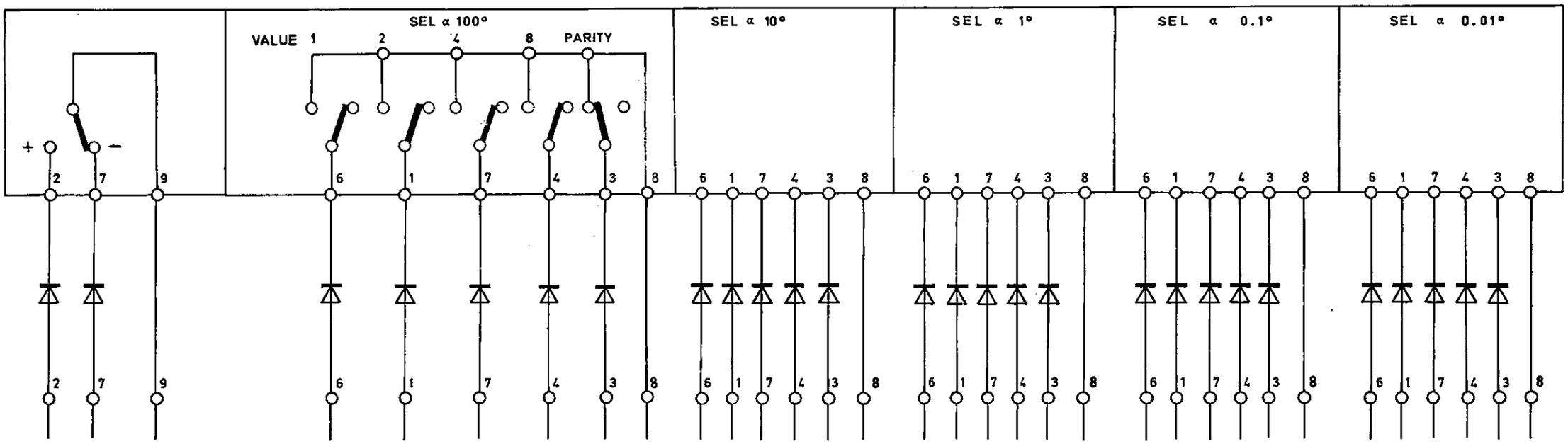
Part 1



Testpanel : Interconnection Table

Figure 12–47

Part 1

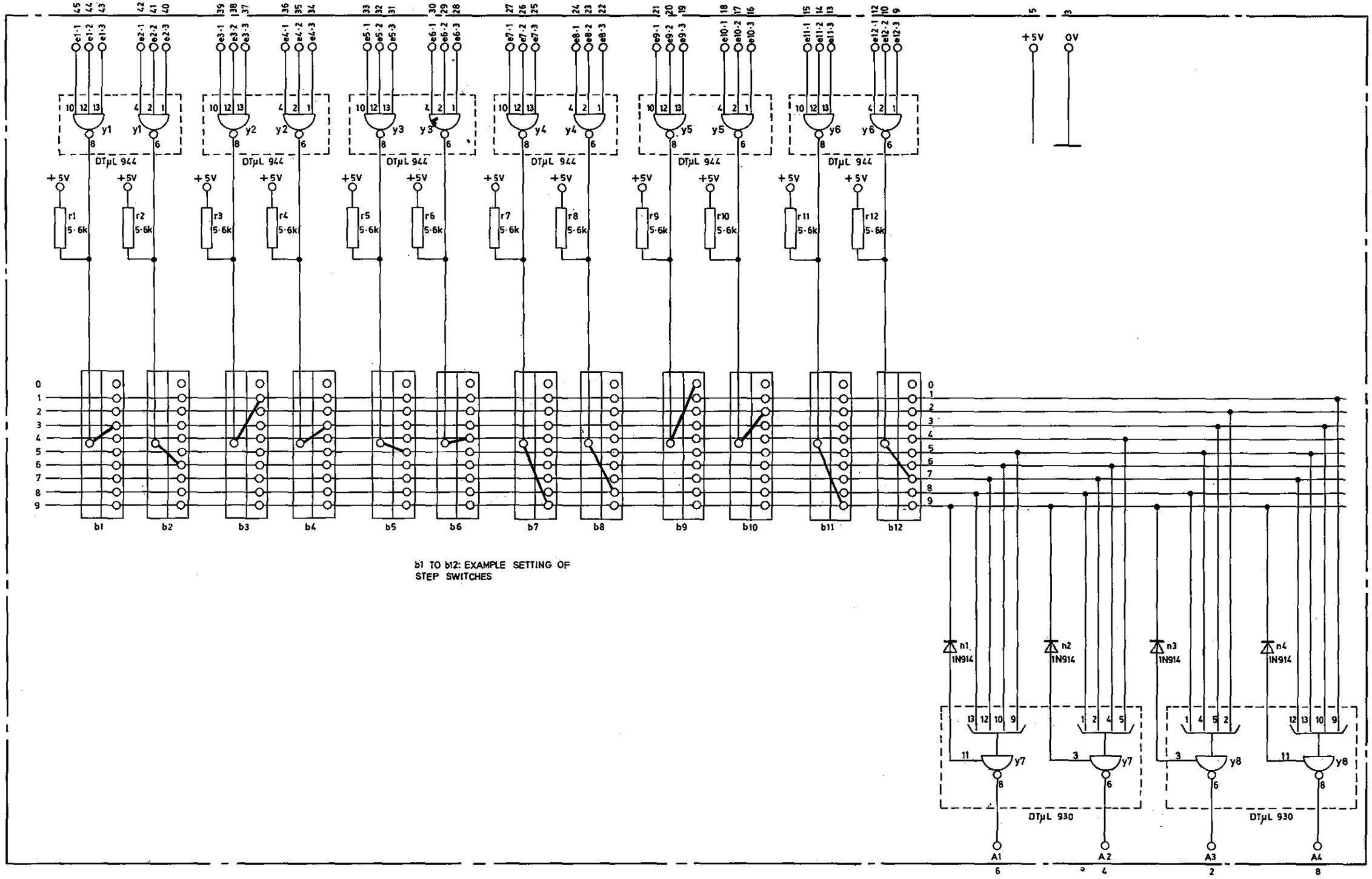


CODE SWITCH ASSEMBLY FOR ITEM 20b20-25
RESP. 30b20-25
CONSISTING OF:
1 SWITCH B001/LS
3 SWITCHES B037/LS (BCD a. IMP. CHECK)
2 SWITCHES B037/LS/3 (CODE AS ABOVE-
RED DIALS, WHITE LETTERS)
1 CLOSING PAIR OF ANGLES BW
4 THREADED RODS M2.6
8 SLOTTED NUTS
MULTIPOINT CONNECTORS x 36
WITH 27 DIODES 1N4003 EQUIPPED ACCORDING
TO ABOVE CONNECTION DIAGRAM
COMPLETE WITH MOUNTING.
DIODES 1N4003 (M1-No.XN 400308 P3) TO
BE PROVIDED.

Code Switch Assembly: Connection Diagram

Figure 12-51

Part 1



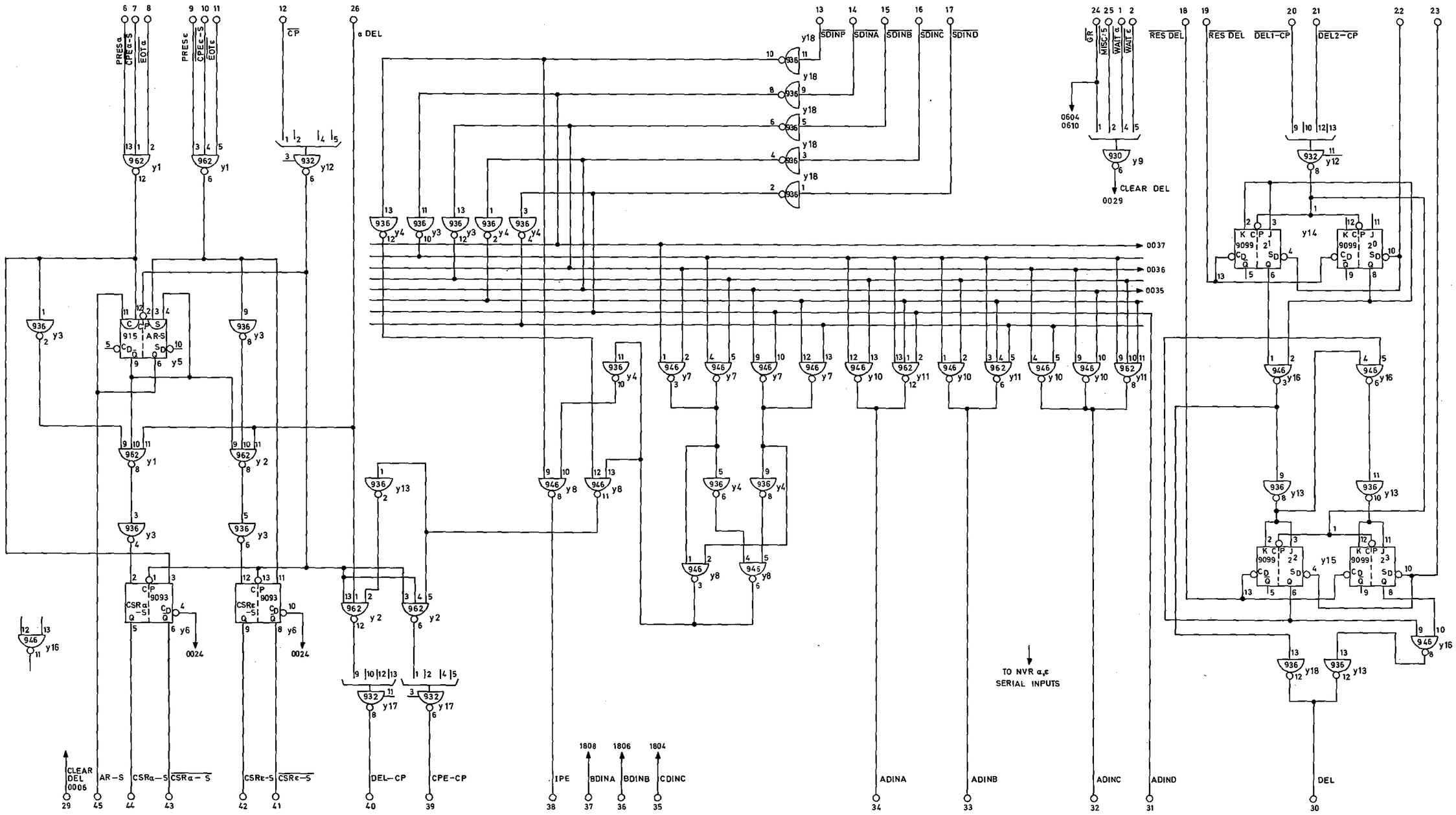
b1 TO b12: EXAMPLE SETTING OF STEP SWITCHES

Fixed Value Store -JC215a-E: Circuit Diagram

Figure 12-60 Part 1

Card. FVS I Location u0411
FVS II u0511
FVS III u0611

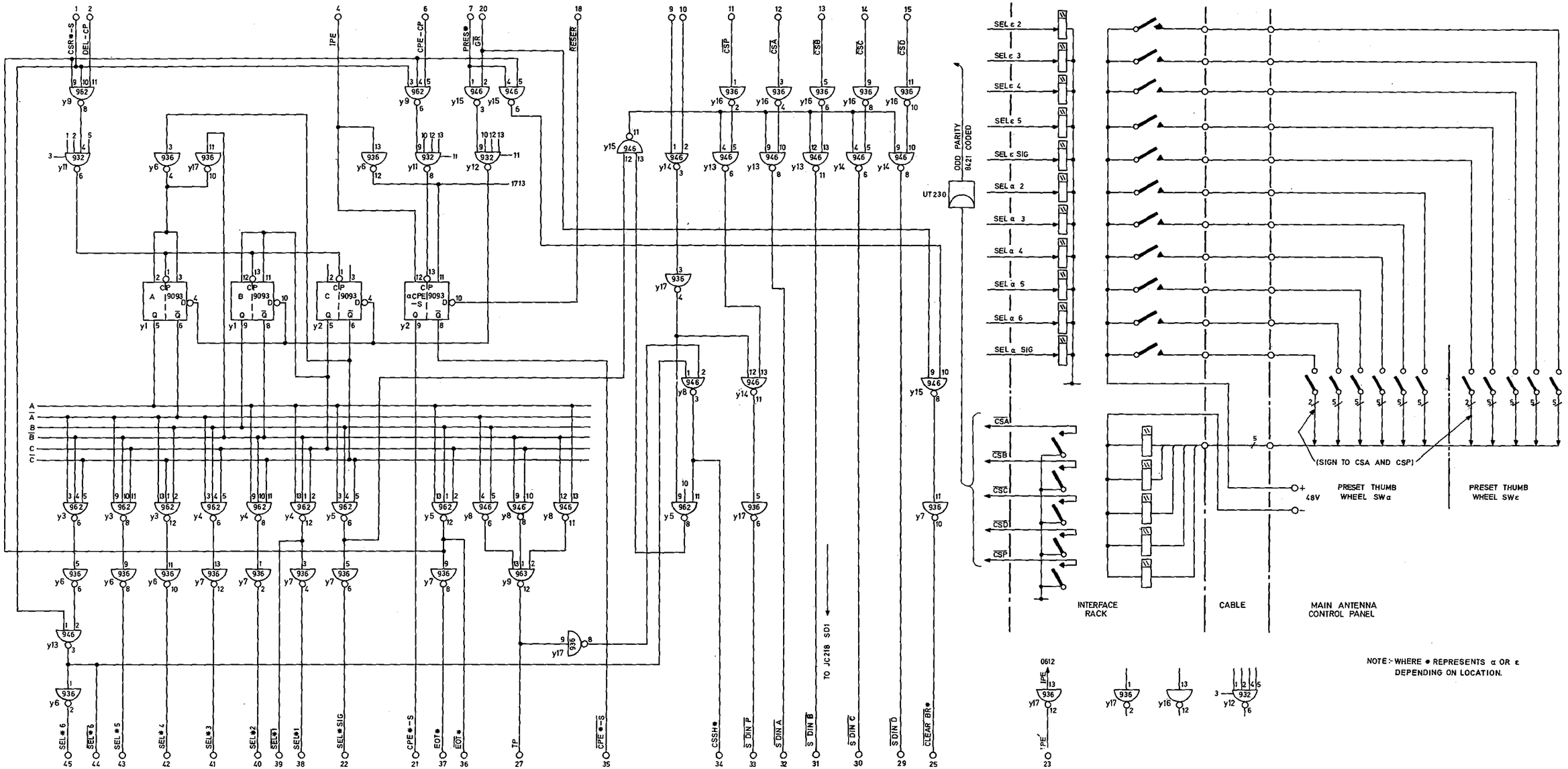
Iss. 1



Serial Data Input, BCD→Aiken, Parity Check-JC218a-E:Circuit Diagram

Figure 12-61 Card. SDI Location u0617

Part 1



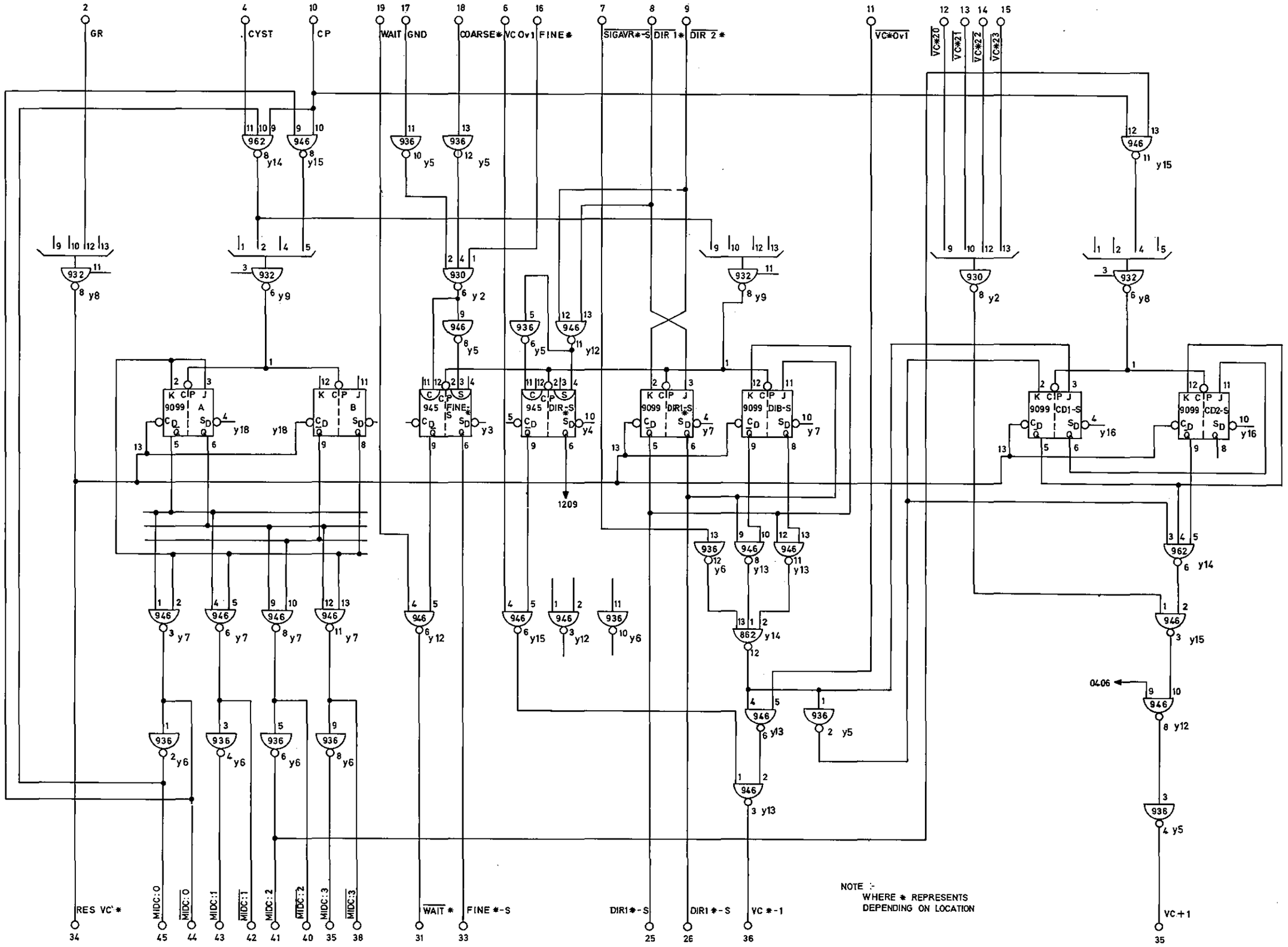
NOTE:- WHERE * REPRESENTS α OR ε DEPENDING ON LOCATION.

Code Switch Request - JC 218a-E : Circuit Diagram

Figure 12-62 Card: CR α Location u 0410
CR ε Location u 0510

Part 1

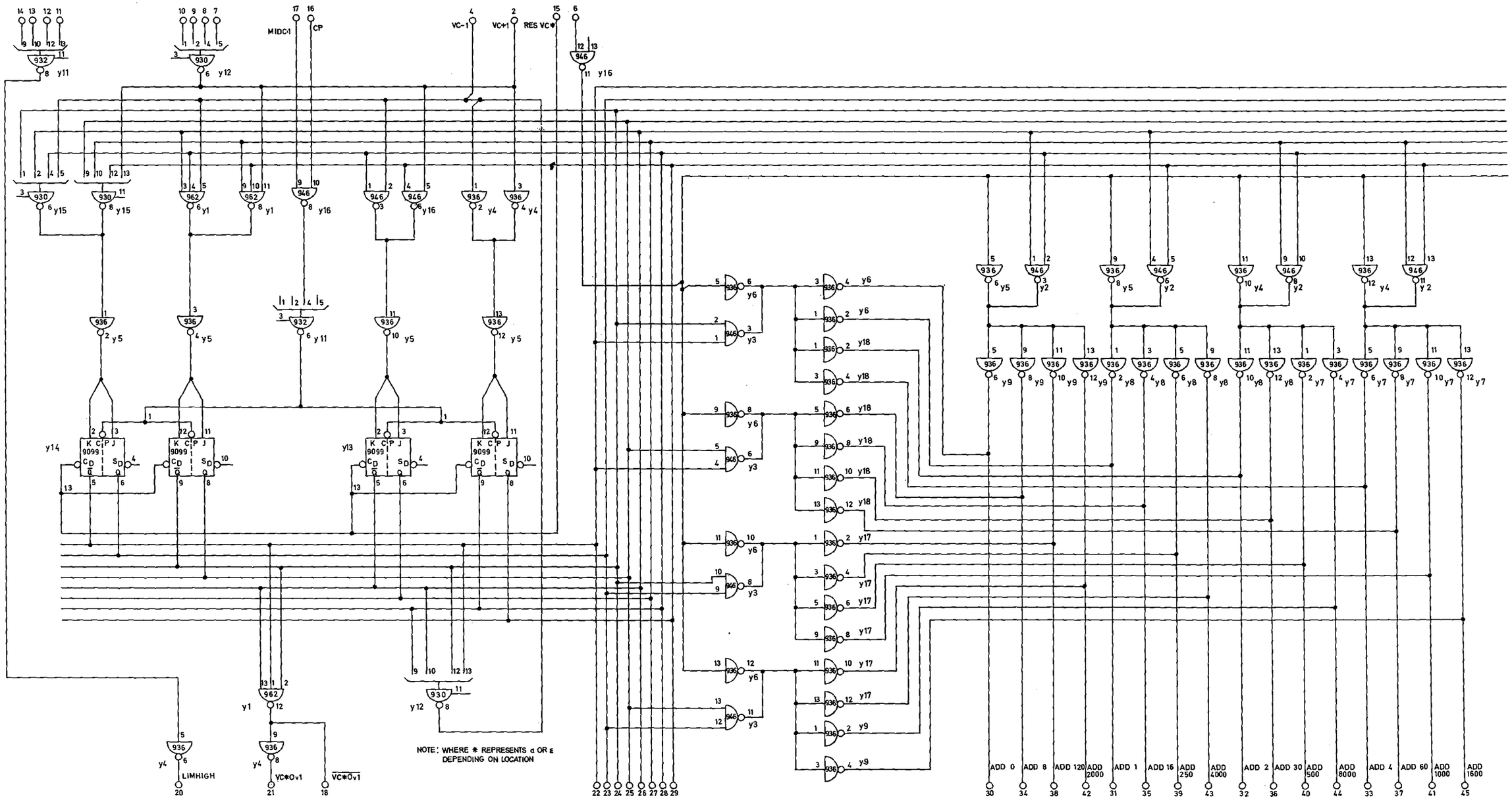
Iss. 1



Digital Copy Control-JC218a-E:Circuit Diagram

Figure 12—63 Card: DCa Location u0401
DCε u0501

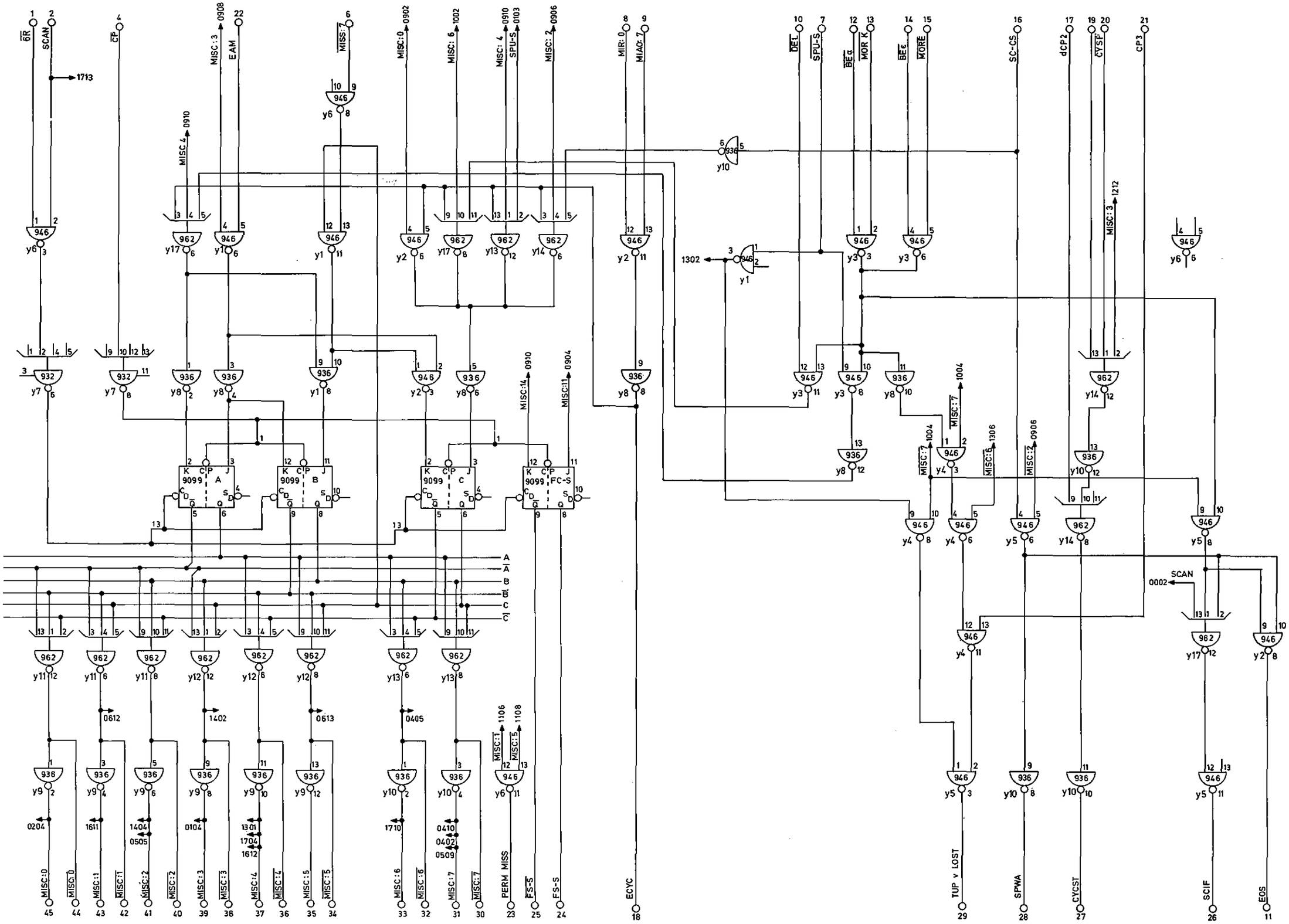
Part 1



Value Counter – JC 218a – E Circuit Diagram

Figure 12 – 64 Card: VCa Location u0402
VC ε u0502

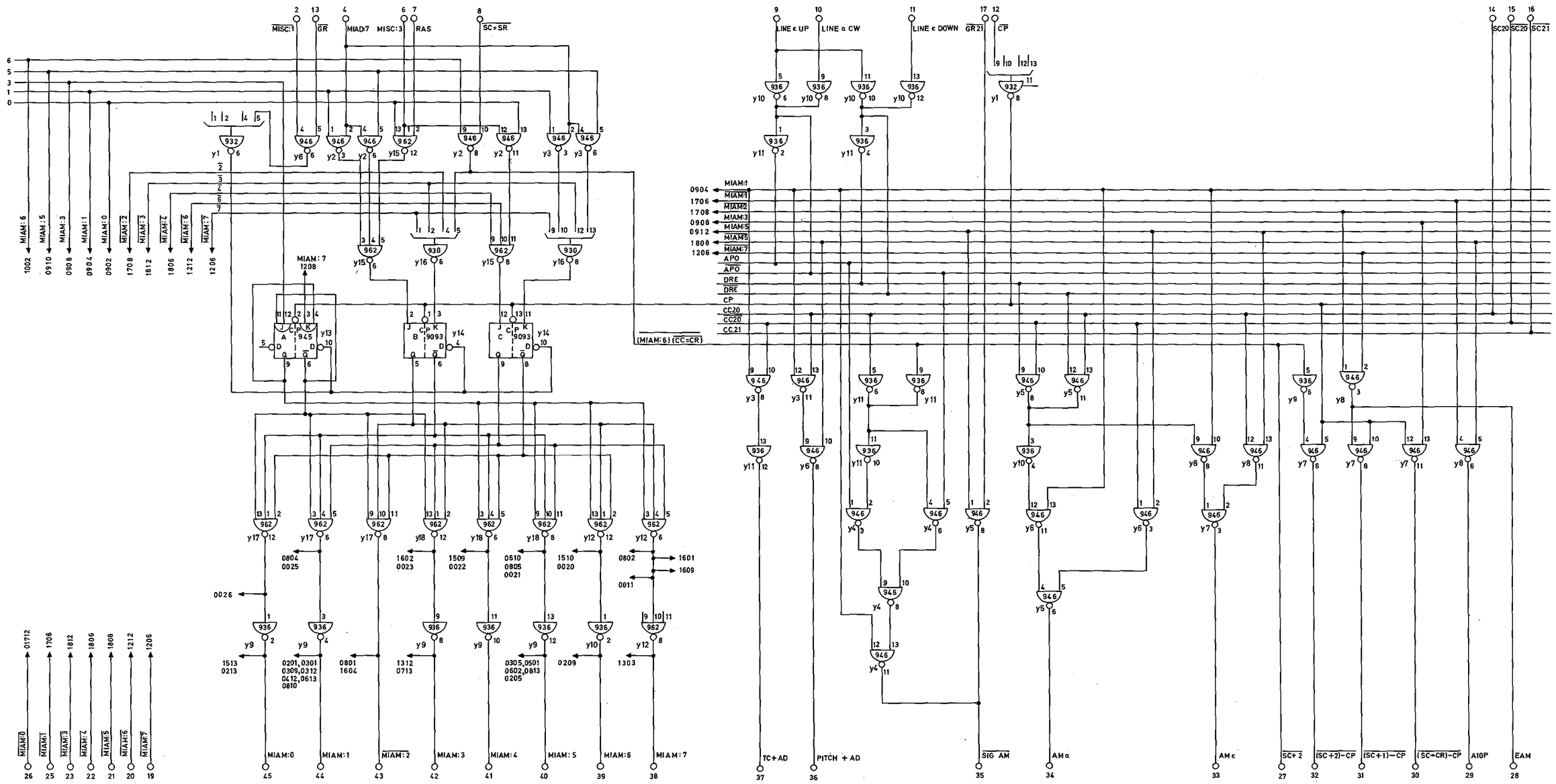
Part 1



Scan Control (MISC) JC 218a – E : Circuit Diagram

Figure 12-65 Card: SC Location u0602

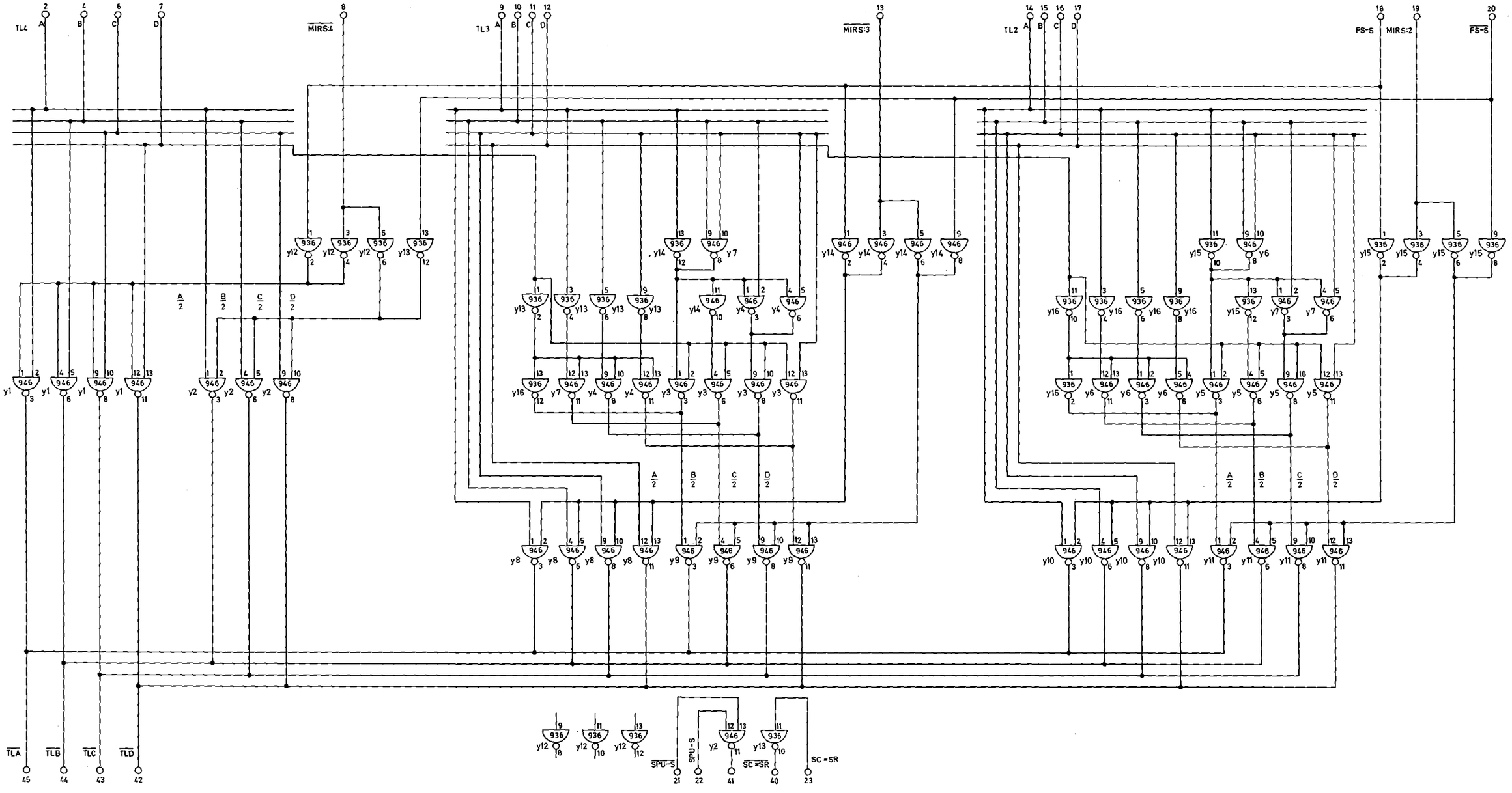
Part 1



Axis Modifier-JC-218a-E: Circuit Diagram

Figure 12-66 Card: AM Location u0603

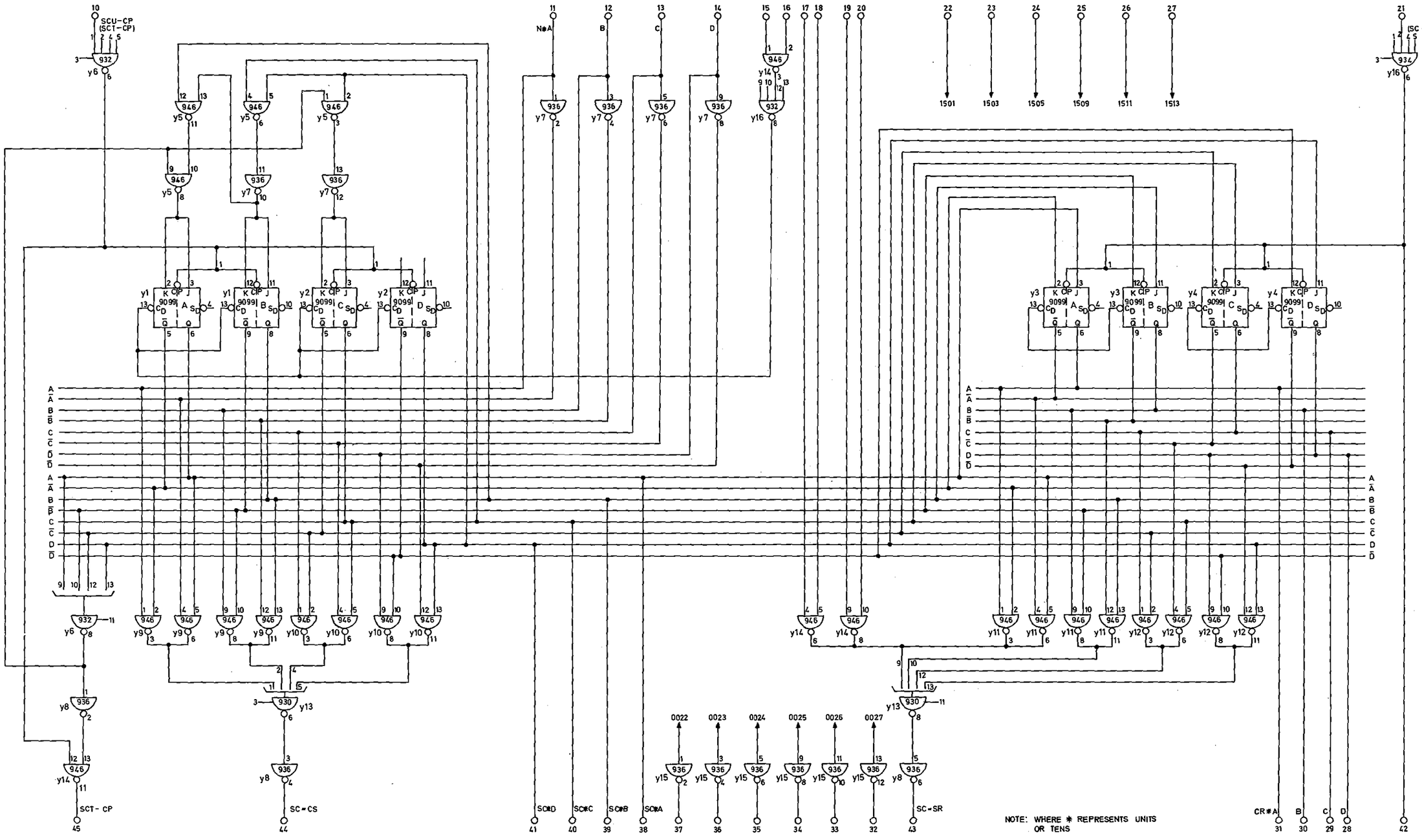
Part 1



Tracklength→½ Tracklength—JC 218a-E: Circuit Diagram

Figure 12-67 Card: TL Location u0601

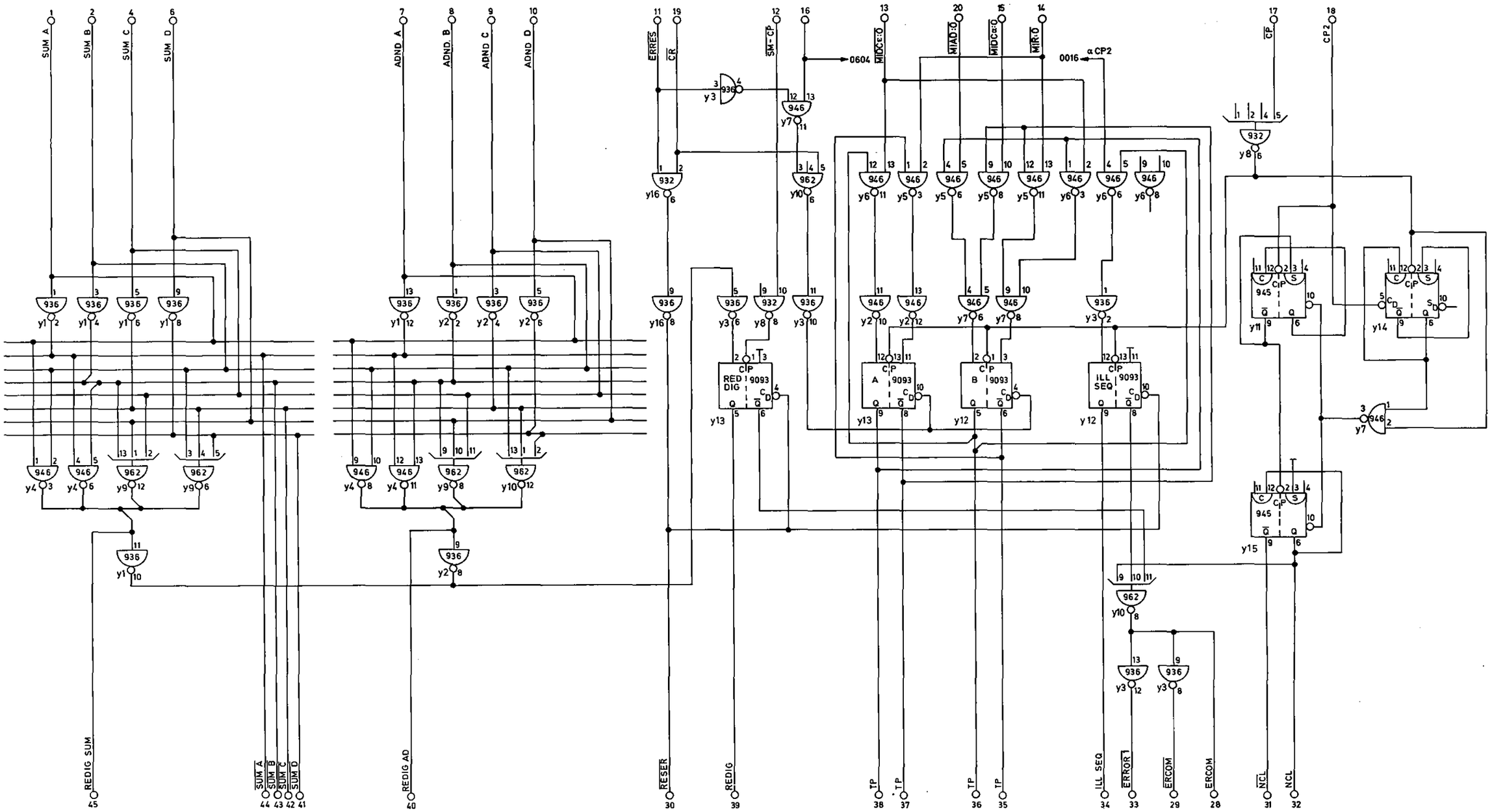
Part 1



Decade and Step Counters — JC218a—E : Circuit Diagram

Figure 12—68 Card: DEC (units) Location u0403
DEC (tens) u0503

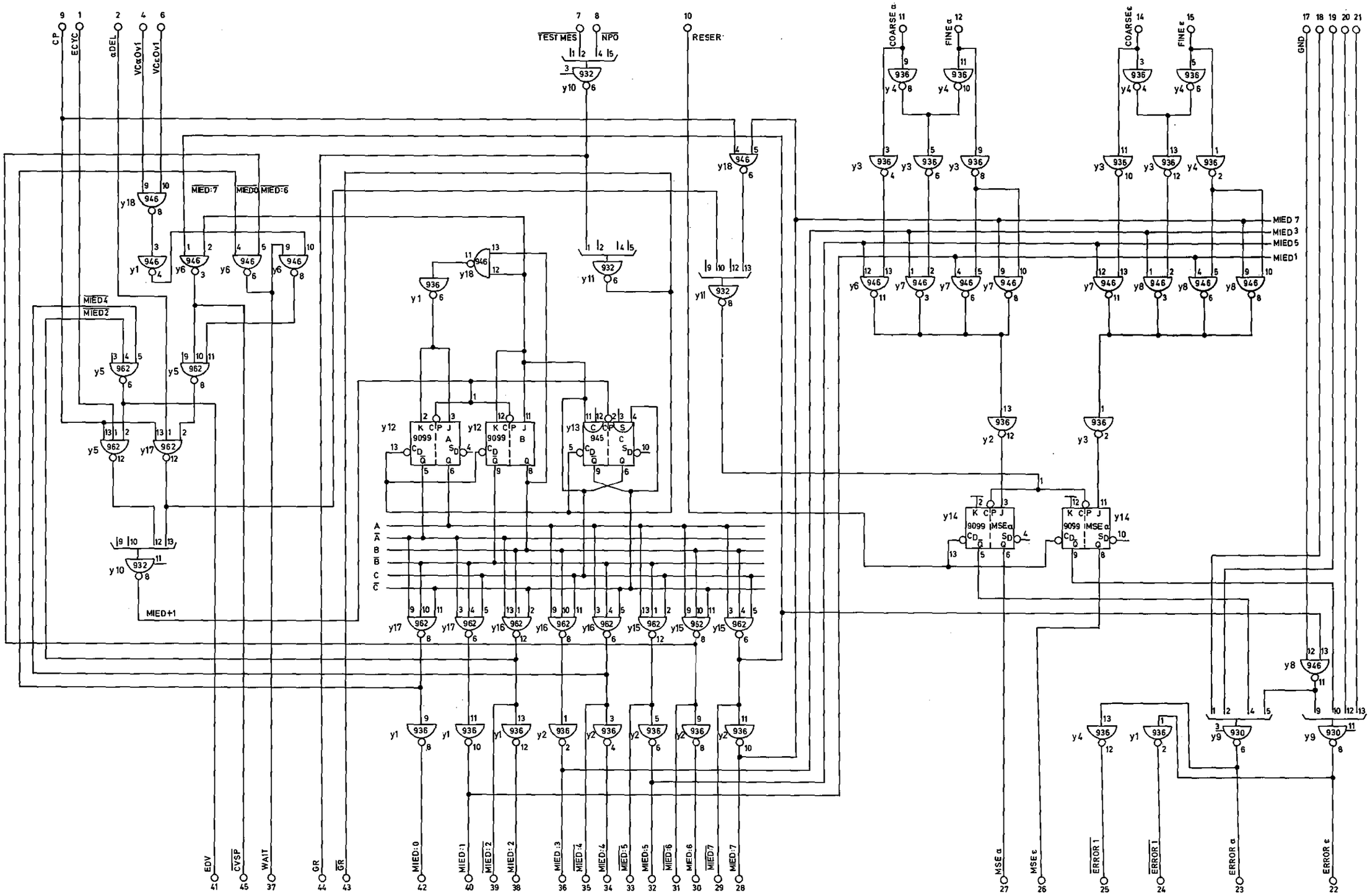
Part 1



Processor Error Detector – JC 218a-E : Circuit Diagram

Figure 12-69 Card: PED Location u0616

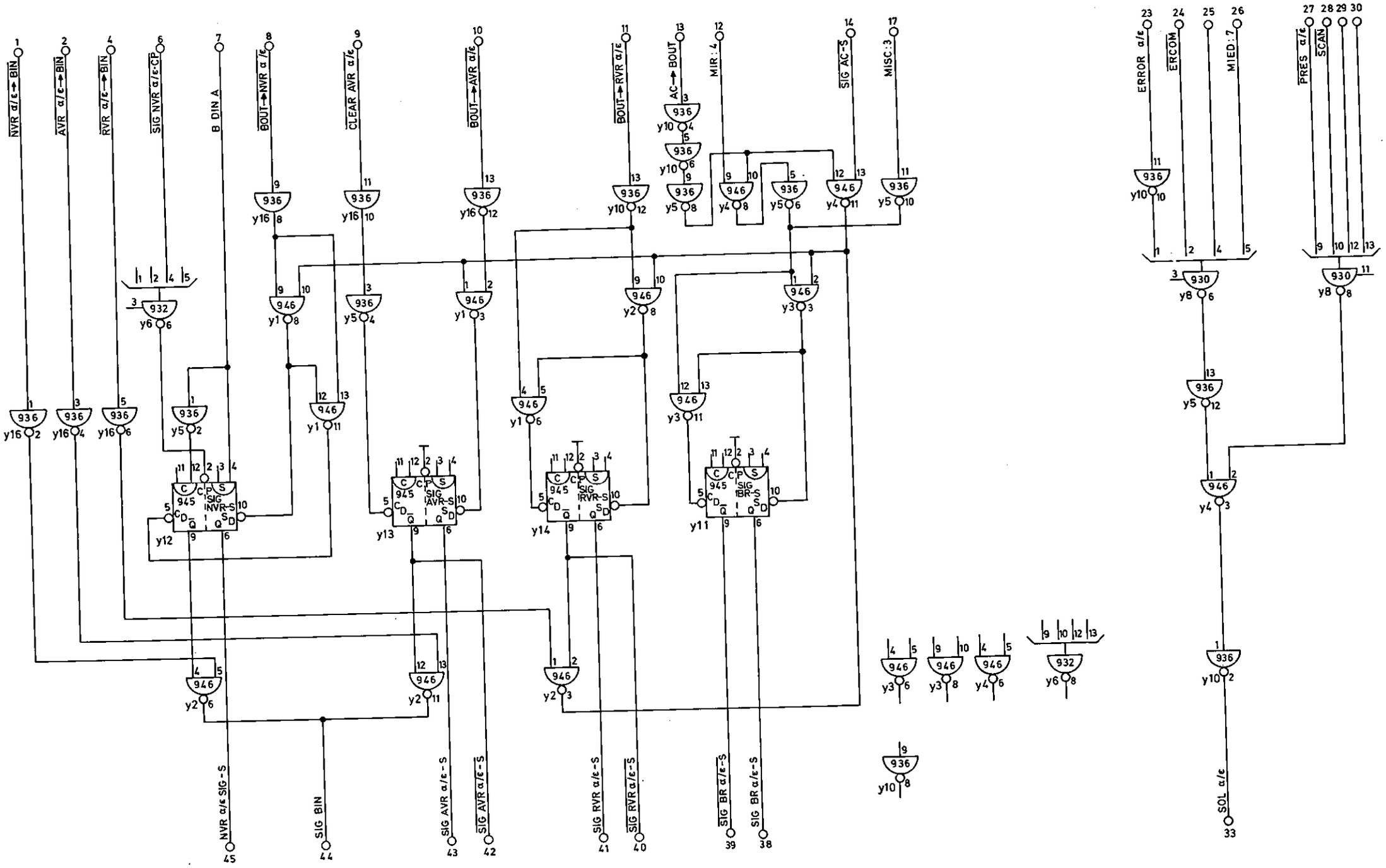
Part 1



Measurement Error Detector–JC218a-E : Circuit Diagram

Figure 12-70 Card: MED Location u0615

Part 1

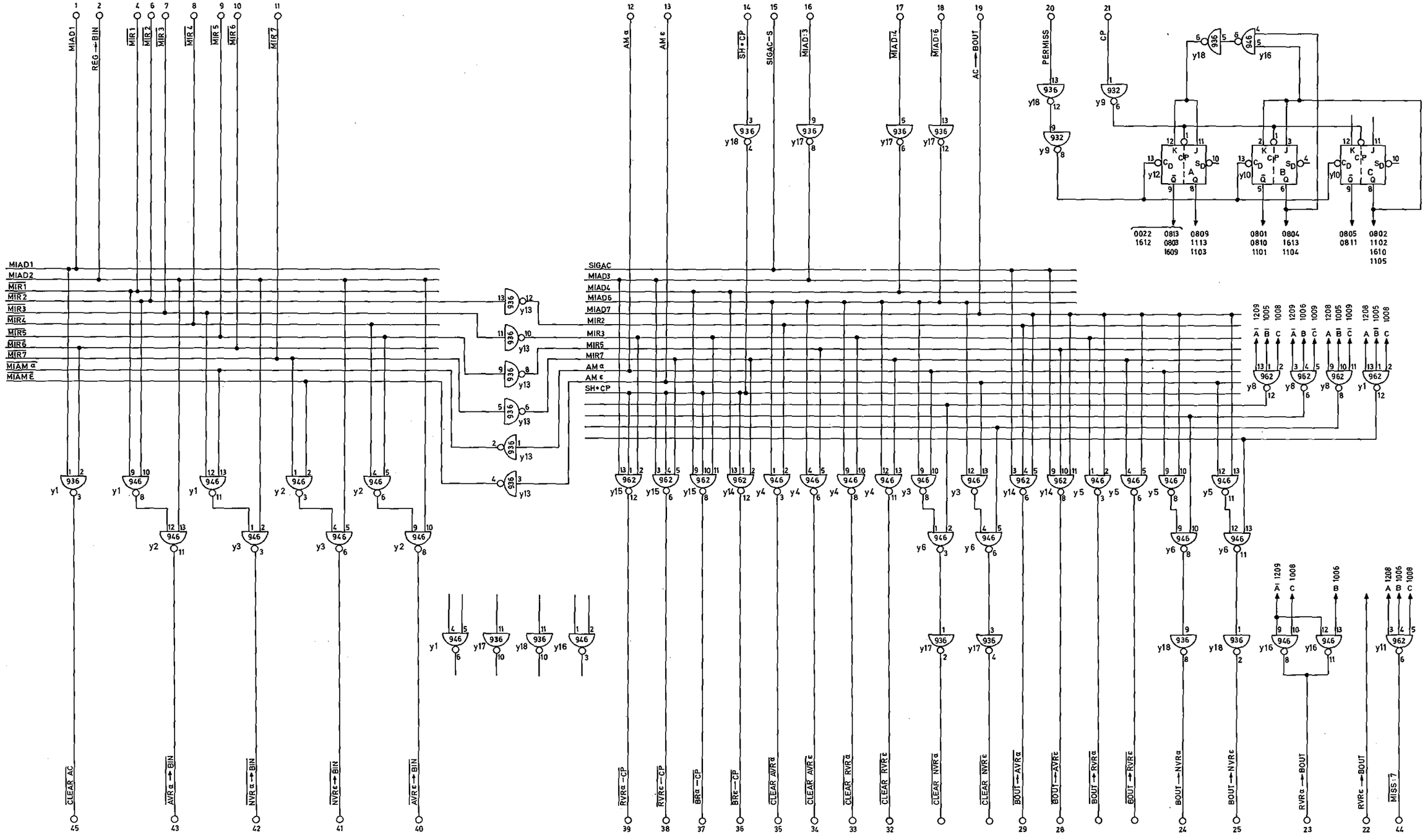


Servo On Line Control Register-JC 218a-E : Circuit Diagram

Figure 12-71 Card: SOL α Location u 0409
SOL ε u 0509

Part 1

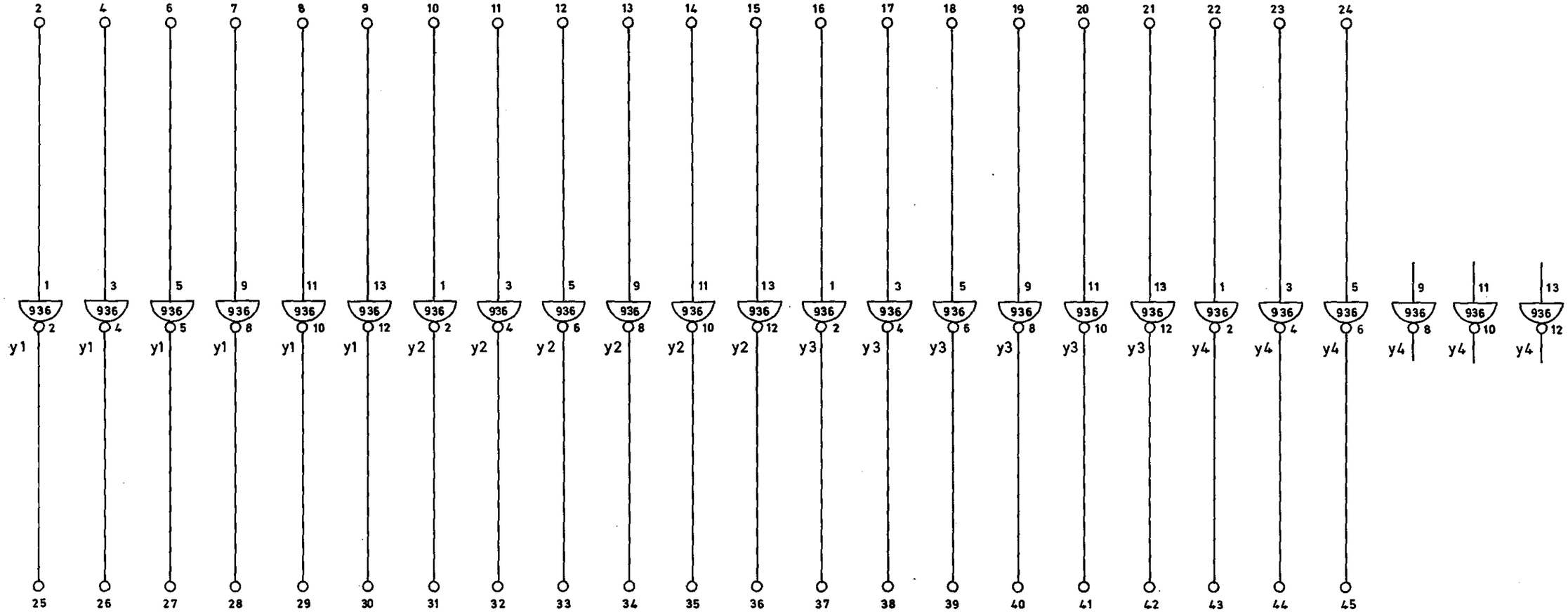
Iss. 1



Register Control I -JC218a-E: Circuit Diagram

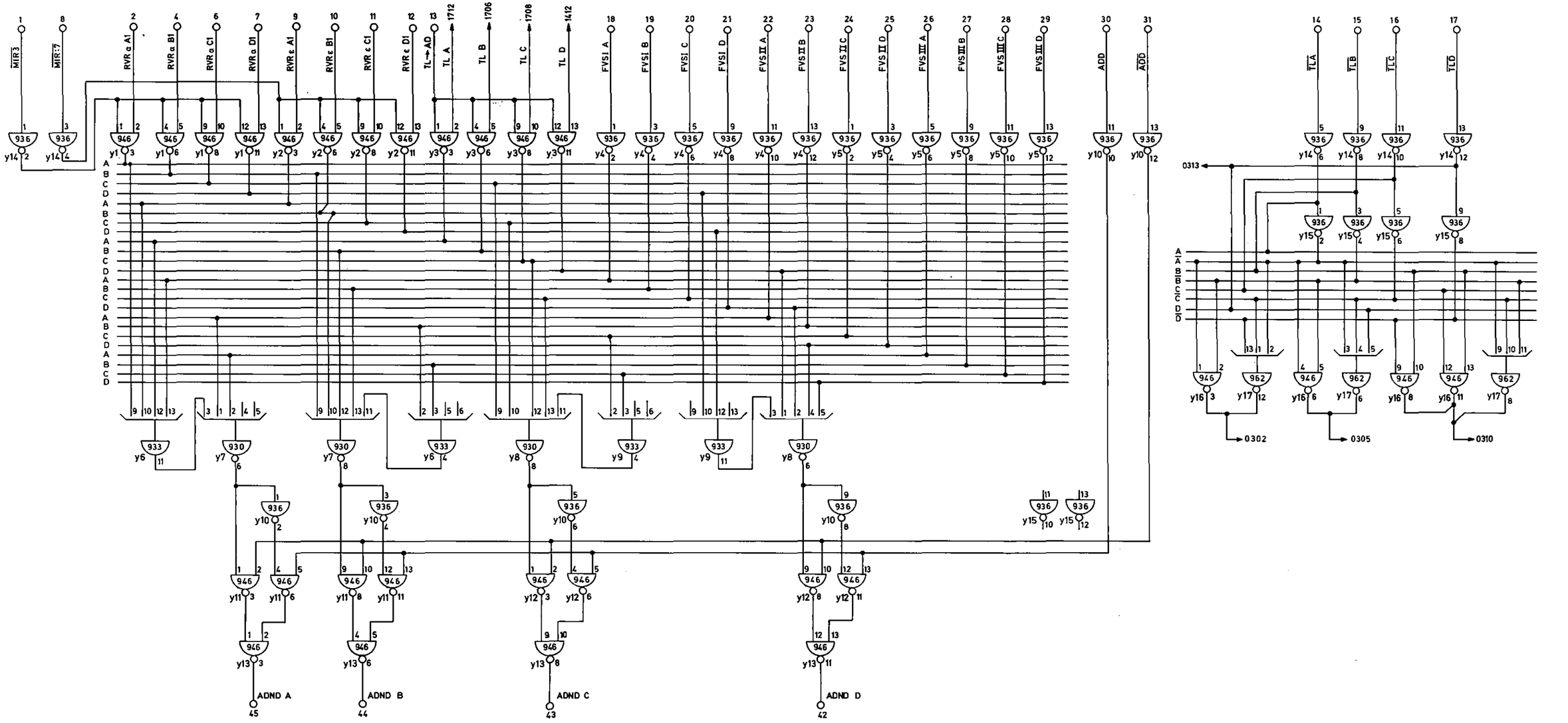
Figure 12-72 Card. RCI Location u0606

Part 1



Inverter-JC 218a-E : Circuit Diagram

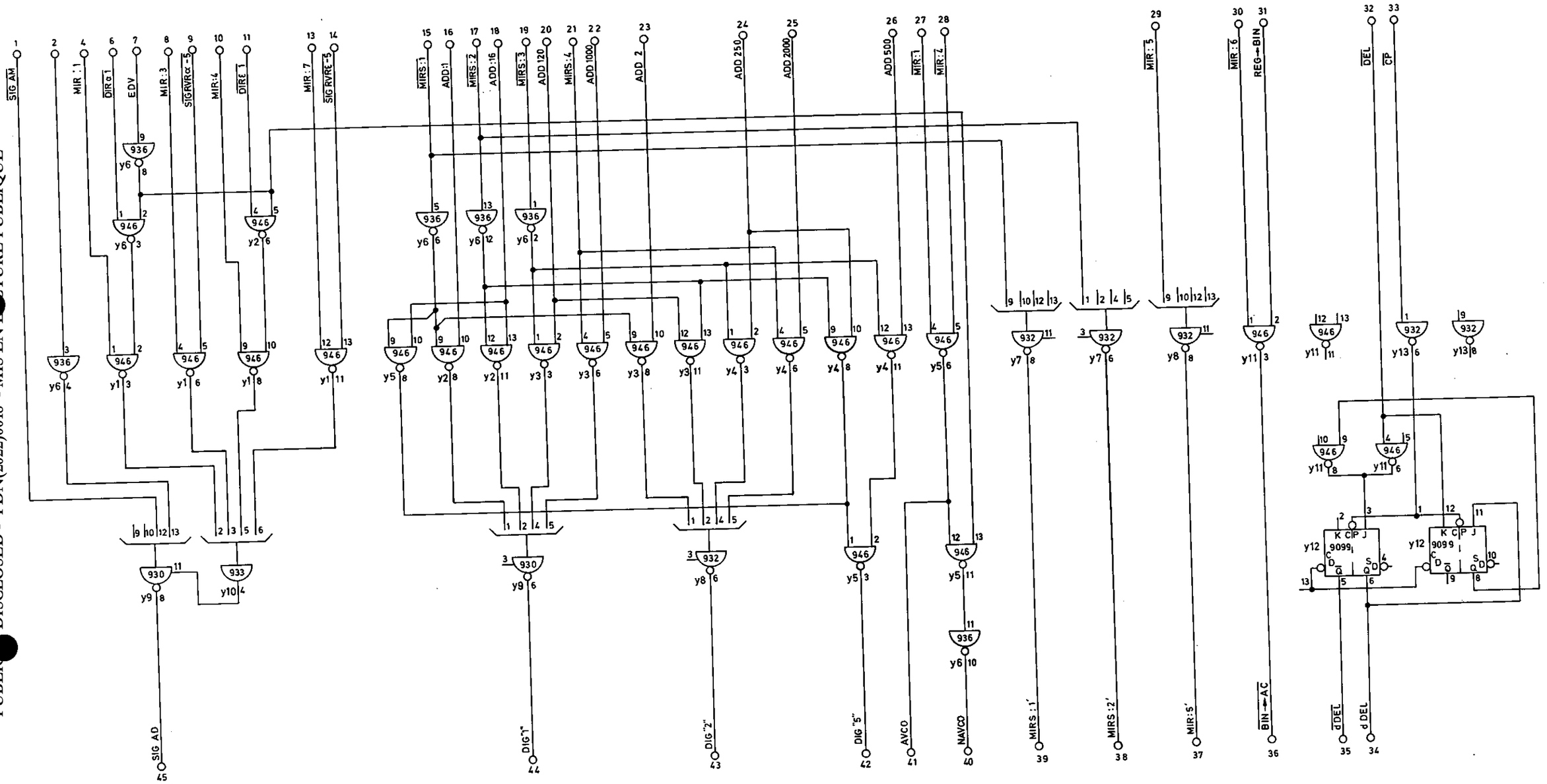
Figure 12-73 Card: INVα Location u0405
INVε u0505
Part – 1 INV u0618



Addend Data Output –JC 218a–E : Circuit Diagram

Figure 12–74 Card: ADT Location u0614

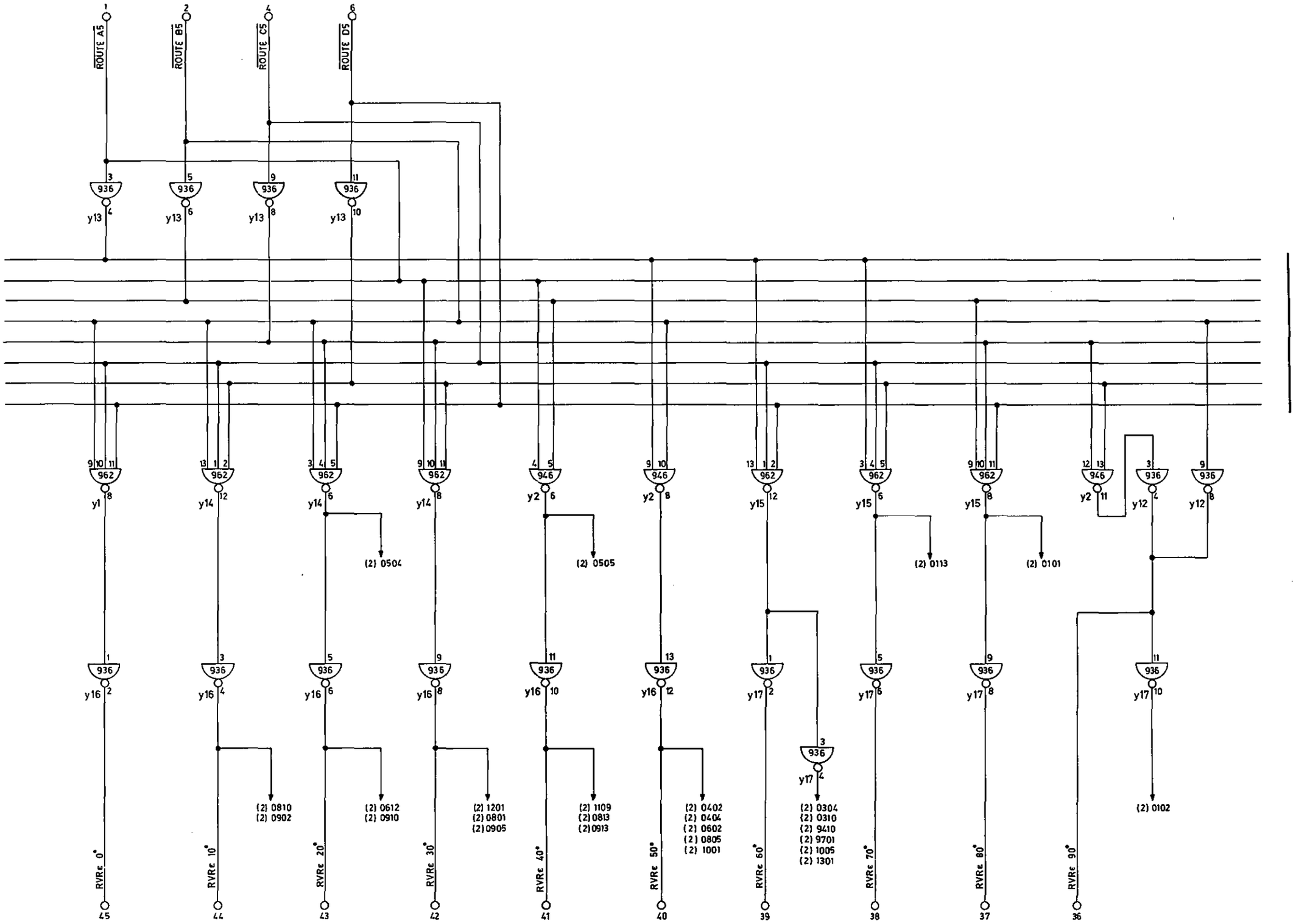
Part 1



Addend Control – JC218a – E : Circuit Diagram

Figure 12–75 Card : ADC Location u0610

Part 1

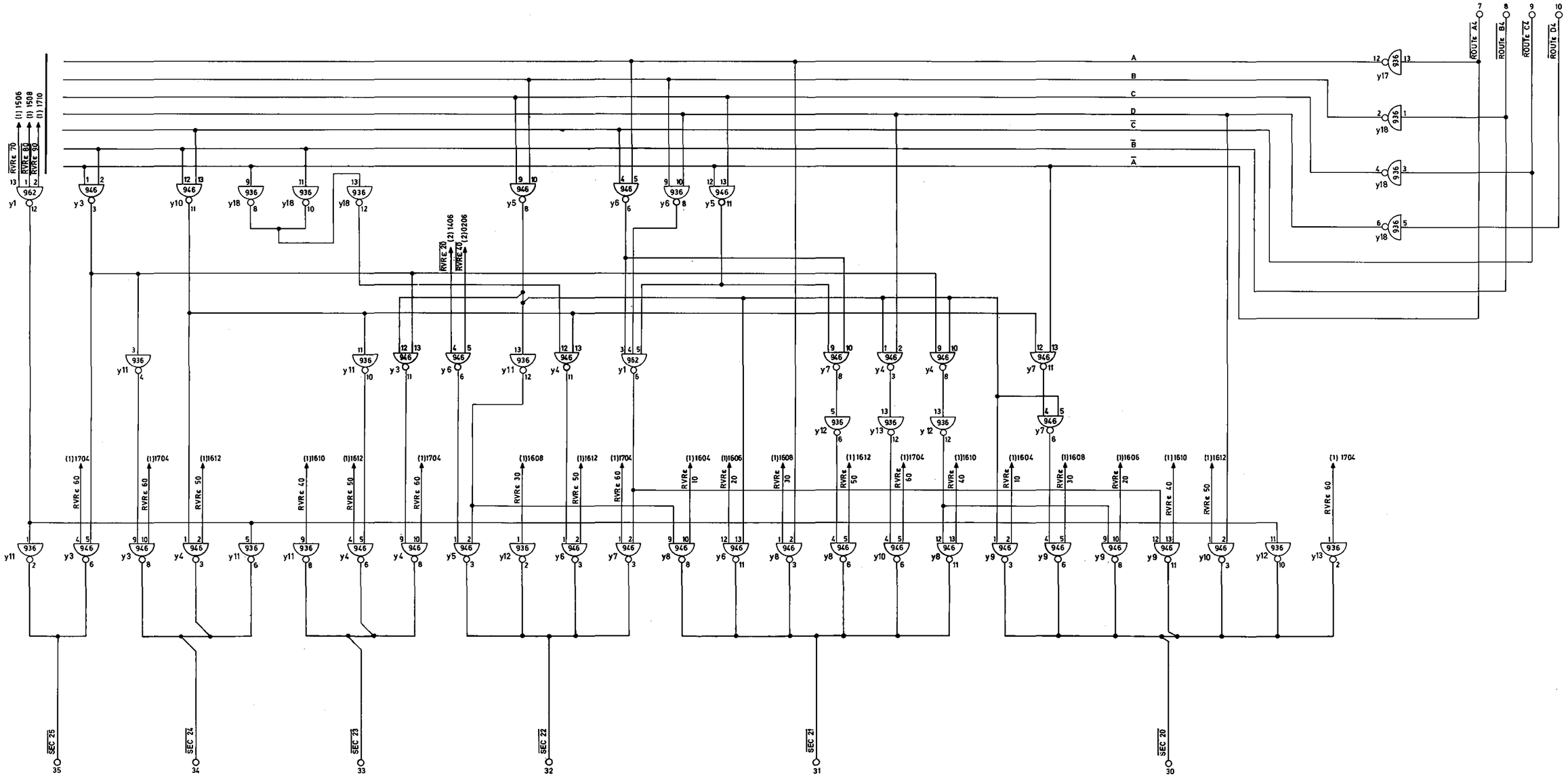


Secant Decoder –JC 218a - E : Circuit Diagram

Figure 12–76 Sheet 1 Card: SEC Location u0612

Part 1

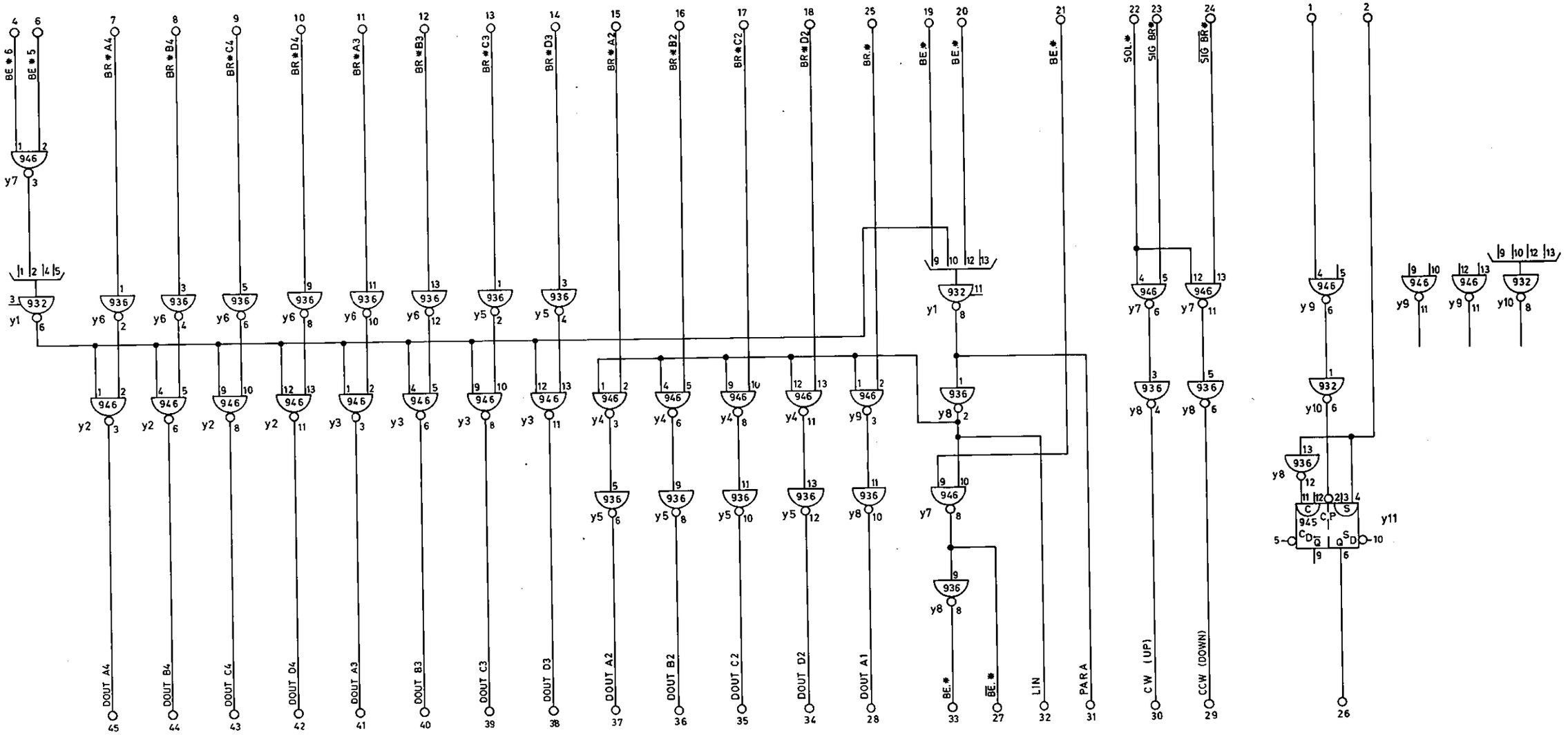
Iss. 1



Secant Decoder-JC218a-E : Circuit Diagram

Figure 12-76 Sheet 2 Card : SEC Location u0612

Part 1

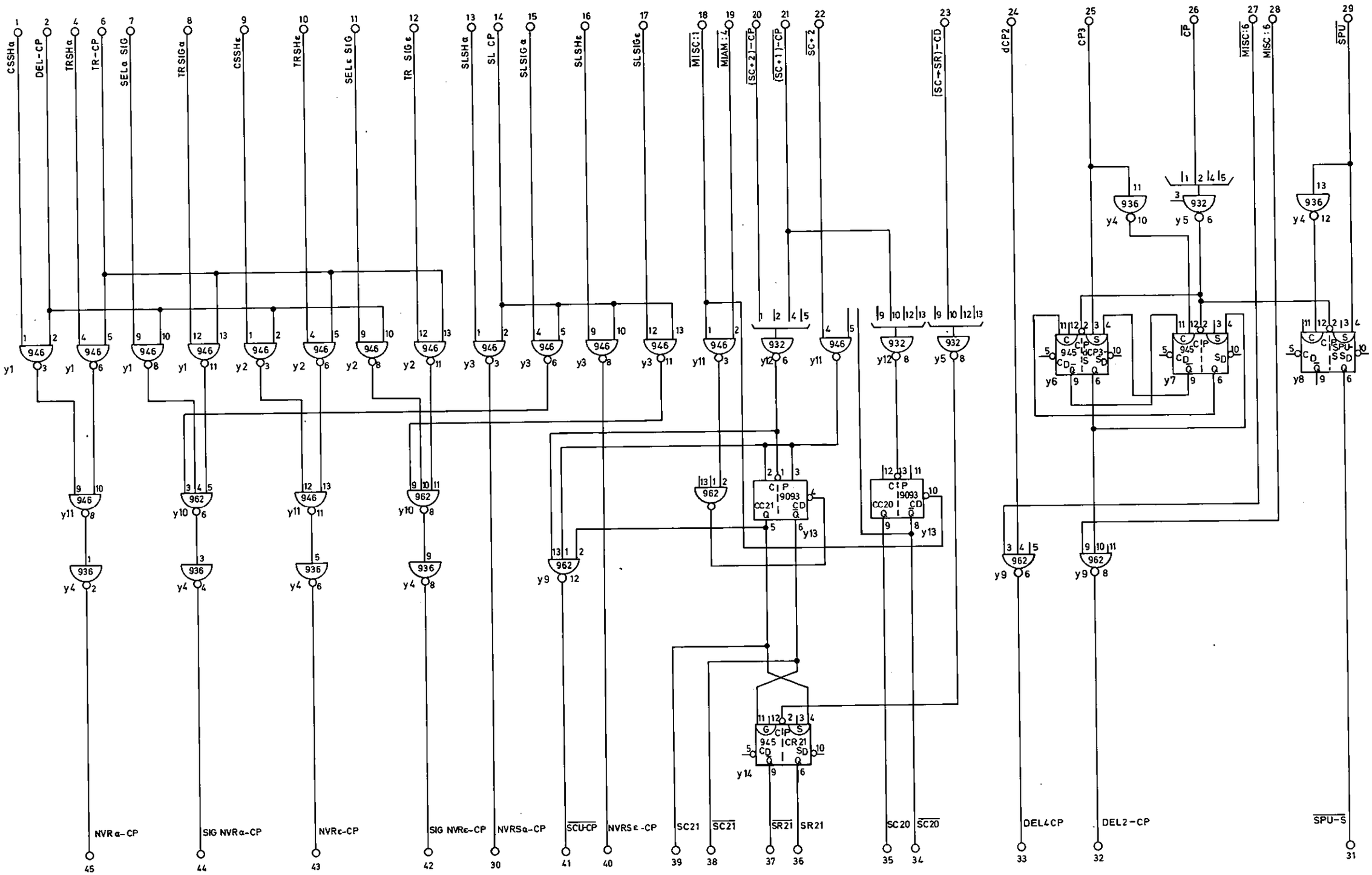


NOTE WHERE * REPRESENTS α OR ε
DEPENDING UPON LOCATION.

Difference Output and Controls - JC218a-E : Circuit Diagram

Figure 12-77 Card: DOUT α Location u0412
DOUT ε u0512

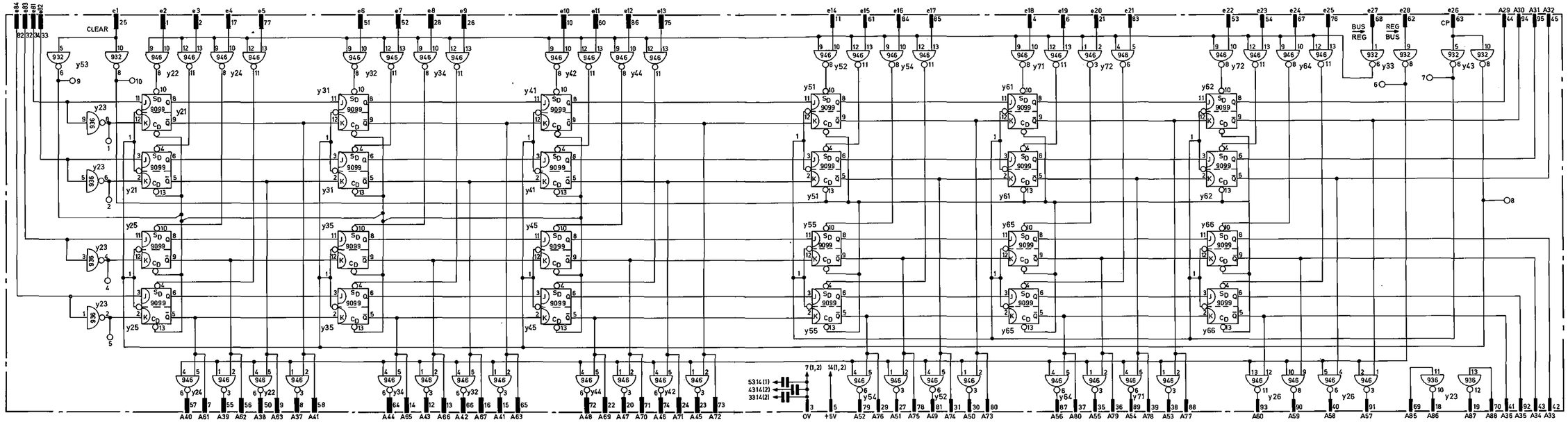
Part 1



Register Control II–JC 218a-E : Circuit Diagram

Figure 12-78 Card RC II Location u0605

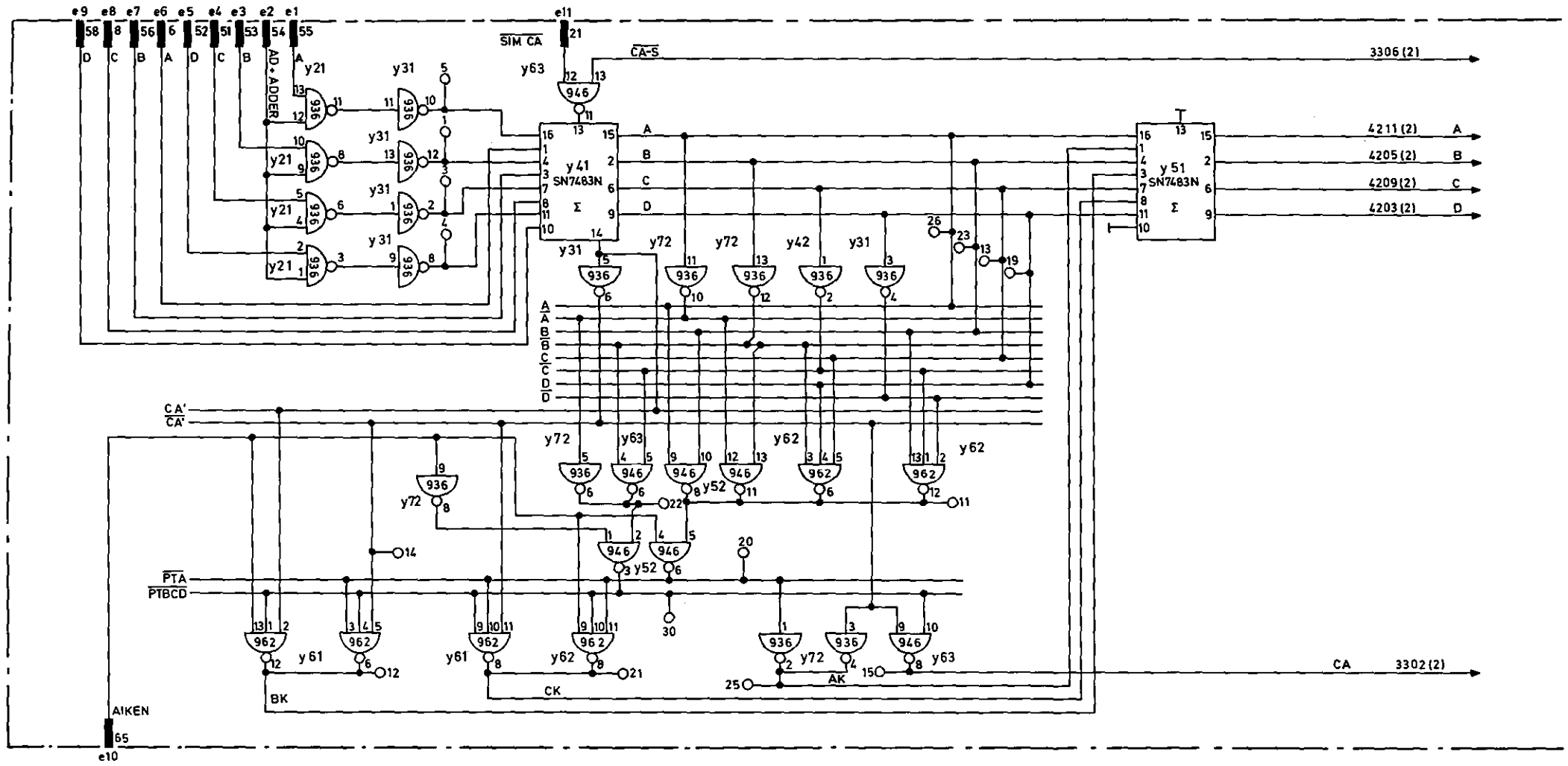
Part 1



Storage Register -JC244a : Circuit Diagram

Figure 12-79 Locations See Figure 02-06

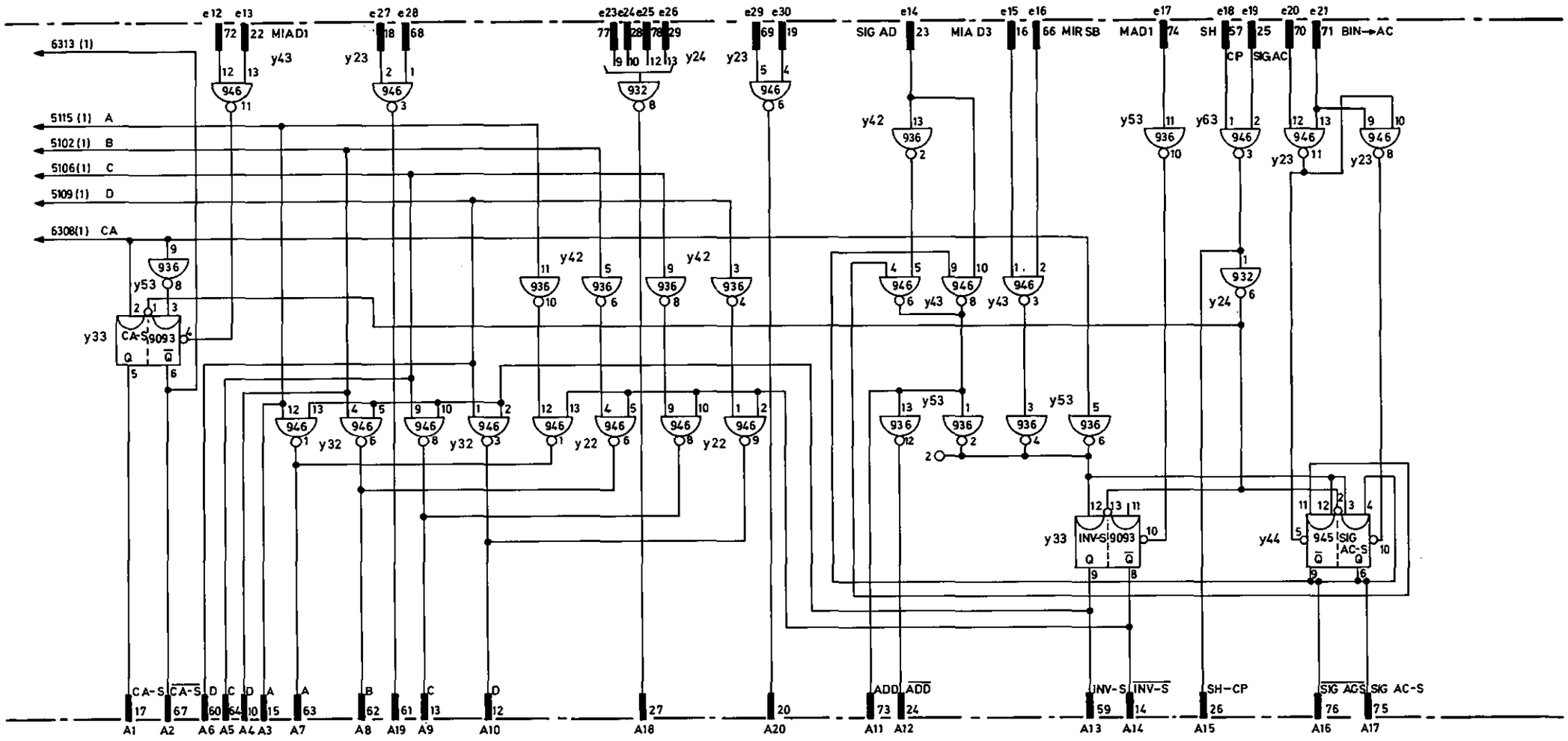
Part 1



Adder Card-JC 245a-R1: Circuit Diagram

Figure 12-80 Sheet 1 Card : ADD Location u0608

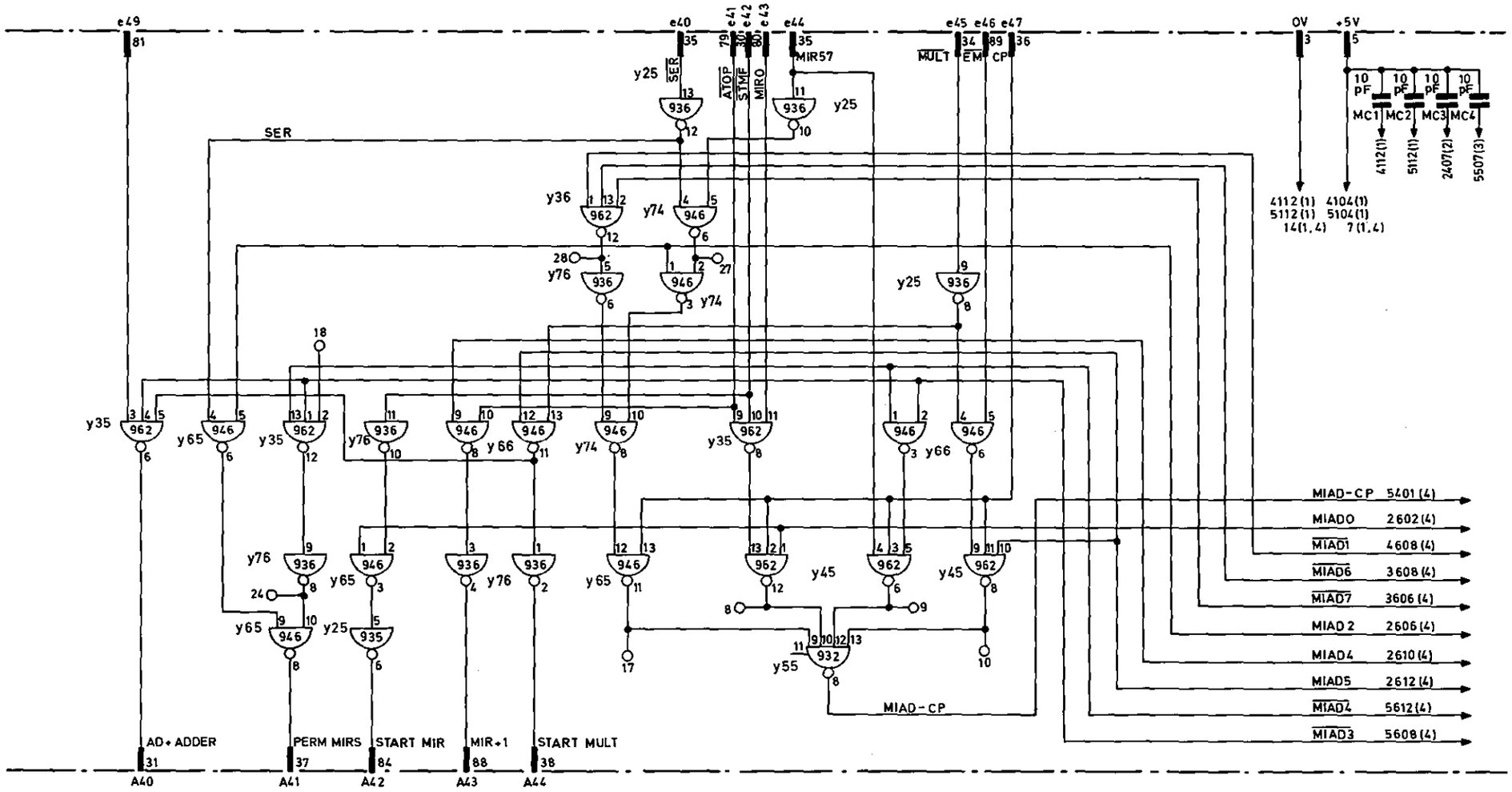
Part 1



Adder Card-JC245a-R1 : Circuit Diagram

Figure 12-80 Sheet 2 Card: ADD Location u0608

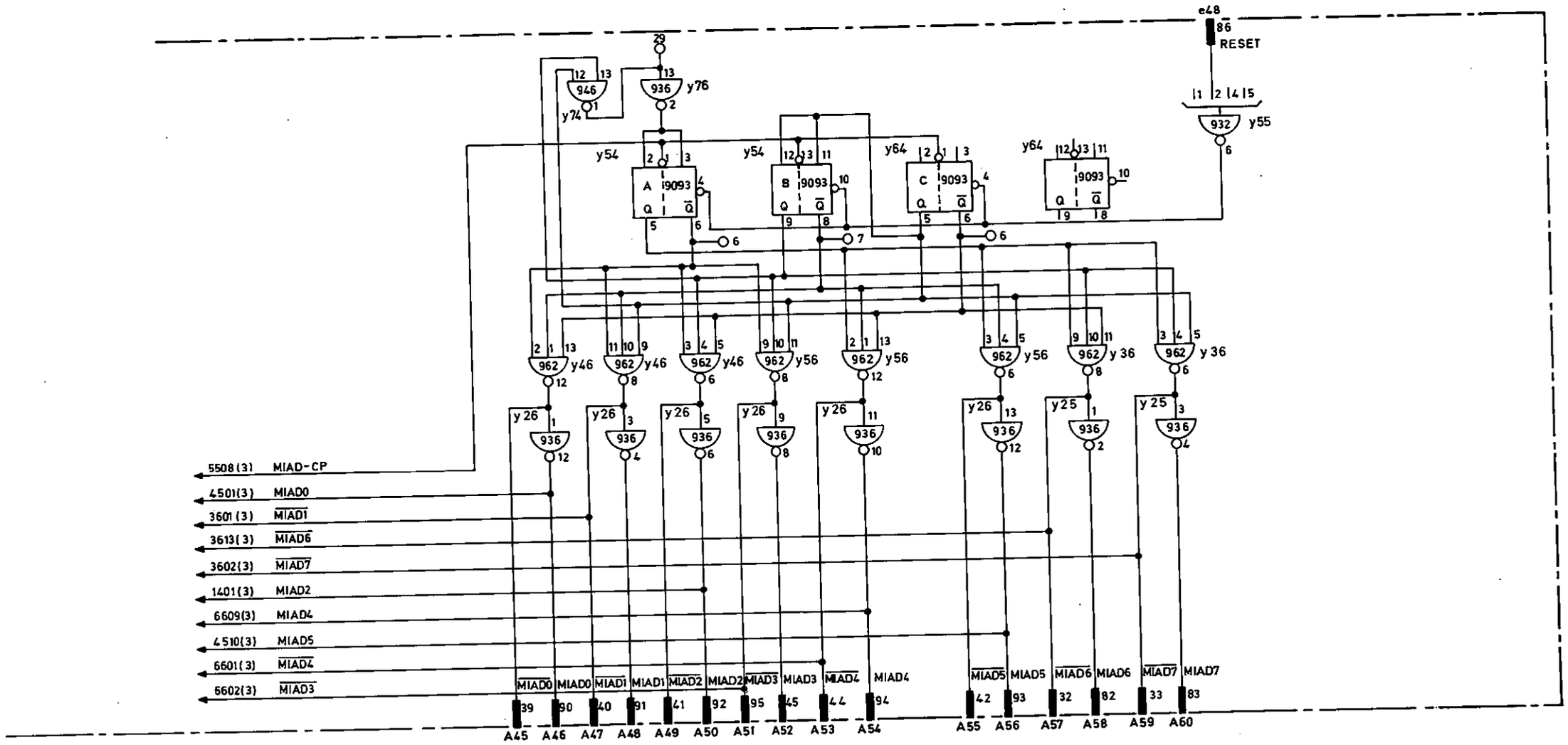
Part 1



Adder Card-JC 245a-R1: Circuit Diagram

Figure 12-80 Sheet 3 Card: ADD Location u0608

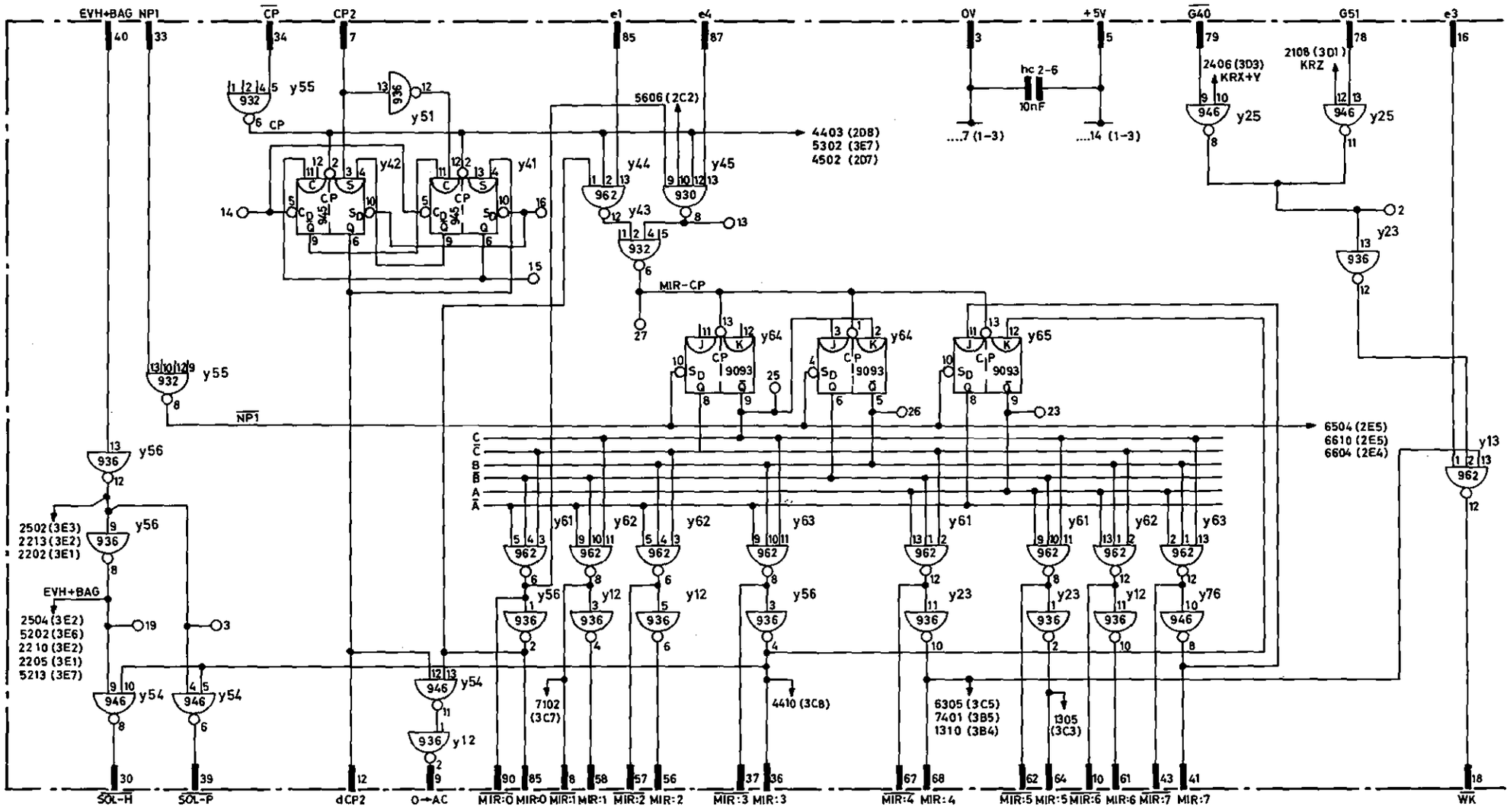
Part 1



Adder Card–JC 245a–R1: Circuit Diagram

Figure 12-80 Sheet 4 Card: ADD Location u0608

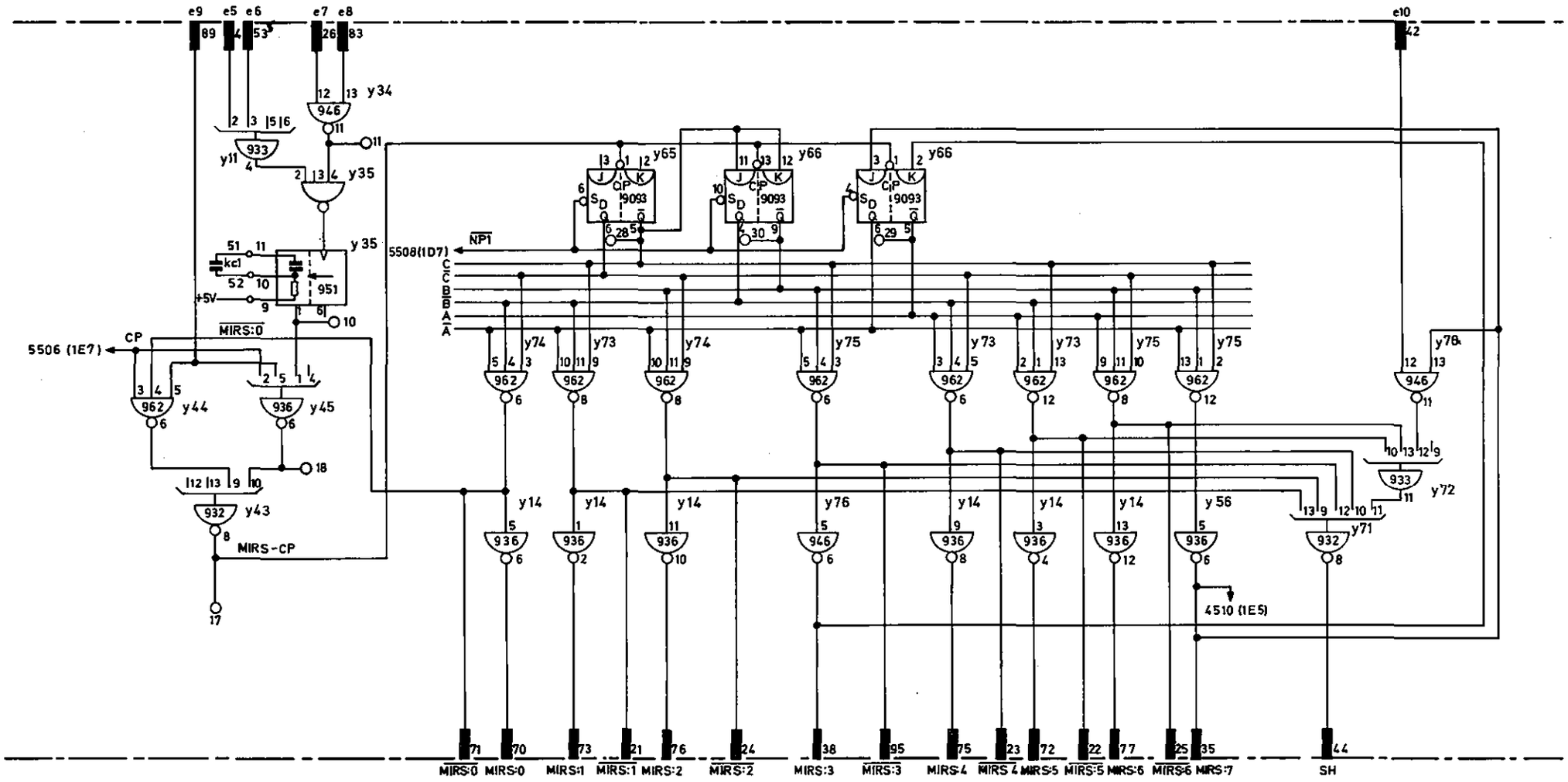
Part 1



Sequence Control Card-JC246a-E: Circuit Diagram

Figure 12-81 Sheet 1 Card. CRC. Location u 0607

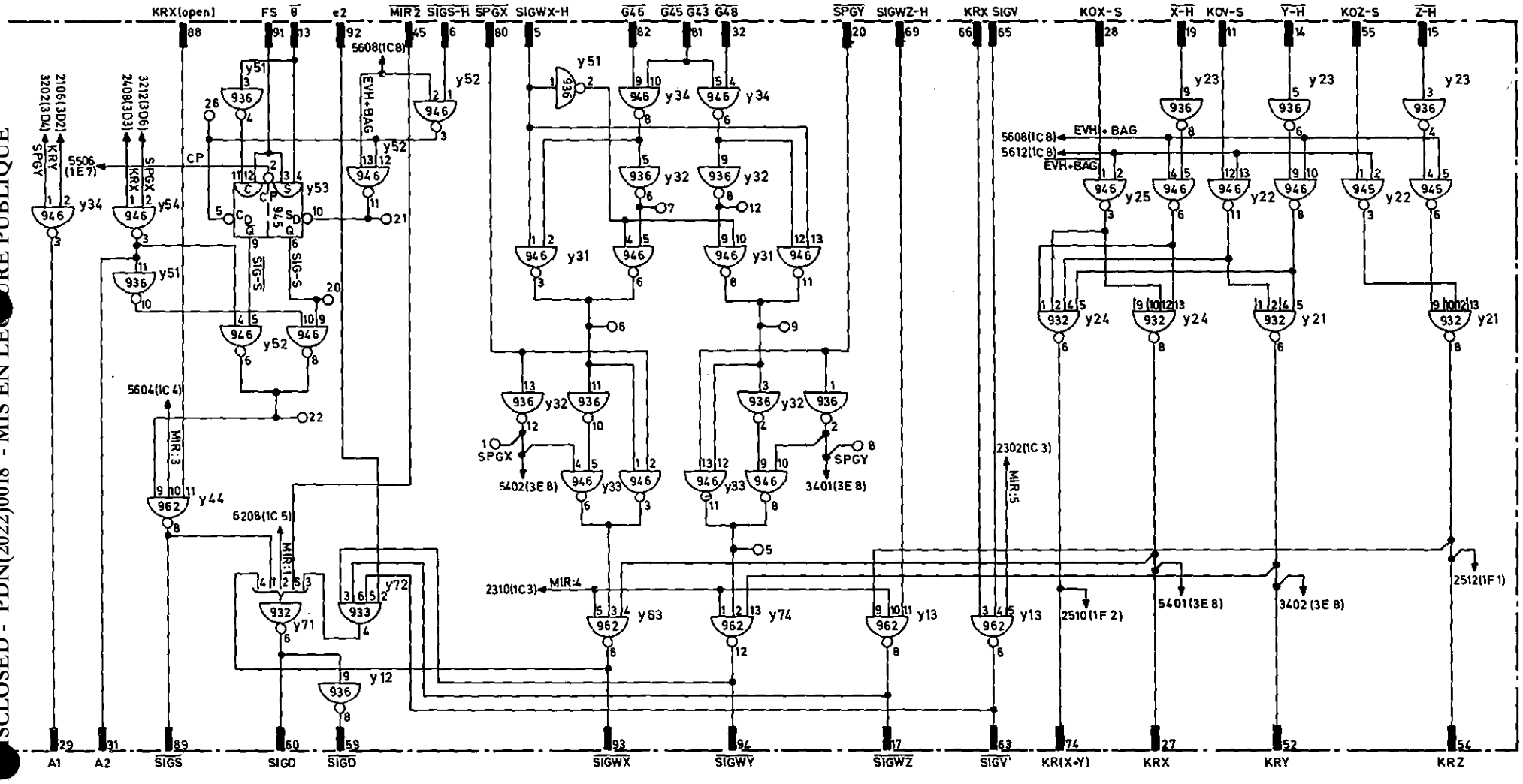
Part 1



Sequence Control Card- JC246a-E:Circuit Diagram

Figure 12-81 Sheet 2 Card CRC Location u0607

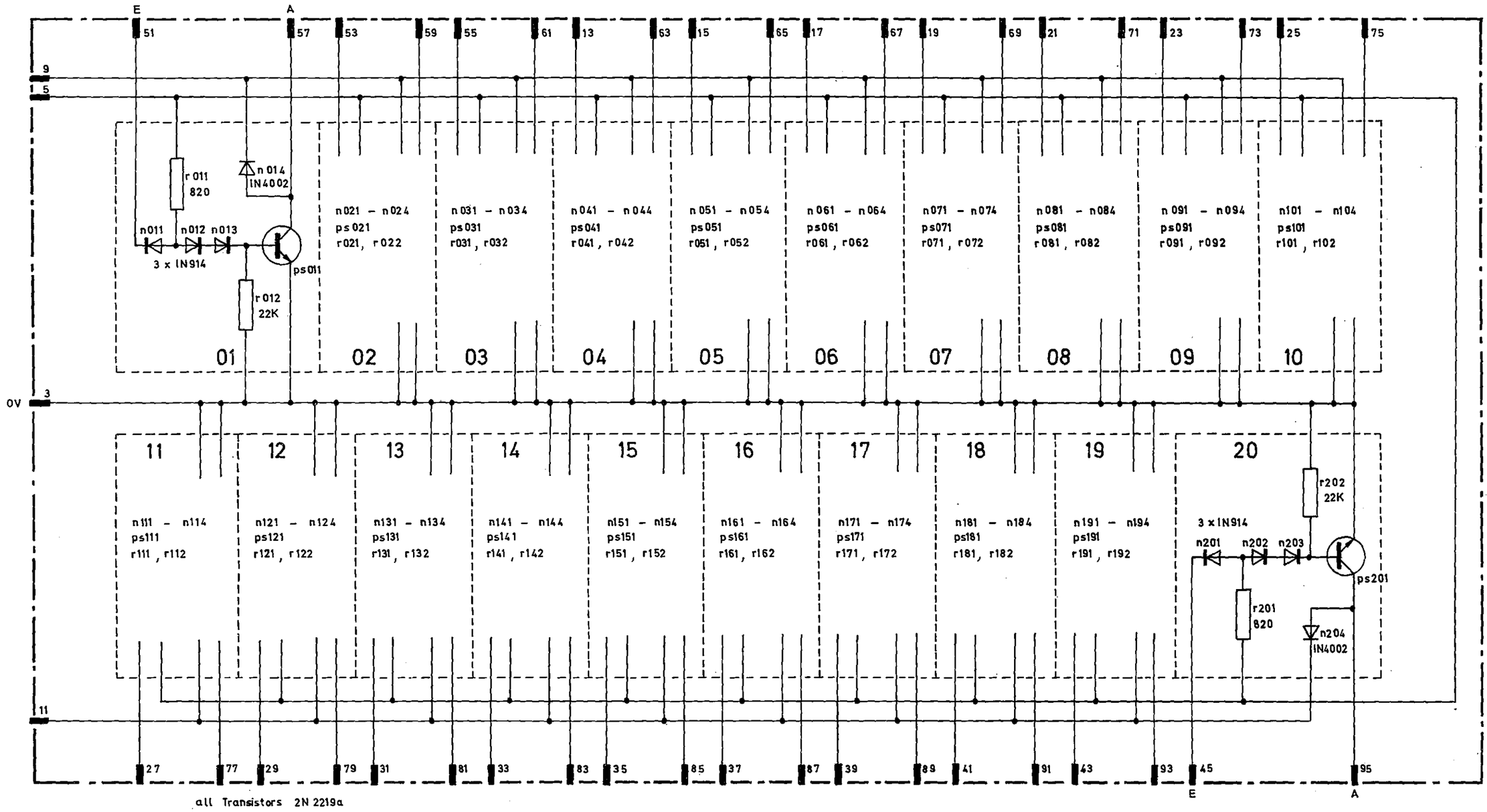
Part 1



Sequence Control Card -JC246a-E:Circuit Diagram

Figure 12-81 Sheet 3 Card. CRC Location u0607

Part 1

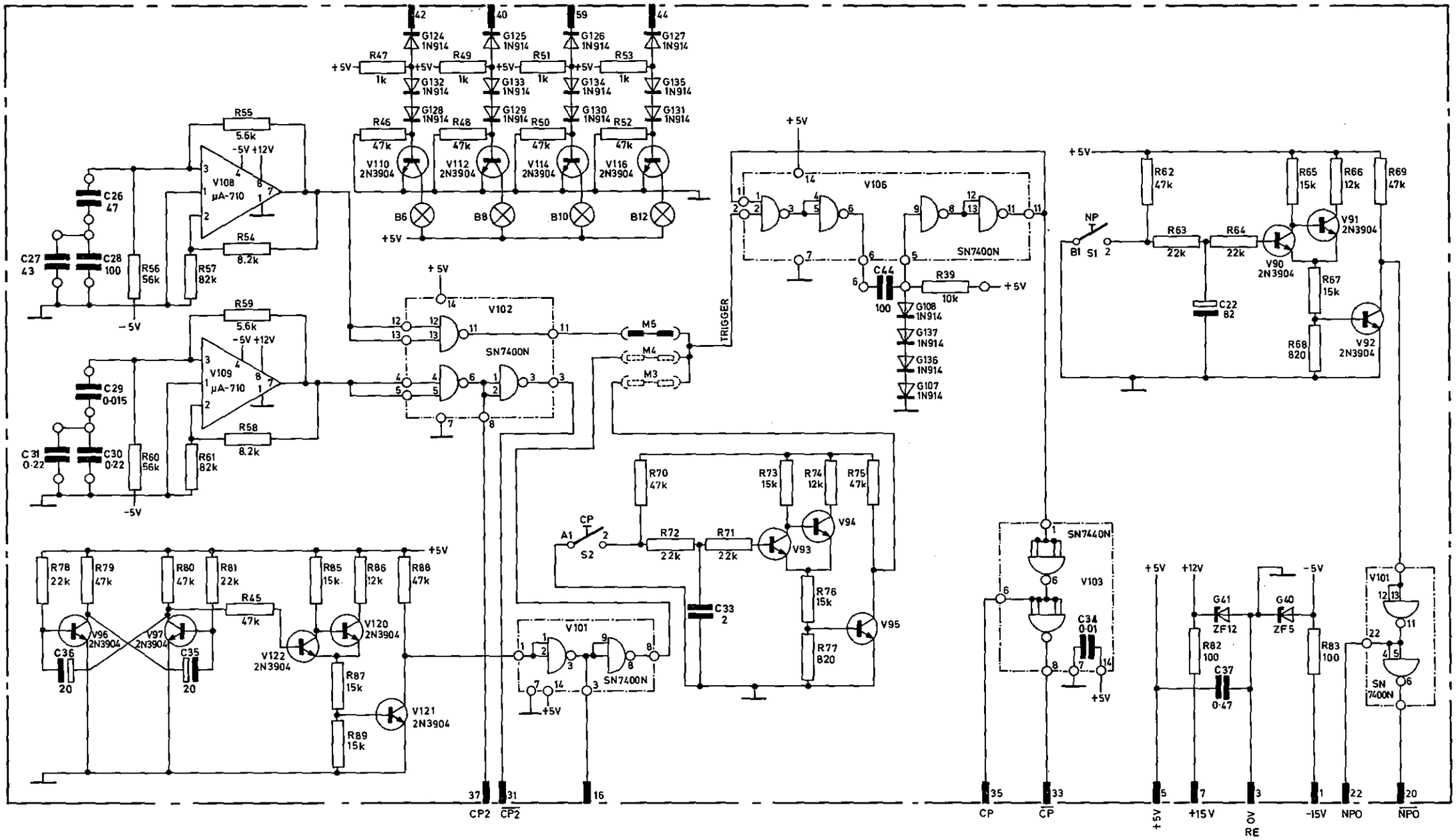


all Transistors 2N 2219a

LT 234a-E : Circuit Diagram

Figure 12-82 Locations See Figure 02-06

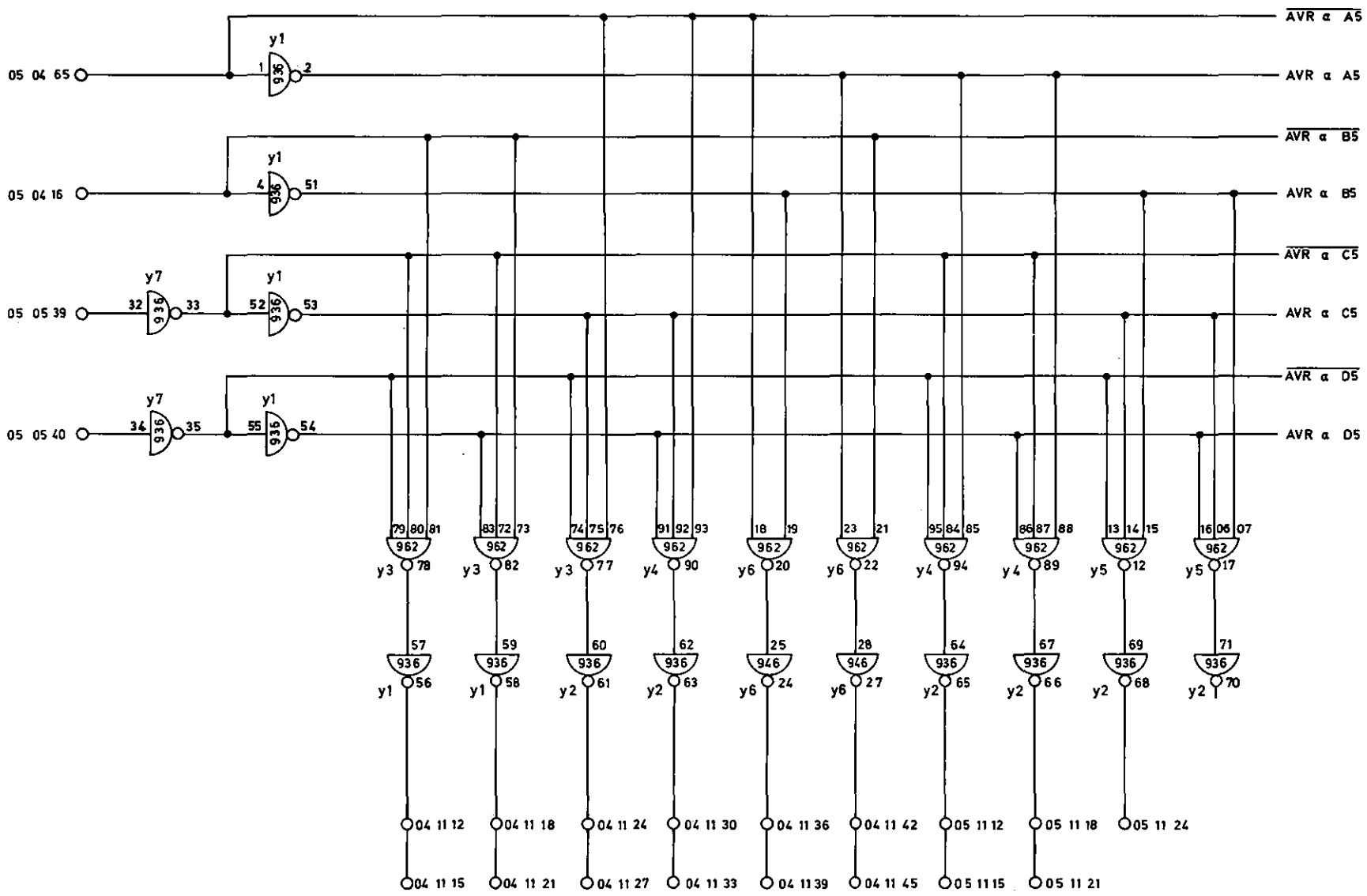
Part 1



Clock-Pulse Generator –ZT230a–E : Circuit Diagram

Figure 12–83 Card CP. Location u 0604

Part 1



Decimal Decoder for Weight Correction: Circuit Diagram

Figure 12-84 Card WC Location u0619

Part 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			53	BOUT A1	BIN A1	91			
			54	BOUT B1	BIN B1	40			
			67	BOUT C1	BIN C1	90			
			76	BOUT D1	BIN D1	93			
			4	BOUT A2	BIN A2	38			
			6	BOUT B2	BIN B2	39			
			21	BOUT C2	BIN C2	35			
			83	BOUT D2	BIN D2	87			
			11	BOUT A3	BIN A3	30			
			61	BOUT B3	BIN B3	81			
			84	BOUT C3	BIN C3	27			
			85	BOUT D3	BIN D3	97			
			10	BOUT A4	BIN A4	23			
			60	BOUT B4	BIN B4	74			
			86	BOUT C4	BIN C4	20			
			75	BOUT D4	BIN D4	72			
			51	BOUT A5	BIN A5	15			
			52	BOUT B5	BIN B5	66			
			28	BOUT C5	BIN C5	12			
			26	BOUT D5	BIN D5	64			
			1	BOUT A6	BIN A6	8			
			2	BOUT B6	BIN B6	59			
			17	BOUT C6	BIN C6	55			
			77	BOUT D6	BIN D6	57			
		18	25	CLEAR AVRα	AVRα A1	44	04	65	09
			63		$\overline{\text{AVRα A1}}$	94	04	65	08
06	06	29	68	$\overline{\text{BOUT→AVRα}}$	AVRα B1	95	04	65	19
06	06	43	62	$\overline{\text{AVRα→BIN}}$	$\overline{\text{AVRα B1}}$	45	04	65	18
06	15	44	70	GR	AVRα C1	42	04	65	29
		19	69	$\overline{\text{CLEAR AVRα}}$	$\overline{\text{AVRα C1}}$	43	04	65	28
			34		AVRα D1	92	04	65	39
			33		$\overline{\text{AVRα D1}}$	41	04	65	38
			32		$\overline{\text{AVRα A2}}$	88	04	05	02
							04	65	06
			82		$\overline{\text{AVRα B2}}$	39	04	05	04
							04	65	16
					$\overline{\text{AVRα C2}}$	36	04	05	06
							04	65	26
					$\overline{\text{AVRα D2}}$	37	04	05	07
							04	65	36
					$\overline{\text{AVRα A3}}$	80	04	05	08
							04	65	04
					$\overline{\text{AVRα B3}}$	31	04	05	09
							04	65	14
					$\overline{\text{AVRα C3}}$	78	04	05	10
							04	65	24

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	04	+5V : PIN 5 GND : PIN 3 BIN,BOUT SEE0609
TYPE JC244	ACTUAL VALUE REGISTER, AVRα SHEET 1	GROUP 12	SHEET 93	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				$\overline{\text{AVR}\alpha\ \text{D3}}$	29	04	05	11
						04	65	34
				$\overline{\text{AVR}\alpha\ \text{A4}}$	73	04	05	12
						04	65	02
				$\overline{\text{AVR}\alpha\ \text{B4}}$	24	04	05	13
						04	65	12
				$\overline{\text{AVR}\alpha\ \text{C4}}$	71	04	05	14
						04	65	22
				$\overline{\text{AVR}\alpha\ \text{D4}}$	22	04	05	15
						04	65	32
				$\overline{\text{AVR}\alpha\ \text{A5}}$	65	04	05	16
						04	65	00
				$\overline{\text{AVR}\alpha\ \text{B5}}$	16	04	05	17
						04	65	10
				$\overline{\text{AVR}\alpha\ \text{C5}}$	13	04	05	18
						04	65	20
				$\overline{\text{AVR}\alpha\ \text{D5}}$	14	04	05	19
						04	65	30
				$\overline{\text{AVR}\alpha\ \text{A6}}$	58	04	05	20
						04	64	00
				$\overline{\text{AVR}\alpha\ \text{B6}}$	9	04	05	21
						04	64	10
				$\overline{\text{AVR}\alpha\ \text{C6}}$	56	04	05	22
						04	64	20
				$\overline{\text{AVR}\alpha\ \text{D6}}$	7	04	05	23
						04	64	30
				CLEAR AVRα	18	04	04	25
				$\overline{\text{CLEAR AVR}\alpha}$	19	06	06	35
						04	04	69
						04	09	09

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	04	+5V : PIN 5 GND : PIN 3 BIN,BOUT SEE(0609)
TYPE		GROUP	SHEET	
JC244	ACTUAL VALUE REGISTER, AVRα SHEET 2	12	93	
		PART 1		

Iss.1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	04	88	2	$\overline{\text{AVR}\alpha\ \text{A2}}$	AVRα A2	25	04	65	07
04	04	39	4	$\overline{\text{AVR}\alpha\ \text{B2}}$	AVRα B2	26	04	65	17
04	04	36	6	$\overline{\text{AVR}\alpha\ \text{C2}}$	AVRα C2	27	04	65	27
04	04	37	7	$\overline{\text{AVR}\alpha\ \text{D2}}$	AVRα D2	28	04	65	37
04	04	80	8	$\overline{\text{AVR}\alpha\ \text{A3}}$	AVRα A3	29	04	65	05
04	04	31	9	$\overline{\text{AVR}\alpha\ \text{B3}}$	AVRα B3	30	04	65	15
04	04	78	10	$\overline{\text{AVR}\alpha\ \text{C3}}$	AVRα C3	31	04	65	25
04	04	29	11	$\overline{\text{AVR}\alpha\ \text{D3}}$	AVRα D3	32	04	65	35
04	04	73	12	$\overline{\text{AVR}\alpha\ \text{A4}}$	AVRα A4	33	04	65	03
04	04	24	13	$\overline{\text{AVR}\alpha\ \text{B4}}$	AVRα B4	34	04	65	13
04	04	71	14	$\overline{\text{AVR}\alpha\ \text{C4}}$	AVRα C4	35	04	65	23
04	04	22	15	$\overline{\text{AVR}\alpha\ \text{D4}}$	AVRα D4	36	04	65	33
04	04	65	16	$\overline{\text{AVR}\alpha\ \text{A5}}$	AVRα A5	37	04	65	01
04	04	16	17	$\overline{\text{AVR}\alpha\ \text{B5}}$	AVRα B5	38	04	65	11
04	04	13	18	$\overline{\text{AVR}\alpha\ \text{C5}}$	AVRα C5	39	04	65	21
04	04	14	19	$\overline{\text{AVR}\alpha\ \text{D5}}$	AVRα D5	40	04	65	31
04	04	58	20	$\overline{\text{AVR}\alpha\ \text{A6}}$	AVRα A6	41	04	64	01
04	04	09	21	$\overline{\text{AVR}\alpha\ \text{B6}}$	AVRα B6	42	04	64	11
04	04	56	22	$\overline{\text{AVR}\alpha\ \text{C6}}$	AVRα C6	43	04	64	21
04	04	07	23	$\overline{\text{AVR}\alpha\ \text{D6}}$	AVRα D6	44	04	64	31
			24			45			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	05	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC218	INVERTER, INVα	12	94	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			53	BOUT A6	BIN A6	91			
			54	BOUT B6	BIN B6	40			
			67	BOUT C6	BIN C6	90			
			76	BOUT D6	BIN D6	93			
			4	BOUT A5	BIN A5	38			
			6	BOUT B5	BIN B5	89			
			21	BOUT C5	BIN C5	35			
			83	BOUT D5	BIN D5	87			
			11	BOUT A4	BIN A4	30			
			61	BOUT B4	BIN B4	81			
			84	BOUT C4	BIN C4	27			
			85	BOUT D4	BIN D4	79			
			10	BOUT A3	BIN A3	23			
			60	BOUT B3	BIN B3	74			
			86	BOUT C3	BIN C3	20			
			75	BOUT D3	BIN D3	73			
			51	BOUT A2	BIN A2	15			
			52	BOUT B2	BIN B2	66			
			28	BOUT C2	BIN C2	12			
			26	BOUT D2	BIN D2	64			
			1	BOUT A1	BIN A1	8			
			2	BOUT B1	BIN B1	59			
			17	BOUT C1	BIN C1	55			
			77	BOUT D1	BIN D1	57			
		18	25	CLEAR NVRα	NVRα A6	44			
		45	63	$\overline{\text{NVRα–CP}}$	$\overline{\text{NVRα A6}}$	94			
06	06	24	68	$\overline{\text{BOUT→NVRα}}$	$\overline{\text{NVRα B6}}$	95			
06	06	42	62	$\overline{\text{NVRα→BIN}}$	$\overline{\text{NVRα B6}}$	45			
06	15	44	70	GR	NVRα C6	42			
		19	69	$\overline{\text{CLEAR NVRα}}$	$\overline{\text{NVRα C6}}$	43			
06	17	34	34	ADIN A	$\overline{\text{NVRα D6}}$	92			
06	17	33	33	ADIN B	$\overline{\text{NVRα D6}}$	41			
06	17	32	32	ADIN C	$\overline{\text{NVRα A5}}$	88			
06	17	37	82	ADIN D	$\overline{\text{NVRα B5}}$	39			
					$\overline{\text{NVRα C5}}$	36			
					$\overline{\text{NVRα D5}}$	37			
					$\overline{\text{NVRα A4}}$	80			
					$\overline{\text{NVRα B4}}$	31			
					$\overline{\text{NVRα C4}}$	78			
					$\overline{\text{NVRα D4}}$	29			
					$\overline{\text{NVRα A3}}$	73			
					$\overline{\text{NVRα B3}}$	24			
					$\overline{\text{NVRα C3}}$	71			
					$\overline{\text{NVRα D3}}$	22			
					$\overline{\text{NVRα A2}}$	65			
					$\overline{\text{NVRα B2}}$	16			
					$\overline{\text{NVRα C2}}$	13			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	06	+5V : PIN 5 GND : PIN 3
TYPE	NOMINAL VALUE REGISTER, NVRα	GROUP	SHEET	
JC244	SHEET 1	12	95	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				$\overline{\text{NVR}\alpha\ \text{D2}}$	14			
				$\overline{\text{NVR}\alpha\ \text{A1}}$	58			
				$\overline{\text{NVR}\alpha\ \text{B1}}$	9			
				$\overline{\text{NVR}\alpha\ \text{C1}}$	56			
				$\overline{\text{NVR}\alpha\ \text{D1}}$	7			
				CLEAR NVRα	18	04	06	25
				$\overline{\text{CLEAR NVR}\alpha}$	19	06	06	31
						04	06	69

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	06	+5V : PIN 5 GND : PIN 3
TYPE	NOMINAL VALUE REGISTER, NVRα SHEET 2	GROUP	SHEET	
JC244		12	95	
		PART 1		

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			53	BOUT A1	BOUT A1	91			
			54	BOUT B1	BOUT B1	40			
			67	BOUT C1	BOUT C1	90			
			76	BOUT D1	BOUT D1	93			
			4	BOUT A2	BOUT A2	38			
			6	BOUT B2	BOUT B2	89			
			21	BOUT C2	BOUT C2	35			
			83	BOUT D2	BOUT D2	87			
			11	BOUT A3	BOUT A3	30			
			61	BOUT B3	BOUT B3	81			
			84	BOUT C3	BOUT C3	27			
			85	BOUT D3	BOUT D3	79			
			10	BOUT A4	BOUT A4	23			
			60	BOUT B4	BOUT B4	74			
			86	BOUT C4	BOUT C4	20			
			75	BOUT D4	BOUT D4	72			
			51	BOUT A5	BOUT A5	15			
			52	BOUT B5	BOUT B5	66			
			28	BOUT C5	BOUT C5	12			
			26	BOUT D5	BOUT D5	64			
			1	BOUT A6	BOUT A6	8			
			2	BOUT B6	BOUT B6	59			
			17	BOUT C6	BOUT C6	55			
			77	BOUT D6	BOUT D6	57			
04	07	18	25	CLEAR RVRα	RVRα A1	44	04	07	34
							06	14	02
06	06	39	63	$\overline{\text{RVRα→CP}}$	$\overline{\text{ROUTα A1}}$	94			
06	06	27	68	$\overline{\text{BOUT→RVRα}}$	RVRα B1	95	04	07	33
							06	14	04
06	06	23	62	$\overline{\text{RVRα→BOUT}}$	$\overline{\text{ROUTα B1}}$	45			
06	15	44	70	GR	RVRα C1	42	04	07	32
							06	14	06
04	07	19	69	$\overline{\text{CLEAR RVRα}}$	$\overline{\text{ROUTα C1}}$	43			
04	07	44	34	RVRα A1	RVRα D1	92	04	07	82
							06	14	07
04	07	95	33	RVRα B1	$\overline{\text{ROUTα D1}}$	41			
04	07	42	32	RVRα C1	$\overline{\text{ROUTα A2}}$	88	04	20	51
04	07	92	82	RVRα D1	$\overline{\text{ROUTα B2}}$	39	04	20	53
					$\overline{\text{ROUTα C2}}$	36	04	20	55
					$\overline{\text{ROUTα D2}}$	37	04	20	13
					$\overline{\text{ROUTα A3}}$	80	04	20	15
					$\overline{\text{ROUTα B3}}$	31	04	20	17
					$\overline{\text{ROUTα C3}}$	78	04	20	19
					$\overline{\text{ROUTα D3}}$	29	04	20	21

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	07	BIN/BOUT SEE 0609 +5V : PIN 5 GND : PIN 3
TYPE	READOUT REGISTER, RVRα	GROUP	SHEET	
JC244	SHEET 1	12	96	
		PART 1		

Iss.1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				$\overline{\text{ROUTα A4}}$	73	04	20	23
				$\overline{\text{ROUTα B4}}$	24	04	20	25
				$\overline{\text{ROUTα C4}}$	71	04	20	27
				$\overline{\text{ROUTα D4}}$	22	04	20	29
				$\overline{\text{ROUTα A5}}$	65	04	20	31
				$\overline{\text{ROUTα B5}}$	16	04	20	33
				$\overline{\text{ROUTα C5}}$	13	04	20	35
				$\overline{\text{ROUTα D5}}$	14	04	20	37
				$\overline{\text{ROUTα A6}}$	58	04	20	39
				$\overline{\text{ROUTα B6}}$	9	04	20	41
				$\overline{\text{ROUTα C6}}$	56	04	20	43
				$\overline{\text{ROUTα D6}}$	7	04	20	45
				CLEAR RVRα	18	04	07	25
				$\overline{\text{CLEAR RVRα}}$	19	06	06	33
						04	07	69

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	07	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC244	READOUT REGISTER, RVRα SHEET 2	12	96	
		PART 1		

Iss.1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			53		BEα1	91			
			54		BEα1	40			
			67		BEα1	90			
			76		BEα1	93			
			4		BEα2	38			
			6		BEα2	89	04	12	21
			21		BEα2	35			
			83		BEα2	87			
			11		BEα3	30			
			61		BEα3	81	04	12	20
			84		BEα3	27			
			85		BEα3	79			
			10		BEα4	23			
			60		BEα4	74	04	12	19
			86		BEα4	20			
			75		BEα4	72			
			51		BEα5	15			
			52		BEα5	66	04	12	06
			28		BEα5	12			
			26		BEα5	64			
			1		BEα6	8			
			2		BEα6	59	04	12	04
			17		BEα6	55			
			77		BEα6	57			
			25	GND	$\overline{\text{BRα A1}}$	44			
06	06	37	63	$\overline{\text{BRα-CP}}$	$\overline{\text{BRα A1}}$	94	04	12	25
04	10	25	68	$\overline{\text{CLEAR BRα}}$	$\overline{\text{BRα B1}}$	95			
			62	GND	$\overline{\text{BRα B1}}$	45			
			70		$\overline{\text{BRα C1}}$	42			
			69		$\overline{\text{BRα C1}}$	43			
06	16	44	34	$\overline{\text{SUMA}}$	$\overline{\text{BRα D1}}$	92			
06	16	43	33	$\overline{\text{SUMB}}$	BRα D1	41			
06	16	42	32	$\overline{\text{SUMC}}$	BRα A2	88	04	12	15
06	16	41	82	$\overline{\text{SUMD}}$	BRα B2	39	04	12	16
					BRα C2	36	04	12	17
					BRα D2	37	04	12	18
					BRα A3	80	04	12	11
					BRα B3	31	04	12	12
					BRα C3	78	04	12	13
					BRα D3	29	04	12	14
					BRα A4	73	04	12	07
					BRα B4	24	04	12	08
					BRα C4	71	04	12	09
					BRα D4	22	04	12	10
					BRα A5	65			
					BRα B5	16			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	08	+5V : PIN 5 GND : PIN 3
TYPE JC244	BUFFER REGISTER, BRα SHEET 1	GROUP 12	SHEET 97	
		PART 1		

Iss.1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
					BRα C5	13			
					BRα D5	14			
					BRα A6	58			
					BRα B6	9			
					BRα C6	56			
					BRα D6	7			
						18			
						19			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	08	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC244	BUFFER REGISTER, BRα SHEET 2	12	97	
		PART 1		

Iss.1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	06	42	1	$\overline{\text{NVR}\alpha\rightarrow\text{BIN}}$					
06	06	43	2	$\overline{\text{AVR}\alpha\rightarrow\text{BIN}}$					
06	06	07	4	$\overline{\text{RVR}\alpha\rightarrow\text{BOUT}}$	SOLα	33	04	12	22
							05	21	27
06	05	44	6	$\overline{\text{SIG NVR}\alpha\text{-CP}}$		34			
06	17	37	7	BDIN A		35			
06	06	24	8	$\overline{\text{BOUT}\rightarrow\text{NVR}\alpha}$		36			
04	04	19	9	$\overline{\text{CLEAR AVR}\alpha}$		37			
06	06	29	10	$\overline{\text{BOUT}\rightarrow\text{AVR}\alpha}$	SIG BRα-S	38	04	12	23
							05	13	35
06	06	27	11	$\overline{\text{BOUT}\rightarrow\text{RVR}\alpha}$	$\overline{\text{SIG BR}\alpha\text{-S}}$	39	04	12	24
06	18	37	12	MIR14	$\overline{\text{SIG RVR}\alpha\text{-S}}$	40	04	21	51
							06	10	09
06	08	20	13	AC$\rightarrow$BOUT	SIG RVRα-S	41	05	13	87
							04	21	39
06	08	76	14	$\overline{\text{SIG AC-S}}$	$\overline{\text{SIG AVR}\alpha\text{-S}}$	42	04	01	07
			15		SIG AVRα-S	43	05	13	38
			16		SIG BIN	44	05	09	44
							06	18	19
							06	13	14
06	02	39	17	MISC:3	SIG NVRα-S	45	05	13	89
			18						
			19						
			20						
			21						
06	04	33	22	$\overline{\text{CP}}$					
06	15	22	23	ERRORα					
			24						
			25						
06	15	28	26	MIED:7					
05	22	17	27	$\overline{\text{PRES}\alpha}$					
05	22	15	28	$\overline{\text{SCAN}}$					
			29						
			30						
06	15	43	31	GR					

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	09	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC218	STORE SERVO ON LINE CONTROL, SOLα	12	98	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	09	67	1	BIN C1					
06	09	86	4	BIN C4					
06	09	40	8	BOUT A2					
06	09	52	10	BIN B5					
06	09	28	11	BIN C5					
06	09	27	12	BOUT C3					
			13						
			14						
06	09	30	15	BOUT A3					
			16						
06	09	02	17	BIN B6					
06	09	11	18	BIN A3					
06	09	84	19	BIN C3					
06	09	20	20	BOUT C4					
06	09	23	23	BOUT A4					
06	09	40	26	BOUT B1					
06	09	12	27	BOUT C5					
06	09	08	28	BOUT A6					
06	09	15	30	BOUT A5					
			31						
06	09	21	32	BIN C2					
06	09	06	33	BIN B2					
06	09	04	34	BIN A2					
06	09	55	35	BOUT C6					
			42						
			44						
06	09	93	51	BOUT D1					
06	09	10	52	BIN A4					
06	09	60	53	BIN B4					
06	09	35	55	BOUT C2					
06	09	87	57	BOUT D2					
06	09	89	59	BOUT B2					
06	09	75	60	BIN D4					
06	09	51	61	BIN A5					
06	09	01	62	BIN A6					
06	09	76	63	BIN D1					
06	09	79	64	BOUT D3					
			65						
06	09	81	66	BOUT B3					
06	09	17	67	BIN C6					
06	09	26	68	BIN D5					
06	09	61	69	BIN B3					
06	09	85	70	BIN D3					
06	09	72	72	BOUT D4					
06	09	77	73	BIN D6					

INTERCONNECTION TABLE	RACK	LOC'N	NOTES
	04	13	
TYPE	GROUP	SHEET	
SIGNAL DISPLAY ADAPTER SHEET 1	12	102	
	PART 1		

Iss. 1

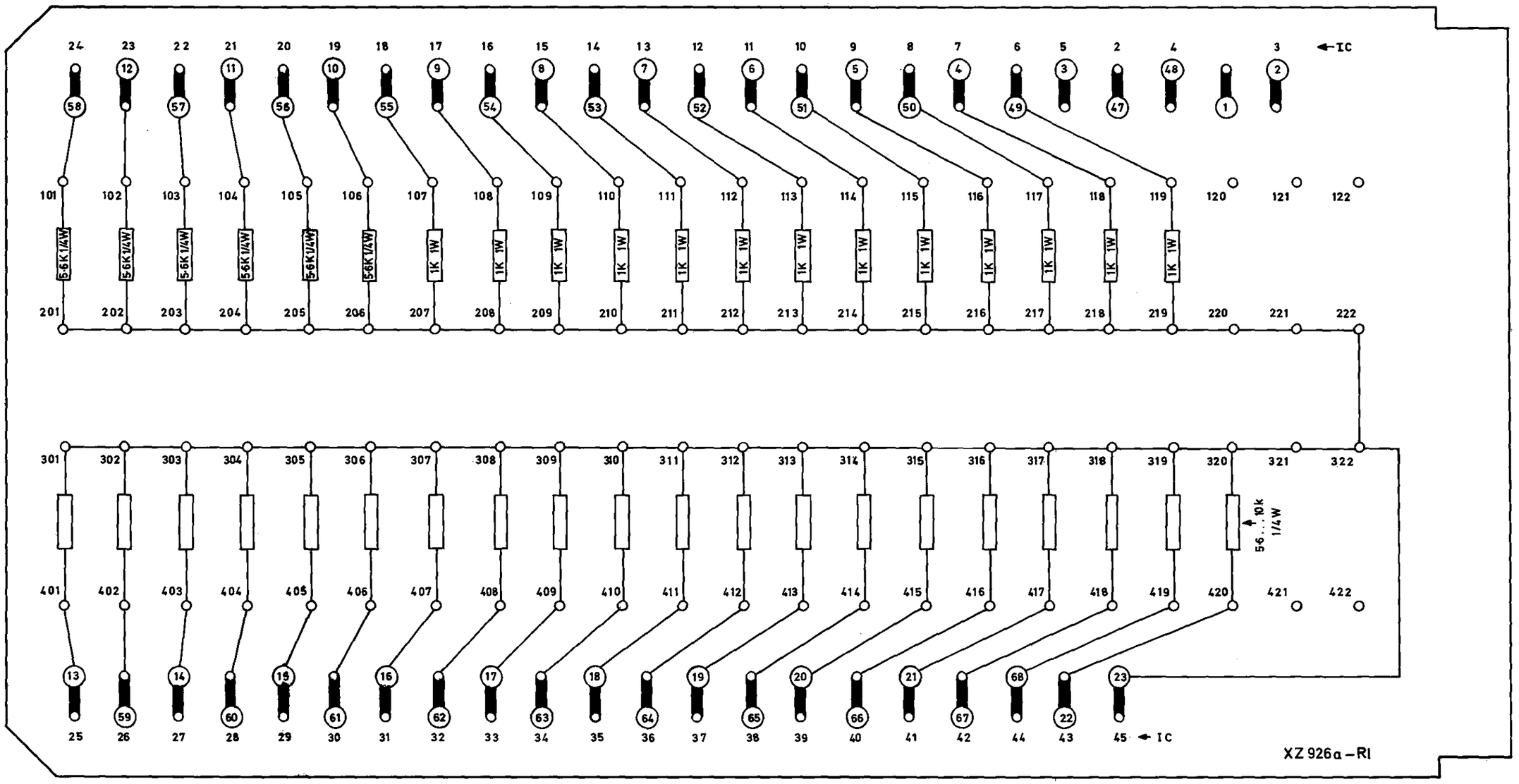
LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	09	74	74	BOUT B4					
06	09	91	75	BOUT A1					
06	09	54	76	BIN B1					
06	09	53	77	BIN A1					
06	09	64	79	BOUT D5					
			80						
06	09	66	81	BOUT B5					
06	09	83	82	BIN D2					
06	09	93	83	BOUT C1					
06	09	57	87	BOUT D6					
06	09	59	89	BOUT B6					
			92						
			95						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	13	
TYPE		GROUP	SHEET	
	SIGNAL DISPLAY ADAPTER SHEET 2	12	102	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				DOUTα A4	6	04	19	73
				DOUTα B4	7	04	19	75
				DOUTα C4	8	04	19	77
				DOUTα D4	9	04	19	79
				DOUTα A3	10	04	19	65
				DOUTα B3	11	04	19	67
				DOUTα C3	12	04	19	69
				DOUTα D3	13	04	19	71
				DOUTα A2	14	04	19	57
				DOUTα B2	15	04	19	59
				DOUTα C2	16	04	19	61
				DOUTα D2	17	04	19	63
				DOUTα A1	18	04	21	95
				$\overline{\text{SIG RVR}\alpha}$	19	04	21	89
				$\overline{\text{SIG RVR}\epsilon}$	20	05	20	93
				ROUTα A2	25	04	20	57
				ROUTα B2	26	04	20	59
				ROUTα C2	27	04	20	61
				ROUTα D2	28	04	20	63
				ROUTα A3	29	04	20	65
				ROUTα B3	30	04	20	67
				ROUTα C3	31	04	20	69
				ROUTα D3	32	04	20	71
				ROUTα A4	33	04	20	73
				ROUTα B4	34	04	20	75
				ROUTα C4	35	04	20	77
				ROUTα D4	36	04	20	79
				ROUTα A5	37	04	20	81
				ROUTα B5	38	04	20	83
				ROUTα C5	39	04	20	85
				ROUTα D5	40	04	20	87
				ROUTα A6	41	04	20	89
				ROUTα B6	42	04	20	91
				ROUTα C6	43	04	20	93
				ROUTα D6	44	04	20	95

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	18	+ 24V : PIN 45
TYPE		GROUP	SHEET	
XZ926	ISOLATING DIODES	12	107	
		PART 1		



Card u0418 XZ 926a - RI: Component Layout

Figure 12-108 Location u0418

Part 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	12	37	51	DOUTα A2	$\overline{\text{DOUTα A2}}$	57	04	61	09
							04	18	14
04	12	36	53	DOUTα B2	$\overline{\text{DOUTα B2}}$	59	04	61	19
							04	18	15
04	12	35	55	DOUTα C2	$\overline{\text{DOUTα C2}}$	61	04	61	29
							04	18	16
04	12	34	13	DOUTα D2	$\overline{\text{DOUTα D2}}$	63	04	61	39
							04	18	17
04	12	41	15	DOUTα A3	$\overline{\text{DOUTα A3}}$	65	04	61	08
							04	18	10
04	12	40	17	DOUTα B3	$\overline{\text{DOUTα B3}}$	67	04	61	18
							04	18	11
04	12	39	19	DOUTα C3	$\overline{\text{DOUTα C3}}$	69	04	61	28
							04	18	12
04	12	38	21	DOUTα D3	$\overline{\text{DOUTα D3}}$	71	04	61	38
							04	18	13
04	12	45	23	DOUTα A4	$\overline{\text{DOUTα A4}}$	73	04	61	07
							04	18	06
04	12	44	25	DOUTα B4	$\overline{\text{DOUTα B4}}$	75	04	61	07
							04	18	07
04	12	43	27	DOUTα C4	$\overline{\text{DOUTα C4}}$	77	04	61	27
							04	18	08
04	12	42	29	DOUTα D4	$\overline{\text{DOUTα D4}}$	79	04	61	37
							04	18	09
04	12	30	31	CW	$\overline{\text{CW}}$	81	05	62	03
04	12	29	33	CCW	$\overline{\text{CCW}}$	83	05	62	13
04	10	45	35	SELα 6	$\overline{\text{SELα 6}}$	85	04	61	05
04	10	43	37	SELα 5	$\overline{\text{SELα 5}}$	87	04	61	15
04	10	42	39	SELα 4	$\overline{\text{SELα 4}}$	89	04	61	25
04	10	41	41	SELα 3	$\overline{\text{SELα 3}}$	91	04	61	35
04	10	40	43	SELα 2	$\overline{\text{SELα 2}}$	93	04	61	04
04	10	22	45	SELα SIG	$\overline{\text{SELα SIG}}$	95	04	61	14

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	19	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
LT234	SELECTION CODE SWITCH, DOUTα	12	109	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	07	88	51	$\overline{\text{ROUTα A2}}$	ROUTα A2	57	04	62	09
							04	18	25
04	07	39	53	$\overline{\text{ROUTα B2}}$	ROUTα B2	59	04	62	19
							04	18	26
04	07	36	55	$\overline{\text{ROUTα C2}}$	ROUTα C2	61	04	62	29
							04	18	27
04	07	37	13	$\overline{\text{ROUTα D2}}$	ROUTα D2	63	04	62	39
							04	18	28
04	07	80	15	$\overline{\text{ROUTα A3}}$	ROUTα A3	65	04	62	08
							04	18	29
04	07	31	17	$\overline{\text{ROUTα B3}}$	ROUTα B3	67	04	62	18
							04	18	30
04	07	78	19	$\overline{\text{ROUTα C3}}$	ROUTα C3	69	04	62	28
							04	18	31
04	07	79	21	$\overline{\text{ROUTα D3}}$	ROUTα D3	71	04	62	38
							04	18	32
04	07	73	23	$\overline{\text{ROUTα A4}}$	ROUTα A4	73	04	62	07
							04	18	33
04	07	24	25	$\overline{\text{ROUTα B4}}$	ROUTα B4	75	04	62	17
							04	18	34
04	07	71	27	$\overline{\text{ROUTα C4}}$	ROUTα C4	77	04	62	27
							04	18	35
04	07	22	29	$\overline{\text{ROUTα D4}}$	ROUTα D4	79	04	62	37
							04	18	36
04	07	65	31	$\overline{\text{ROUTα A5}}$	ROUTα A5	81	04	62	06
							04	18	37
04	07	16	33	$\overline{\text{ROUTα B5}}$	ROUTααB5	83	04	62	16
							04	18	38
04	07	13	35	$\overline{\text{ROUTα C5}}$	ROUTα C5	85	04	62	26
							04	18	39
04	07	14	37	$\overline{\text{ROUTα D5}}$	ROUTα D5	87	04	62	36
							04	18	40
04	07	58	39	$\overline{\text{ROUTα A6}}$	ROUTα A6	89	04	62	05
							04	18	41
04	07	09	41	$\overline{\text{ROUTα B6}}$	ROUTα B6	91	04	62	15
							04	18	42
04	07	56	43	$\overline{\text{ROUTα C6}}$	ROUTα C6	93	04	62	25
							04	18	43
04	07	07	45	$\overline{\text{ROUTα D6}}$	ROUTα D6	95	04	62	35
							04	18	44

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	20	+5V : PIN 5
TYPE		GROUP	SHEET	GND : PIN 3
LT234	READOUTα	12	110	
				PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	09	40	51	$\overline{\text{SIG RVR}\alpha}$	SIG RVRα	57	04	62	04
			53			59			
06	02	26	55	SCIF	$\overline{\text{SCIF}}$	61	04	61	13
06	18	25	13	$\overline{\text{SEC20}}$	SEC20	63	05	62	01
							05	18	19
06	18	26	15	$\overline{\text{SEC21}}$	SEC21	65	05	62	11
							05	18	20
06	18	27	17	$\overline{\text{SEC22}}$	SEC22	67	05	62	21
							05	18	21
06	18	28	19	$\overline{\text{SEC23}}$	SEC23	69	05	62	31
							05	18	22
06	18	29	21	$\overline{\text{SEC24}}$	SEC24	71	05	62	00
							05	18	23
06	18	30	23	$\overline{\text{SEC25}}$	SEC25	73	05	62	10
							05	18	24
05	24	43	25	ER-24V	$\overline{\text{ERROR1}}$	75	06	16	33
							04	21	27
							06	15	24
							06	15	25
04	21	75	27	$\overline{\text{ERROR1}}$	ERROR1	77	04	61	24
			29			79			
06	02	29	31	TUP/LOST	$\overline{\text{TUP/LOST}}$	81	04	61	33
06	02	28	33	SPWA	$\overline{\text{SPWA}}$	83	04	61	02
05	24	43	35	ER-24V	ERROR2	85	04	21	37
							05	21	13
04	21	85	37	$\overline{\text{ERROR2}}$	ERROR2	87	04	61	34
04	09	41	39	SIG RVRα	$\overline{\text{SIG RVR}\alpha}$	89	04	62	14
							04	18	19
			41			91			
			43			93			
04	12	28	45	DOUTα A1	$\overline{\text{DOUT}\alpha\ \text{A1}}$	95	04	61	06
							04	18	18

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	21	+5V : PIN 5 GND : PIN 3
TYPE	SECANT AND MACP DISPLAYS	GROUP	SHEET	
		12	111	
LT234		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
04	63	09	35	NUA	17	04	03	11
04	63	19	36	NUB	18	04	03	12
04	63	29	37	NUC	15	04	03	13
04	63	39	38	NUD	16	04	03	14
04	63	08	39	NTA	13	05	03	11
04	63	18	40	NTB	14	05	03	12
04	63	28	41	NTC	11	05	03	13
04	63	38	42	NTD	12	05	03	14
04	63	07	43	TL2A	9	06	01	14
04	63	17	44	TL2B	10	06	01	15

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	22	+5V PIN 5,45 GND PIN 3
TYPE		GROUP	SHEET	
UT230	NUMBER OF TURNS	12	112	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	63	27	35	TL2C		17	06	01	16
04	63	37	36	TL2D		18	06	01	17
04	63	06	37	TL3A		15	06	01	09
04	63	16	38	TL3B		16	06	01	10
04	63	26	39	TL3C		13	06	01	11
04	63	36	40	TL3D		14	06	01	12
04	63	05	41	TL4A		11	06	01	02
04	63	15	42	TL4B		12	06	01	04
04	63	25	43	TL4C		9	06	01	06
04	63	35	44	TL4D		10	06	01	07

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	23	+5V : PIN 5,45 GND : PIN 3
TYPE	TRACK LENGTH	GROUP	SHEET	
UT230		12	113	
		PART 1		

LOCATION			PIN	SIGNAL	PIN	LOCATION		
04	64	02	35	COARSE$_\alpha$	17	06	15	11
						04	01	18
04	64	12	36	FINE$_\alpha$	18	06	15	12
						04	01	16
05	64	02	37	COARSE$_\varepsilon$	15	06	15	14
						05	01	18
05	64	12	38	FINE$_\varepsilon$	16	06	15	15
						05	01	16
04	64	22	39	$\overline{\text{DIR1}}_\alpha$	13	04	01	08
04	64	32	40	$\overline{\text{DIR2}}_\alpha$	14	04	01	09
05	64	22	41	$\overline{\text{DIR1}}_\varepsilon$	11	05	01	08
05	64	32	42	$\overline{\text{DIR2}}_\varepsilon$	12	05	01	09
			43		9			
			44		10			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	24	+5V : PIN 5 GND : PIN 3 NOT CON. PIN 45
TYPE		GROUP	SHEET	
UT230	MEASUREMENT SYSTEM SIGNAL INPUTS	12	114	
		PART 1		

Iss. 1

		9	8	7	6	5	4	3	2	1	0
61	0	DOUTα A2	DOUTα A3	DOUTα A4	DOUTα A1	SELα 6	SELα 1		SPWA		
	1	DOUTα B2	DOUTα B3	DOUTα B4		SELα 5	SELα SIG	SCIF			
	2	DOUTα C2	DOUTα C3	DOUTα C4		SELα 4	ERROR1				
	3	DOUTα D2	DOUTα D3	DOUTα D4		SELα 2	ERROR2	TPU/LOST			
62	0	ROUTα A2	ROUTα A3	ROUTα A4	ROUTα A5	ROUTα A6	SIG RVRα-S				
	1	ROUTα B2	ROUTα B3	ROUTα B4	ROUTα B5	ROUTα B6	$\overline{\text{SIG RVRα-S}}$				
	2	ROUTα C2	ROUTα C3	ROUTα C4	ROUTα C5	ROUTα C6					
	3	ROUTα D2	ROUTα D3	ROUTα D4	ROUTα D5	ROUTα D6					
63	0	NUA	NTA	TL2A	TL3A	TL4A					
	1	NUB	NTB	TL2B	TL3B	TL4B					
	2	NUC	NTC	TL2C	TL3C	TL4C					
	3	NUD	NTD	TL2D	TL3D	TL4D					
64	0								COARSEα	AVRα A6	$\overline{\text{AVRα A6}}$
	1								MEDIUMα	AVRα B6	$\overline{\text{AVRα B6}}$
	2								DIR1α	AVRα C6	$\overline{\text{AVRα C6}}$
	3								DIR2α	AVRα D6	$\overline{\text{AVRα D6}}$
65	0	AVRα A1	$\overline{\text{AVRα A1}}$	AVRα A2	$\overline{\text{AVRα A2}}$	AVRα A3	$\overline{\text{AVRα A3}}$	AVRα A4	$\overline{\text{AVRα A4}}$	AVRα A5	$\overline{\text{AVRα A5}}$
	1	AVRα B1	$\overline{\text{AVRα B1}}$	AVRα B2	$\overline{\text{AVRα B2}}$	AVRα B3	$\overline{\text{AVRα B3}}$	AVRα B4	$\overline{\text{AVRα B4}}$	AVRα B5	$\overline{\text{AVRα B5}}$
	2	AVRα C1	$\overline{\text{AVRα C1}}$	AVRα C2	$\overline{\text{AVRα C2}}$	AVRα C3	$\overline{\text{AVRα C3}}$	AVRα C4	$\overline{\text{AVRα C4}}$	AVRα C5	$\overline{\text{AVRα C5}}$
	3	AVRα D1	$\overline{\text{AVRα D1}}$	AVRα D2	$\overline{\text{AVRα D2}}$	AVRα D3	$\overline{\text{AVRα D3}}$	AVRα D4	$\overline{\text{AVRα D4}}$	AVRα D5	$\overline{\text{AVRα D5}}$

INTERCONNECTION TABLE		RACK 04	LOC'N	NOTES
TYPE	SUB-RACK TERMINAL BLOCK	GROUP 12	SHEET 115	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			00						
			01						
04	21	83	02	SPWA			01	63	11
			03						
04	19	43	04	SELα 2			01	63	00
04	19	35	05	SELα 6			01	62	14
04	21	95	06	DOUTα A1			01	66	16
04	19	73	07	DOUTα A4			01	61	20
04	19	65	08	DOUTα A3			01	61	10
04	19	57	09	DOUTα A2			01	61	00
			10						
			11						
			12						
04	21	61	13	SC1F			01	67	05
04	19	95	14	SELα SIG			01	63	01
04	19	87	15	SELα 5			01	62	15
			16						
04	19	75	17	DOUT B4			01	61	21
04	19	67	18	DOUT B3			01	61	11
04	19	59	19	DOUT B2			01	61	01
			20						
			21						
			22						
			23						
04	21	77	24	ERROR1			01	63	02
04	19	89	25	SELα 4			01	62	16
			26						
04	19	77	27	DOUTα C4			01	61	22
04	19	69	28	DOUTα C3			01	61	12
04	19	61	29	DOUTα C2			01	61	02
			30						
			31						
			32						
04	21	81	33	TPU/LOST			01	63	10
04	21	87	34	ERROR2			01	63	03
04	19	91	35	SELα 3			01	62	17
			36						
04	19	79	37	DOUT D4			01	61	23
04	19	71	38	DOUT D3			01	61	13
04	19	63	39	DOUT D2			01	61	03

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	61	
TYPE	TERMINAL BLOCK 0461	GROUP 12	SHEET 116	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			00						
			01						
			02						
			03						
04	21	57	04	SIG RVRα-S			01	62	04
04	20	89	05	ROUTα A6			01	62	20
04	20	81	06	ROUTα A5			01	62	10
04	20	73	07	ROUTα A4			01	62	00
04	20	65	08	ROUTα A3			01	61	14
04	20	57	09	ROUTα A2			01	61	04
			10						
			11						
			12						
			13						
04	21	89	14	SIG RVRα-S			01	62	05
04	20	91	15	ROUTα B6			01	62	21
04	20	83	16	ROUTα B5			01	62	11
04	20	75	17	ROUTα B4			01	62	01
04	20	67	18	ROUTα B3			01	61	15
04	20	59	19	ROUTα B2			01	61	05
			20						
			21						
			22						
			23						
			24						
04	20	93	25	ROUTα C6			01	62	22
04	20	85	26	ROUTα C5			01	62	12
04	20	77	27	ROUTα C4			01	62	02
04	20	69	28	ROUTα C3			01	61	16
04	20	61	29	ROUTα C2			01	61	06
			30						
			31						
			32						
			33						
			34						
04	20	95	35	ROUTα D6			01	62	23
04	20	87	36	ROUTα D5			01	62	13
04	20	79	37	ROUTα D4			01	62	03
04	20	71	38	ROUTα D3			01	61	17
04	20	63	39	ROUTα D2			01	61	07

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	62	
TYPE	TERMINAL BLOCK 0462	GROUP 12	SHEET 117	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			00					
			01					
			02					
			03					
			04					
01	64	01	05	TL4A		04	23	41
01	63	26	06	TL3A		04	23	37
01	63	17	07	TL2A		04	22	43
01	63	08	08	NTA		04	22	39
01	63	04	09	NUA		04	22	35
			10					
			11					
			12					
			13					
			14					
01	64	02	15	TL4B		04	23	42
01	63	27	16	TL3B		04	23	38
01	63	18	17	TL2B		04	22	44
01	63	14	18	NTB		04	22	40
01	63	05	19	NUB		04	22	36
			20					
			21					
			22					
			23					
			24					
01	64	03	25	TL4C		04	23	43
01	63	28	26	TL3C		04	23	39
01	63	24	27	TL2C		04	23	35
01	63	15	28	NTC		04	22	41
01	63	06	29	NUC		04	22	37
			30					
			31					
			32					
			33					
			34					
01	64	04	35	TL4D		04	23	44
01	64	00	36	TL3D		04	23	40
01	63	25	37	TL2D		04	23	36
01	63	16	38	NTD		04	22	42
			39	NUD		04	22	38

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	63	
TYPE	TERMINAL BLOCK 0463	GROUP	SHEET	
		12	118	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	04	58	00	$\overline{\text{AVR}\alpha\ \text{A6}}$			07	64	00
04	05	41	01	AVRα A6			07	64	01
07	64	02	02	COARSEα			04	24	35
			03						
			04						
			05						
			06						
			07						
			08						
			09						
04	04	09	10	$\overline{\text{AVR}\alpha\ \text{B6}}$			07	64	10
04	05	42	11	AVRα B6			07	64	11
07	64	12	12	MEDIUMα			04	24	36
			13						
			14						
			15						
			16						
			17						
			18						
			19						
04	04	56	20	$\overline{\text{AVR}\alpha\ \text{C6}}$			07	64	20
04	05	43	21	AVRα C6			07	64	21
07	64	22	22	DIR1α			04	24	39
			23						
			24						
			25						
			26						
			27						
			28						
			29						
04	04	07	30	$\overline{\text{AVR}\alpha\ \text{D6}}$			07	64	30
04	05	44	31	AVRα D6			07	64	31
07	64	32	32	DIR2α			04	24	40
			33						
			34						
			35						
			36						
			37						
			38						
			39						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	64	
TYPE	TERMINAL BLOCK 0464	GROUP	SHEET	
		12	119	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	04	65	00	AVRα A5			07	65	00
04	05	37	01	AVRα A5			07	65	01
04	04	73	02	AVRα A4			07	65	02
04	05	33	03	AVRα A4			07	65	03
04	04	80	04	AVRα A3			07	65	04
04	05	29	05	AVRα A3			07	65	05
04	04	88	06	AVRα A2			07	65	06
04	05	25	07	AVRα A2			07	65	07
04	04	94	08	AVRα A1			07	65	08
04	04	44	09	AVRα A1			07	65	09
04	04	16	10	AVRα B5			07	65	10
04	05	38	11	AVRα B5			07	65	11
04	04	24	12	AVRα B4			07	65	12
04	05	34	13	AVRα B4			07	65	13
04	04	31	14	AVRα B3			07	65	14
04	05	30	15	AVRα B3			07	65	15
04	04	39	16	AVRα B2			07	65	16
04	05	26	17	AVRα B2			07	65	17
04	04	45	18	AVRα B1			07	65	18
04	04	95	19	AVRα B1			07	65	19
04	04	13	20	AVRα C5			07	65	20
04	05	39	21	AVRα C5			07	65	21
04	04	71	22	AVRα C4			07	65	22
04	05	35	23	AVRα C4			07	65	23
04	04	78	24	AVRα C3			07	65	24
04	05	31	25	AVRα C3			07	65	25
04	04	36	26	AVRα C2			07	65	26
04	05	27	27	AVRα C2			07	65	27
04	04	43	28	AVRα C1			07	65	28
04	04	42	29	AVRα C1			07	65	29
04	04	14	30	AVRα D5			07	65	30
04	05	40	31	AVRα D5			07	65	31
04	04	22	32	AVRα D4			07	65	32
04	05	36	33	AVRα D4			07	65	33
04	04	29	34	AVRα D3			07	65	34
04	05	32	35	AVRα D3			07	65	35
04	04	37	36	AVRα D2			07	65	36
04	05	28	37	AVRα D2			07	65	37
04	04	41	38	AVRα D1			07	65	38
04	04	92	39	AVRα D1			07	65	39

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		04	65	
TYPE	TERMINAL BLOCK 0465	GROUP 12	SHEET 120	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			53	BOUT A1	BIN A1	91			
			54	BOUT B1	BIN B1	40			
			67	BOUT C1	BIN C1	90			
			76	BOUT D1	BIN D1	93			
			4	BOUT A2	BIN A2	38			
			6	BOUT B2	BIN B2	89			
			21	BOUT C2	BIN C2	35			
			83	BOUT D2	BIN D2	87			
			11	BOUT A3	BIN A3	30			
			61	BOUT B3	BIN B3	81			
			84	BOUT C3	BIN C3	27			
			85	BOUT D3	BIN D3	79			
			10	BOUT A4	BIN A4	23			
			60	BOUT B4	BIN B4	74			
			86	BOUT C4	BIN C4	20			
			75	BOUT D4	BIN D4	72			
			51	BOUT A5	BIN A5	15			
			58	BOUT B5	BIN B5	66			
			28	BOUT C5	BIN C5	12			
			26	BOUT D5	BIN D5	64			
			1	BOUT A6	BIN A6	8			
			2	BOUT B6	BIN B6	59			
			17	BOUT C6	BIN C6	55			
			77	BOUT D6	BIN D6	57			
05	04	18	25	CLEAR AVRε	AVRε A1	44	05	65	09
			63		$\overline{\text{AVRε A1}}$	94	05	65	08
06	06	28	68	$\overline{\text{BOUT→AVRε}}$	AVRε B1	95	05	65	19
06	06	41	62	$\overline{\text{AVRε→BIN}}$	$\overline{\text{AVRε B1}}$	45	05	65	18
06	15	44	70	GR	AVRε C1	42	05	65	29
05	04	19	69	$\overline{\text{CLEAR AVRε}}$	$\overline{\text{AVRε C1}}$	43	05	65	28
			34		AVRε D1	92	05	65	39
			33		$\overline{\text{AVRε D1}}$	41	05	65	38
			32		$\overline{\text{AVRε A2}}$	88	05	05	02
							05	65	06
			82		$\overline{\text{AVRε B2}}$	39	05	05	04
							05	65	16
					$\overline{\text{AVRε C2}}$	36	05	05	06
							05	65	26
					$\overline{\text{AVRε D2}}$	37	05	05	07
							05	65	36
					$\overline{\text{AVRε A3}}$	80	05	05	08
							05	65	04
					$\overline{\text{AVRε B3}}$	31	05	05	09
							05	65	14
					$\overline{\text{AVRε C3}}$	78	05	05	10
							05	65	24

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	04	x BIN1 BOUT SEE 0609 +5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC244	ACTUAL VALUE REGISTER, AVRε SHEET 1	12	127	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				$\overline{\text{AVR}\varepsilon\ \text{D3}}$	29	05	05	11
						05	65	34
				$\overline{\text{AVR}\varepsilon\ \text{A4}}$	73	05	05	12
						05	65	02
				$\overline{\text{AVR}\varepsilon\ \text{B4}}$	24	05	05	13
						05	65	12
				$\overline{\text{AVR}\varepsilon\ \text{C4}}$	71	05	05	14
						05	65	22
				$\overline{\text{AVR}\varepsilon\ \text{D4}}$	22	05	05	15
						05	65	32
				$\overline{\text{AVR}\varepsilon\ \text{A5}}$	65	05	05	16
						05	65	00
				$\overline{\text{AVR}\varepsilon\ \text{B5}}$	16	05	05	17
						05	65	10
				$\overline{\text{AVR}\varepsilon\ \text{C5}}$	13	05	05	18
						05	65	20
				$\overline{\text{AVR}\varepsilon\ \text{D5}}$	14	05	05	19
						05	65	30
				$\overline{\text{AVR}\varepsilon\ \text{A6}}$	58	05	05	20
						05	64	00
				$\overline{\text{AVR}\varepsilon\ \text{B6}}$	9	05	05	21
						05	64	10
				$\overline{\text{AVR}\varepsilon\ \text{C6}}$	56	05	05	22
						05	64	20
				$\overline{\text{AVR}\varepsilon\ \text{D6}}$	7	05	05	23
						05	64	30
				CLEAR AVRε	18	05	04	25
				$\overline{\text{CLEAR AVR}\varepsilon}$	19	06	06	34
						05	04	69
						05	09	09

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	04	+5V : PIN 5 GND : PIN 3
TYPE JC244	ACTUAL VALUE REGISTER, AVRε SHEET 2	GROUP 12	SHEET 127	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	04	88	2	$\overline{\text{AVR}\varepsilon\ \text{A2}}$	AVRε A2	25	05	65	07
05	04	39	4	$\overline{\text{AVR}\varepsilon\ \text{B2}}$	AVRε B2	26	05	65	17
05	04	36	6	$\overline{\text{AVR}\varepsilon\ \text{C2}}$	AVRε C2	27	05	65	27
05	04	37	7	$\overline{\text{AVR}\varepsilon\ \text{D2}}$	AVRε D2	28	05	65	37
05	04	80	8	$\overline{\text{AVR}\varepsilon\ \text{A3}}$	AVRε A3	29	05	65	05
05	04	31	9	$\overline{\text{AVR}\varepsilon\ \text{B3}}$	AVRε B3	30	05	65	15
05	04	78	10	$\overline{\text{AVR}\varepsilon\ \text{C3}}$	AVRε C3	31	05	65	25
05	04	29	11	$\overline{\text{AVR}\varepsilon\ \text{D3}}$	AVRε D3	32	05	65	35
05	04	73	12	$\overline{\text{AVR}\varepsilon\ \text{A4}}$	AVRε A4	33	05	65	03
05	04	24	13	$\overline{\text{AVR}\varepsilon\ \text{B4}}$	AVRε B4	34	05	65	13
05	04	71	14	$\overline{\text{AVR}\varepsilon\ \text{C4}}$	AVRε C4	35	05	65	23
05	04	22	15	$\overline{\text{AVR}\varepsilon\ \text{D4}}$	AVRε D4	36	05	65	33
05	04	65	16	$\overline{\text{AVR}\varepsilon\ \text{A5}}$	AVRε A5	37	05	65	01
05	04	16	17	$\overline{\text{AVR}\varepsilon\ \text{B5}}$	AVRε B5	38	05	65	11
05	04	13	18	$\overline{\text{AVR}\varepsilon\ \text{C5}}$	AVRε C5	39	05	65	21
05	04	14	19	$\overline{\text{AVR}\varepsilon\ \text{D5}}$	AVRε D5	40	05	65	31
05	04	58	20	$\overline{\text{AVR}\varepsilon\ \text{A6}}$	AVRε A6	41	05	64	01
05	04	09	21	$\overline{\text{AVR}\varepsilon\ \text{B6}}$	AVRε B6	42	05	64	11
05	04	56	22	$\overline{\text{AVR}\varepsilon\ \text{C6}}$	AVRε C6	43	05	64	21
05	04	07	23	$\overline{\text{AVR}\varepsilon\ \text{D6}}$	AVRε D6	44	05	64	31
			24			45			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	05	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC218	INVERTER, INVε	12	128	
				PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			53	BOUT A6	BIN A6	91			
			54	BOUT B6	BIN B6	40			
			67	BOUT C6	BIN C6	90			
			76	BOUT D6	BIN D6	93			
			4	BOUT A5	BIN A5	38			
			6	BOUT B5	BIN B5	89			
			21	BOUT C5	BIN C5	35			
			83	BOUT D5	BIN D5	87			
			11	BOUT A4	BIN A4	30			
			61	BOUT B4	BIN B4	81			
			84	BOUT C4	BIN C4	27			
			85	BOUT D4	BIN D4	79			
			10	BOUT A3	BIN A3	23			
			60	BOUT B3	BIN B3	74			
			86	BOUT C3	BIN C3	20			
			75	BOUT D3	BIN D3	72			
			51	BOUT A2	BIN A2	15			
			52	BOUT B2	BIN B2	66			
			28	BOUT C2	BIN C2	12			
			26	BOUT D2	BIN D2	64			
			1	BOUT A1	BIN A1	8			
			2	BOUT B1	BIN B1	59			
			17	BOUT C1	BIN C1	55			
			77	BOUT D1	BIN D1	57			
05	06	18	25	CLEAR NVRε	NVRε A6	44			
06	05	43	63	$\overline{\text{NVRε} - \text{CP}}$	$\overline{\text{NVRε A6}}$	94			
06	06	25	68	$\overline{\text{BOUT}\rightarrow\text{NVRε}}$	NVRε B6	95			
06	06	41	62	$\overline{\text{NVRε}\rightarrow\text{BIN}}$	$\overline{\text{NVRε B6}}$	45			
06	15	44	70	GR	NVRε C6	42			
05	06	19	69	$\overline{\text{CLEAR NVRε}}$	$\overline{\text{NVRε C6}}$	43			
06	17	34	34	ADINA	$\overline{\text{NVRε D6}}$	92			
06	17	33	33	ADINB	$\overline{\text{NVRε D6}}$	41			
06	17	32	32	ADINC	$\overline{\text{NVRε A5}}$	88			
06	17	31	82	ADIND	$\overline{\text{NVRε B5}}$	39			
					$\overline{\text{NVRε C5}}$	36			
					$\overline{\text{NVRε D5}}$	37			
					$\overline{\text{NVRε A4}}$	80			
					$\overline{\text{NVRε B4}}$	31			
					$\overline{\text{NVRε C4}}$	78			
					$\overline{\text{NVRε D4}}$	29			
					$\overline{\text{NVRε A3}}$	73			
					$\overline{\text{NVRε B3}}$	24			
					$\overline{\text{NVRε C3}}$	71			
					$\overline{\text{NVRε D3}}$	22			
					$\overline{\text{NVRε A2}}$	65			
					$\overline{\text{NVRε B2}}$	16			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	06	SIN1 BOUT SEE 0609 +5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC244	NOMINAL VALUE REGISTER, NVRε SHEET 1	12	129	
				PART 1

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				$\overline{\text{NVRε C2}}$	13			
				$\overline{\text{NVRε D2}}$	14			
				$\overline{\text{NVRε A1}}$	58			
				$\overline{\text{NVRε B1}}$	9			
				$\overline{\text{NVRε C1}}$	56			
				$\overline{\text{NVRε D1}}$	7			
				CLEAR NVRε	18	05	06	25
				$\overline{\text{CLEAR NVRε}}$	19	06	06	30
						05	06	69

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	06	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC244	NOMINAL VALUE REGISTER, NVRε SHEET 2	12	129	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			53	BOUT A1	BOUT A1	91			
			54	BOUT B1	BOUT B1	40			
			67	BOUT C1	BOUT C1	90			
			76	BOUT D1	BOUT D1	93			
			4	BOUT A2	BOUT A2	38			
			6	BOUT B2	BOUT B2	89			
			21	BOUT C2	BOUT C2	35			
			83	BOUT D2	BOUT D2	87			
			11	BOUT A3	BOUT A3	30			
			61	BOUT B3	BOUT B3	81			
			84	BOUT C3	BOUT C3	27			
			85	BOUT D3	BOUT D3	79			
			10	BOUT A4	BOUT A4	23			
			60	BOUT B4	BOUT B4	74			
			86	BOUT C4	BOUT C4	20			
			75	BOUT D4	BOUT D4	72			
			51	BOUT A5	BOUT A5	15			
			52	BOUT B5	BOUT B5	66			
			28	BOUT C5	BOUT C5	12			
			26	BOUT D5	BOUT D5	64			
			1	BOUT A6	BOUT A6	8			
			2	BOUT B6	BOUT B6	59			
			17	BOUT C6	BOUT C6	55			
			77	BOUT D6	BOUT D6	57			
05	07	18	25	CLEAR RVRε	RVRε A1	44	05	07	34
							06	14	09
06	06	38	63	$\overline{\text{RVRε-CP}}$	$\overline{\text{ROUT A1}}$	94			
06	06	26	68	$\overline{\text{BOUT→RVRε}}$	RVRε B1	95	05	07	33
							06	14	10
06	06	22	62	$\overline{\text{RVRε→BOUT}}$	$\overline{\text{ROUT B1}}$	45			
06	15	44	70	GR	RVRε C1	42	05	07	32
							06	14	11
05	07	19	69	$\overline{\text{CLEAR RVRε}}$	$\overline{\text{ROUT C1}}$	43			
05	07	44	34	RVRε A1	RVRε D1	92	05	07	82
							06	14	12
05	07	95	33	RVRε B1	$\overline{\text{ROUT D1}}$	41			
05	07	42	32	RVRε C1	$\overline{\text{ROUT A2}}$	88	05	20	51
05	07	92	82	RVRε D1	$\overline{\text{ROUT D2}}$	39	05	20	53
					$\overline{\text{ROUT C2}}$	36	05	20	55
					$\overline{\text{ROUT D2}}$	37	05	20	13
					$\overline{\text{ROUT A3}}$	80	05	20	15
					$\overline{\text{ROUT B3}}$	31	05	20	17
					$\overline{\text{ROUT C3}}$	78	05	20	19
					$\overline{\text{ROUT D3}}$	29	05	20	21
					$\overline{\text{ROUT A4}}$	73	05	20	23
							06	12	07

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	07	+5V : PIN 5 GND : PIN 3 BIN/BOUT SEE 0609
TYPE		GROUP	SHEET	
JC244	READOUT REGISTER, RVRε SHEET 1	12	130	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				$\overline{\text{ROUTεB4}}$	24	05	20	25
						06	12	08
				$\overline{\text{ROUTεC4}}$	71	05	20	27
						06	12	09
				$\overline{\text{ROUTεD4}}$	22	05	20	29
						06	12	10
				$\overline{\text{ROUTεA5}}$	65	05	20	31
						06	12	01
				$\overline{\text{ROUTεB5}}$	16	05	20	33
						06	12	02
				$\overline{\text{ROUTεC5}}$	13	05	20	35
						06	12	04
				$\overline{\text{ROUTεD5}}$	14	05	20	37
						06	12	06
				$\overline{\text{ROUTεA6}}$	58			
				$\overline{\text{ROUTεB6}}$	9			
				$\overline{\text{ROUTεC6}}$	56			
				$\overline{\text{ROUTεD6}}$	7			
				CLEAR RVRε	18	05	07	25
				$\overline{\text{CLEAR RVRε}}$	19	06	06	32
						05	07	69

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	07	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC244	READOUT REGISTER, RVRε SHEET 2	12	130	
		PART 1		

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			53		BEε 1	91			
			54		BEε 1	40			
			67		BEε 1	90			
			76		BEε 1	93			
			4		BEε 2	38			
			6		BEε 2	89	05	12	21
			21		BEε 2	35			
			83		BEε 2	87			
			11		BEε 3	30			
			61		BEε 3	81	05	12	20
			84		BEε 3	27			
			85		BEε 3	79			
			10		BEε 4	23			
			60		BEε 4	74	05	12	19
			86		BEε 4	20			
			75		BEε 4	72			
			51		BEε 5	15			
			52		BEε 5	66	05	12	06
			28		BEε 5	12			
			26		BEε 5	64			
			1		BEε 6	8			
			2		BEε 6	59	05	12	04
			17		BEε 6	55			
			77		BEε 6	57			
			25	GND	$\overline{\text{BRε A1}}$	44			
06	06	36	63	$\overline{\text{BRε-CP}}$	BRε A1	94	05	12	25
05	10	25	68	$\overline{\text{CLEAR BRε}}$	$\overline{\text{BRε B1}}$	95			
			62	GND	BRε B1	45			
			70		$\overline{\text{BRε C1}}$	42			
			69		BRε C1	43			
06	16	44	34	$\overline{\text{SUMA}}$	$\overline{\text{BRε D1}}$	92			
06	16	43	33	$\overline{\text{SUMB}}$	BRε D1	41			
06	16	42	32	$\overline{\text{SUMC}}$	BRε A2	88	05	12	15
06	16	41	82	$\overline{\text{SUMD}}$	BRε B2	39	05	12	16
					BRε C2	36	05	12	17
					BRε D2	37	05	12	18
					BRε A3	80	05	12	11
					BRε B3	31	05	12	12
					BRε C3	78	05	12	13
					BRε D3	29	05	12	14
					BRε A4	73	05	12	07
					BRε B4	24	05	12	08
					BRε C4	71	05	12	09
					BRε D4	22	05	12	10
					BRε A5	65			
					BRε B5	16			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	08	+5V : PIN 5 GND : PIN 3
TYPE	BUFFER REGISTER, BRε SHEET 1	GROUP	SHEET	
JC244		12	131	
		PART 1		

Iss. 1

LOCATION	PIN	SIGNAL	PIN	LOCATION
		BRε C5	13	
		BRε D5	14	
		BRε A6	58	
		BRε B6	9	
		BRε C6	56	
		BRε D6	7	
			18	
			19	

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	08	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC244	BUFFER REGISTER , BRε SHEET 2	12	131	
				PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	06	41	1	$\overline{\text{NVR}\varepsilon\rightarrow\text{BIN}}$					
06	06	40	2	$\overline{\text{AVR}\varepsilon\rightarrow\text{BIN}}$					
06	06	22	4	$\overline{\text{RVR}\varepsilon\rightarrow\text{BOUT}}$	SOL	33	05	12	22
							05	21	29
06	05	42	6	$\overline{\text{SIG NVR}\varepsilon\text{-CP}}$		34			
06	17	37	7	BDINA		35			
06	06	25	8	$\overline{\text{BOUT}\rightarrow\text{NVR}\varepsilon}$		36			
05	04	19	9	$\overline{\text{CLEAR AVR}\varepsilon}$		37			
06	06	28	10	$\overline{\text{BOUT}\rightarrow\text{AVR}\varepsilon}$	SIG BRε-S	38	05	12	23
							05	13	67
06	06	26	11	$\overline{\text{BOUT}\rightarrow\text{RVR}\varepsilon}$	$\overline{\text{SIG BR}\varepsilon\text{-S}}$	39	05	12	24
06	18	38	12	MIR:0	$\overline{\text{SIG RVR}\varepsilon\text{-S}}$	40	05	20	39
							06	10	14
06	18	20	13	AC→BOUT	SIG RVRε-S	41	05	13	73
							05	20	43
			14		$\overline{\text{SIG AVR}\varepsilon\text{-S}}$	42	05	01	07
			15		SIG AVRε-S	43	05	13	62
			16		SIG BIN	44	04	09	44
06	02	39	17	MISC:3	NVRεSIG-S	45	05	13	17
			18						
			19						
			20						
			21						
06	04	33	22	$\overline{\text{CP}}$					
06	15	24	23	$\overline{\text{ERROR}\varepsilon}$					
			24						
			25						
06	15	28	26	MIED:7					
05	22	18	27	$\overline{\text{PRES}\varepsilon}$					
05	22	15	28	$\overline{\text{SCAN}}$					
			29						
			30						
06	15	43	31	$\overline{\text{GR}}$					

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	09	+5V : PIN 5 GND : PIN 3
TYPE	STORE SERVO ON LINE CONTROL, SOLε	GROUP	SHEET	
JC218		12	132	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	17	42	1	CSRε -S					
06	17	40	2	DEL -CP					
06	17	38	4	IPE					
06	17	39	6	CPE-CP	CPEε-S	21	05	21	21
06	03	36	7	PRESε	SELεSIG	22	05	19	43
06	04	20	8	NPO			06	05	11
		39	9	SELε 1			05	13	11
		44	10	SELε 6					
05	23	17	11	CSP					
05	23	18	12	CSA	CLEAR BRε	25	05	08	68
05	23	15	13	CSB					
05	23	16	14	CSC	TP	27			
05	23	13	15	CSD					
			17		S DIN D	29	04	10	29
06	16	30	18	RESER					
					S DIN C	30	04	10	30
06	15	43	20	GR					
			21		S DIN B	31	04	10	31
			22						
					S DIN A	32	04	10	32
					S DIN P	33	04	10	33
					CSSHε	34	06	05	09
					CPEε-S	35	06	15	21
							06	17	10
					EOTε	36	06	17	11
					EOTε	37	05	13	68
					SELε 1	38	05	13	10
					SELε 1	39	05	13	09
					SELε 2	40	05	19	41
							05	13	61
					SELε 3	41	05	19	39
							05	13	79
					SELε 4	42	05	19	37
							05	13	27
					SELε 5	43	05	19	35
							05	13	81
					SELε 6	44	05	19	10
					SELε 6	45	05	13	30

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	10	+5V : PIN 5
TYPE		GROUP	SHEET	GND : PIN 3
JC 218	CODE-SWITCH REQUEST, CRε	12	133	
				PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	08	59	4	BEε 6	$\overline{\text{BE}\alpha}$	27	06	02	14
05	08	66	6	BEε 5	DOUTε A1	28	05	19	45
05	08	73	7	BRε A4	DOWN	29	05	19	33
05	08	24	8	BRε B4	UP	30	05	19	31
05	08	71	9	BRε C4	DACOε	31			
05	08	22	10	BRε D4	DAFIε	32			
05	08	80	11	BRε A3	BEε	33			
05	08	31	12	BRε B3	DOUTε D2	34	05	19	13
05	08	78	13	BRε C3	DOUTε C2	35	05	19	55
05	08	29	14	BRε D3	DOUTε B2	36	05	19	53
05	08	88	15	BRε A2	DOUTε A2	37	05	19	51
05	08	39	16	BRε B2	DOUTε D3	38	05	19	21
05	08	36	17	BRε C2	DOUTε C3	39	05	19	19
05	08	37	18	BRε D2	DOUTε B3	40	05	19	17
05	08	74	19	BEε 4	DOUTε A3	41	05	19	15
05	08	81	20	BEε 3	DOUTε D4	42	05	19	29
05	08	89	21	BEε 2	DOUTε C4	43	05	19	27
05	09	33	22	SOLε	DOUTε B4	44	05	19	25
05	09	38	23	$\overline{\text{SIG BR}\varepsilon\text{-S}}$	DOUTε A4	45	05	19	23
05	09	39	24	$\overline{\text{SIG BR}\varepsilon\text{-S}}$					
05	08	94	25	BRε A1					

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	12	+5V : PIN 5 GND : PIN 3
TYPE JC218	DIFFERENCE OUTPUT AND CONTROLS, DOUTε	GROUP 12	SHEET 135	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
05	01	41	1	MIDCε:2				
04	10	22	4	SELα SIG				
05	02	30	8	ADD 0				
05	10	38	10	SELε 0.001°				
05	10	22	11	SELε SIG				
05	02	41	12	ADD 1000				
05	03	40	13	CCTC				
05	03	41	14	CCTD				
05	02	39	15	ADD 250				
05	03	39	16	CCTB				
05	09	45	17	SIG NVRε-S				
06	05	35	18	CC20				
06	05	36	19	CR21				
05	13	42	20	SELα 1°				
05	13	45	23	SELα 100°				
04	01	43	26	MIDCα:1				
05	13	42	27	SELε 1°				
04	09	43	28	SIG AVRα-S				
05	13	45	30	SELε 100°				
05	02	38	31	ADD 120				
05	02	36	32	ADD 30				
05	02	35	33	ADD 16				
05	02	34	34	ADD 8				
04	09	38	35	SIG BRα-S				
04	03	40	42	CCUC				
04	03	38	44	CCUA				
04	01	39	51	MIDCα:3				
05	13	40	52	SELα 0.01°				
05	13	39	53	SELα 0.001°				
05	02	32	55	ADD 2				
05	02	33	57	ADD 4				
05	02	31	59	ADD 1				
05	13	37	60	EOTα				
05	13	40	61	SELε 0.01°				
05	09	43	62	SIG AVRε-S				
05	01	39	63	MIDOε:3				
05	02	42	64	ADD 2000				
05	03	38	65	CCTA				
05	02	40	66	ADD 500				
05	09	38	67	SIG BRε-S				
05	13	37	68	EOTε				
06	05	39	69	CC21				
			70					
04	10	41	72	SELα 0.1°				
05	09	41	73	SIG RVRε-S				
05	13	43	74	SELα 10°				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	13	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
	SIGNAL DISPLAY ADAPTER SHEET 1	12	136	
				PART 1

Iss. 1

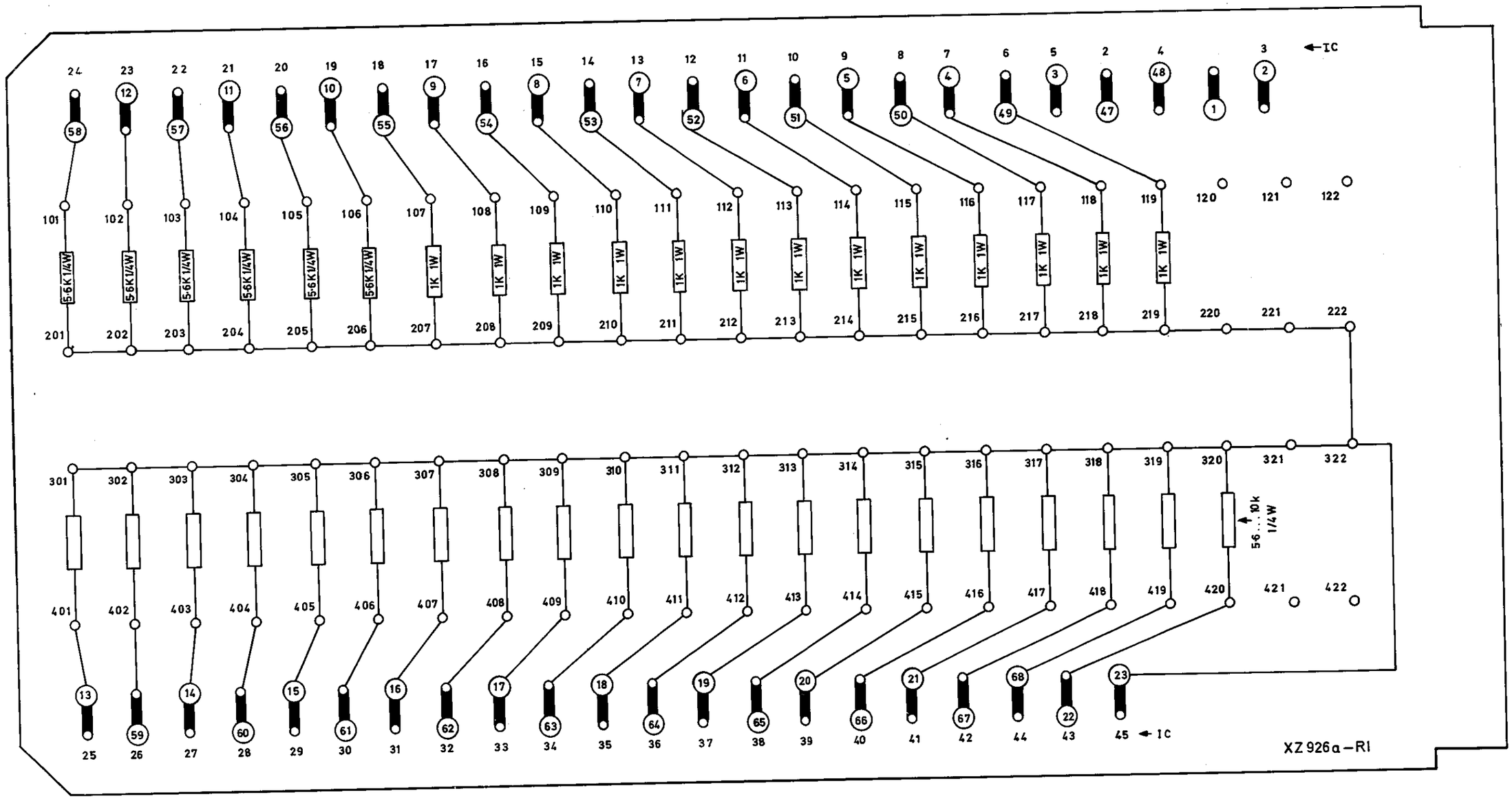
LOCATION			PIN	SIGNAL	PIN	LOCATION		
04	01	45	75	MIDCα:0				
05	01	43	76	MIDCε:1				
05	01	45	77	MIDCε:0				
05	13	41	79	SELε 0.1°				
			80					
05	13	43	81	SELε 10°				
05	02	37	82	ADD 60				
04	01	41	83	MIDCα 2				
04	09	41	87	SIG RVRα-S				
04	09	45	89	SIG NVRα-S				
04	03	41	92	CCUD				
04	03	39	95	CCUB				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	13	+5V : PIN 5 GND : PIN
TYPE		GROUP	SHEET	
	SIGNAL DISPLAY ADAPTER SHEET 2	12	136	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				DOUTε A4	6	05	19	73
				DOUTε B4	7	05	19	75
				DOUTε C4	8	05	19	77
				DOUTε D4	9	05	19	79
				DOUTε A3	10	05	19	65
				DOUTε B3	11	05	19	67
				DOUTε C3	12	05	19	69
				DOUTε D3	13	05	19	71
				DOUTε A2	14	05	19	57
				DOUTε B2	15	05	19	59
				DOUTε C2	16	05	19	61
				DOUTε D2	17	05	19	63
				DOUTε A1	18	05	19	95
				ROUTε A2	25	05	20	57
				ROUTε B2	26	05	20	59
				ROUTε C2	27	05	20	61
				ROUTε D2	28	05	20	63
				ROUTε A3	29	05	20	65
				ROUTε B3	30	05	20	67
				ROUTε C3	31	05	20	69
				ROUTε D3	32	05	20	71
				ROUTε A4	33	05	20	73
				ROUTε B4	34	05	20	75
				ROUTε C4	35	05	20	77
				ROUTε D4	36	05	20	79
				ROUTε A5	37	05	20	81
				ROUTε B5	38	05	20	83
				ROUTε C5	39	05	20	85
				ROUTε D5	40	05	20	87
				SEC 20	19	04	21	63
				SEC 21	20	04	21	65
				SEC 22	21	04	21	67
				SEC 23	22	04	21	69
				SEC 24	23	04	21	71
				SEC 25	24	04	21	73

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	18	+24V : PIN 45
TYPE		GROUP	SHEET	
XZ926	ISOLATING DIODES	12	141	
		PART 1		



Card u0518 XZ926a – R1: Component Layout

Figure 12 – 142 Location u0518

Part 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	12	37	51	DOUTε A2	$\overline{\text{DOUTε A2}}$	57	05	61	09
							05	18	14
05	12	36	53	DOUTε B2	$\overline{\text{DOUTε B2}}$	59	05	61	19
							05	18	15
05	12	35	55	DOUTε C2	$\overline{\text{DOUTε C2}}$	61	05	61	29
							05	18	16
05	12	34	13	DOUTε D2	$\overline{\text{DOUTε D2}}$	63	05	61	39
							05	18	17
05	12	41	15	DOUTε A3	$\overline{\text{DOUTε A3}}$	65	05	61	08
							05	18	10
05	12	40	17	DOUTε B3	$\overline{\text{DOUTε B3}}$	67	05	61	18
							05	18	11
05	12	39	19	DOUTε C3	$\overline{\text{DOUTε C3}}$	69	05	61	28
							05	18	12
05	12	38	21	DOUTε D3	$\overline{\text{DOUTε D3}}$	71	05	61	38
							05	18	13
05	12	45	23	DOUTε A4	$\overline{\text{DOUTε A4}}$	73	05	61	07
							05	18	06
05	12	44	25	DOUTε B4	$\overline{\text{DOUTε B4}}$	75	05	61	17
							05	18	07
05	12	43	27	DOUTε C4	$\overline{\text{DOUTε C4}}$	77	05	61	27
							05	18	08
05	12	42	29	DOUTε D4	$\overline{\text{DOUTε D4}}$	79	05	61	37
							05	18	09
05	12	30	31	UP	$\overline{\text{UP}}$	81	05	62	23
05	12	29	33	DOWN	$\overline{\text{DOWN}}$	83	05	62	33
05	10	43	35	SELε 2	$\overline{\text{SELε 2}}$	85	05	61	05
05	10	42	37	SELε 3	$\overline{\text{SELε 3}}$	87	05	61	15
05	10	41	39	SELε 3	$\overline{\text{SELε 4}}$	89	05	61	25
05	10	40	41	SELε 4	$\overline{\text{SELε 5}}$	91	05	61	35
05	10	22	43	SELε SIG	$\overline{\text{SELε SIG}}$	93	05	61	14
05	12	28	45	DOUTε A1	$\overline{\text{DOUTε A1}}$	95	05	61	06
							05	18	18

INTERCONNECTION TABLE

RACK	LOC'N
05	19

NOTES

+5V : PIN 5
GND : PIN 3

TYPE	
LT234	SELECTION CODE SWITCH, DOUTε

GROUP	SHEET
12	143

PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	07	88	51	ROUTε A2	ROUTε A2	57	05	61	03
							05	18	25
05	07	39	53	ROUTε B2	ROUTε B2	59	05	61	13
							05	18	26
05	07	36	55	ROUTε C2	ROUTε C2	61	05	61	23
							05	18	27
05	07	37	13	ROUTε D2	ROUTε D2	63	05	61	33
							05	18	28
05	07	80	15	ROUTε A3	ROUTε A3	65	05	61	02
							05	18	29
05	07	81	17	ROUTε B3	ROUTε B3	67	05	61	12
							05	18	30
05	07	78	19	ROUTε C3	ROUTε C3	69	05	61	22
							05	18	31
05	07	29	21	ROUTε D3	ROUTε D3	71	05	61	32
							05	18	32
05	07	73	23	ROUTε A4	ROUTε A4	73	05	61	01
							05	18	33
05	07	24	25	ROUTε B4	ROUTε B4	75	05	61	11
							05	18	34
05	07	71	27	ROUTε C4	ROUTε C4	77	05	61	21
							05	18	35
05	07	22	29	ROUTε D4	ROUTε D4	79	05	61	31
							05	18	36
05	07	65	31	ROUTε A5	ROUTε A5	81	05	61	00
							05	18	37
05	07	16	33	ROUTε B5	ROUTε B5	83	05	61	10
							05	18	38
05	07	13	35	ROUTε C5	ROUTε C5	85	05	61	20
							05	18	39
05	07	14	37	ROUTε D5	ROUTε D5	87	05	61	30
							05	18	40
05	09	40	39	SIG RVRε	SIG RVRε	89	05	61	34
			41			91			
05	09	41	43	SIG RVRε	SIG RVRε	93	05	61	24
							04	18	20
			45			95			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	20	+5V : PIN 5 GND: PIN 3
TYPE		GROUP	SHEET	
LT234	READOUTε	12	144	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	16	39	51	REDIG	$\overline{\text{REDIG}}$	57	05	62	09
06	16	34	53	ILL SEQ	$\overline{\text{ILL SEQ}}$	59	05	62	19
06	16	31	55	$\overline{\text{NCL}}$	NCL	61	05	62	29
04	21	85	13	ERROR2	POWER	63	05	62	39
06	02	29	15	TPU/LOST	$\overline{\text{TPU/LOST}}$	65	05	62	08
06	02	28	17	SPWA	$\overline{\text{SPWA}}$	67	05	62	18
04	10	21	19	CPEα-S	$\overline{\text{CPEα-S}}$	69	05	62	28
05	10	21	21	CPEε-S	$\overline{\text{CPEε-S}}$	71	05	62	38
06	15	27	23	MSEα	$\overline{\text{MSEα}}$	73	05	62	07
06	15	26	25	MSEε	$\overline{\text{MSEε}}$	75	05	62	17
04	09	33	27	SOLα	$\overline{\text{SOLα}}$	77	05	62	27
05	09	33	29	SOLε	$\overline{\text{SOLε}}$	79	05	62	37
			31			81			
			33			83			
			35			85			
			37			87			
			39			89			
			41			91			
			43			93			
			45			95			
05	62	25	09	$\overline{\text{LAMPCH}}$					
05	62	25	11	$\overline{\text{LAMPCH}}$					

INTERCONNECTION TABLE

RACK	LOC'N	NOTES
05	21	+5V : PIN 5 GND : PIN 3

TYPE		GROUP	SHEET
LT234	TEST PANEL LAMP DRIVER	12	145

PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	24	11	35	$\overline{\text{PRES}\alpha}$		17	06	18	12
							04	09	27
05	24	14	36	$\overline{\text{PRES}\varepsilon}$		18	06	18	13
							05	09	27
05	24	17	37	$\overline{\text{SCAN}}$		15	06	18	14
							04	09	28
							05	09	28
05	24	20	38	$\overline{\text{SPU}}$		16	06	05	29
05	62	15	39	$\overline{\text{TEST MES}}$		13	06	15	07
05	62	05	40	$\overline{\text{ERRES}}$		14	06	16	11
			41			11			
			42			12			
			43			9			
			44			10			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	22	+5V : PIN 5, 45 GND : PIN 3
TYPE	MODE INPUT	GROUP	SHEET	
UT 230		12	146	
		PART 1		

Iss. 1

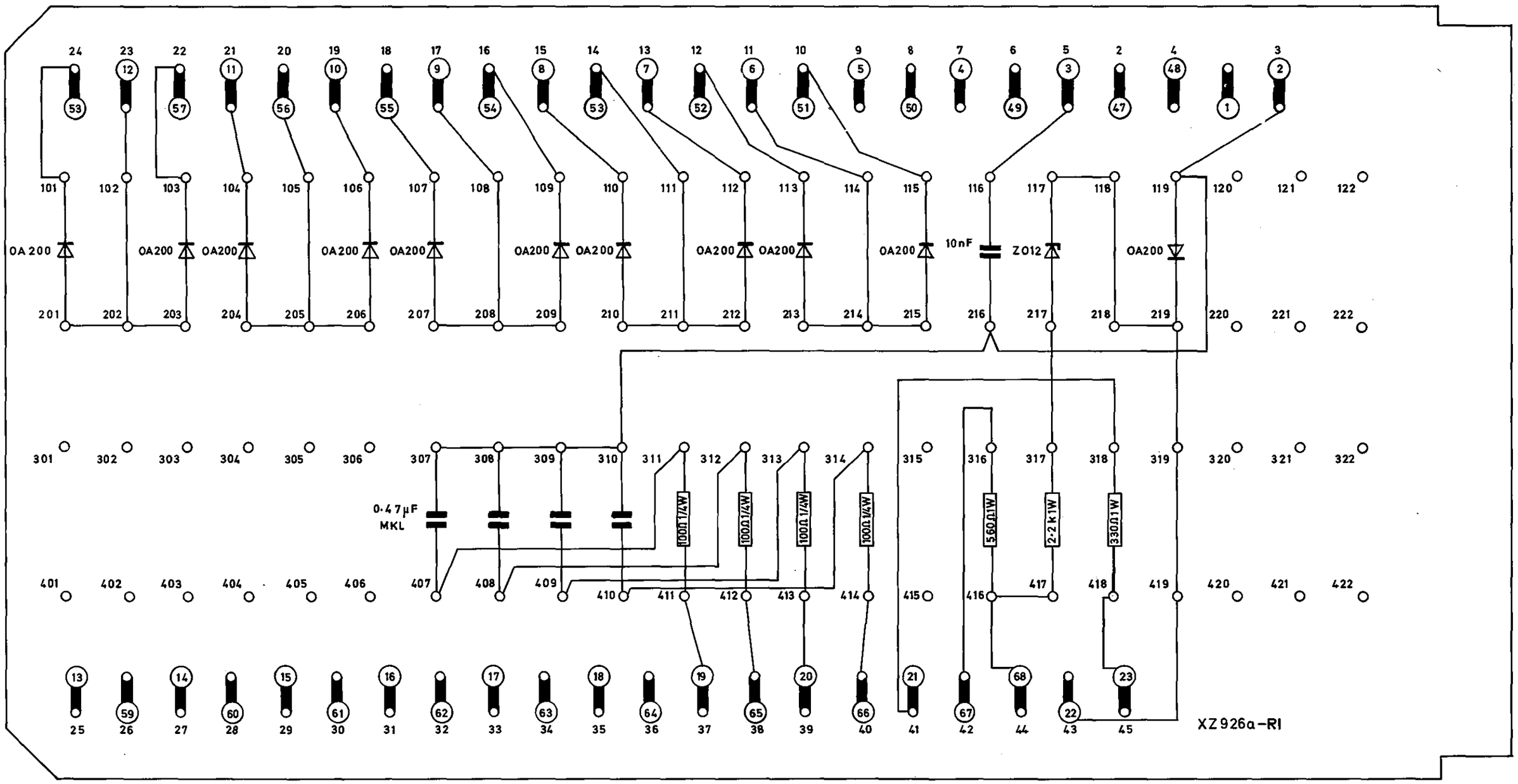
LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	63	06	35	$\overline{CSP}$		17	05	10	11
							04	10	11
05	63	07	36	$\overline{CSA}$		18	05	10	12
							04	10	12
05	63	17	37	$\overline{CSB}$		15	05	10	13
							04	10	13
05	63	27	38	$\overline{CSC}$		16	05	10	14
							04	10	14
05	63	27	39	$\overline{CSD}$		13	05	10	15
							04	10	15
05	62	35	40	TEST GND		14	06	02	26
			41			11			
			42			12			
			43			9			
			44			10			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	23	+5V :PIN 5,45 GND :PIN 3
TYPE		GROUP	SHEET	
UT230	CODE SWITCH INPUTS	12	147	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
					CYST	6	06	02	27
05	63	09	10	$\overline{\text{MPRES}\alpha}$	$\overline{\text{PRES}\alpha}$	11	05	22	35
05	62	06	12	$\overline{\text{TPRES}\alpha}$					
05	63	19	13	$\overline{\text{MPRES}\varepsilon}$	$\overline{\text{PRES}\varepsilon}$	14	05	22	36
05	62	16	15	$\overline{\text{TPRES}\varepsilon}$					
05	63	29	16	$\overline{\text{MSCAN}}$	$\overline{\text{SCAN}}$	17	05	22	37
05	62	26	18	$\overline{\text{TSCAN}}$					
05	63	39	19	$\overline{\text{MSPU}}$	SPU	20	05	22	38
05	62	36	21	$\overline{\text{TSPU}}$					
					SCAN		06	18	35
					PRESε		06	18	34
					PRESα		06	18	33
					* +15V	41	06	04	07
					** -15V	42	06	04	01
			44	-24V	ER-24V	43	04	21	25
							04	21	35
			45	+24V					

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	24	* 66 ...80 mA ** 39 ...47 mA
TYPE		GROUP	SHEET	
XZ 926	MODES AND SPU INPUTS	12	148	
		PART 1		



Card u0524 XZ 926a-R1 : Component Layout

Figure 12–149 Location u0524

Part 1

Iss. 1

		9	8	7	6	5	4	3	2	1	0
61	0	DOUTε A2	DOUTε A3	DOUTε A4	DOUTε A1	SELε 5		ROUTε A2	ROUTε A3	ROUTε A4	ROUTε A5
	1	DOUTε B2	DOUTε B3	DOUTε B4		SELε 4	SELεSIG	ROUTε B2	ROUTε B3	ROUTε B4	ROUTε B5
	2	DOUTε C2	DOUTε C3	DOUTε C4		SELε 3	$\overline{\text{SIG RVRε-S}}$	ROUTε C2	ROUTε C3	ROUTε C4	ROUTε C5
	3	DOUTε D2	DOUTε D3	DOUTε D4		SELε 2	SIG RVRε-S	ROUTε D2	ROUTε D3	ROUTε D4	ROUTε D5
62	0	REDIG	TPU/LOST	MSEα	TPRESα	ERRES		CW		SEC20	SEC24
	1	ILLSEQ	SPWA	MSEε	TPRESε	TEST MES		CCW		SEC21	SEC25
	2	MANCL	CPEα	SOLα	TSCAN	LAMPCH		UP		SEC22	
	3	POWER	CPE	SOLε	TSPU	TEST GND		DOWN		SEC23	
63	0	PRESα	REMGND	$\overline{\text{CSA}}$	$\overline{\text{CSP}}$						
	1	PRESε		$\overline{\text{CSB}}$							
	2	SCAN		$\overline{\text{CSC}}$							
	3	SPU		$\overline{\text{CSD}}$							
64	0								COARSEε	AVRα A6	$\overline{\text{AVRα A6}}$
	1								MEDIUMε	AVRα B6	$\overline{\text{AVRα B6}}$
	2								DIR1ε	AVRα C6	$\overline{\text{AVRα C6}}$
	3								DIR2ε	AVRα D6	$\overline{\text{AVRα D6}}$
65	0	AVRε A1	$\overline{\text{AVRε A1}}$	AVRε A2	$\overline{\text{AVRε A2}}$	AVRε A3	$\overline{\text{AVRε A3}}$	AVRε A4	$\overline{\text{AVRε A4}}$	AVRε A5	$\overline{\text{AVRε A5}}$
	1	AVRε B1	$\overline{\text{AVRε B1}}$	AVRε B2	$\overline{\text{AVRε B2}}$	AVRε B3	$\overline{\text{AVRε B3}}$	AVRε B4	$\overline{\text{AVRε B4}}$	AVRε B5	$\overline{\text{AVRε B5}}$
	2	AVRε C1	$\overline{\text{AVRε C1}}$	AVRε C2	$\overline{\text{AVRε C2}}$	AVRε C3	$\overline{\text{AVRε C3}}$	AVRε C4	$\overline{\text{AVRε C4}}$	AVRε C5	$\overline{\text{AVRε C5}}$
	3	AVRε D1	$\overline{\text{AVRε D1}}$	AVRε D2	$\overline{\text{AVRε D2}}$	AVRε D3	$\overline{\text{AVRε D3}}$	AVRε D4	$\overline{\text{AVRε D4}}$	AVRε D5	$\overline{\text{AVRε D5}}$

INTERCONNECTION TABLE		RACK 05	LOC'N	NOTES
TYPE		GROUP 12	SHEET 150	
	SUB-RACK TERMINAL BLOCK			PART 1

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
05	20	81	00	ROUTε A5		01	66	10
05	20	73	01	ROUTε A4		01	66	00
05	20	65	02	ROUTε A3		01	65	14
05	20	57	03	ROUTε A2		01	65	04
			04					
05	19	85	05	SELε 5		01	67	03
05	19	95	06	DOUTε A1		01	66	17
05	19	73	07	DOUTε A4		01	65	20
05	19	65	08	DOUTε A3		01	65	10
05	19	57	09	DOUTε A2		01	65	00
05	20	83	10	ROUTε B5		01	66	11
05	20	75	11	ROUTε B4		01	66	01
05	20	67	12	ROUTε B3		01	65	15
05	20	59	13	ROUTε B2		01	65	05
05	19	93	14	SELε SIG		01	67	04
05	19	87	15	SELε 4		01	67	02
			16					
05	19	75	17	DOUTε B4		01	65	21
05	19	67	18	DOUTε B3		01	65	11
05	19	59	19	DOUTε B2		01	65	01
05	20	85	20	ROUTε C5		01	66	12
05	20	77	21	ROUTε C4		01	66	02
05	20	69	22	ROUTε C3		01	65	16
05	20	61	23	ROUTε C2		01	65	06
05	20	93	24	SIG RVRε-S		01	62	07
05	19	89	25	SELε 3		01	67	01
			26					
05	19	77	27	DOUT C4		01	65	22
05	19	69	28	DOUT C3		01	65	12
05	19	61	29	DOUT C2		01	65	02
05	20	87	30	ROUT D5		01	66	13
05	20	79	31	ROUT D4		01	66	03
05	20	71	32	ROUT D3		01	65	17
05	20	63	33	ROUT D2		01	65	07
05	20	89	34	$\overline{\text{SIG RVRε-S}}$		01	62	06
05	19	91	35	SELε 2		01	67	00
			36					
05	19	79	37	DOUTε D4		01	65	23
05	19	71	38	DOUTε D3		01	65	13
05	19	63	39	DOUTε D2		01	65	03

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	61	
TYPE	TERMINAL BLOCK 0561	GROUP	SHEET	
		12	151	
		PART 1		

LOCATION			PIN	SIGNAL	PIN	LOCATION		
04	21	73	00	SEC24		01	66	14
04	21	63	01	SEC20		01	66	04
			02					
04	19	81	03	CW		02	60	00
			04					
02	60	11	05	$\overline{\text{ERRES}}$		05	22	40
02	60	05	06	TPRESα		05	24	12
05	21	73	07	MSEα		02	60	13
05	21	65	08	TPU/LOST		02	60	19
05	21	57	09	REDIG		02	60	15
04	21	75	10	SEC25		01	66	15
04	21	65	11	SEC21		01	66	05
			12					
04	19	83	13	CCW		02	60	01
			14					
02	60	10	15	TEST MES		05	22	39
02	60	06	16	TPRESϵ		05	24	15
05	21	75	17	MSEϵ		02	60	14
05	21	67	18	SPWA		02	60	18
05	21	59	19	ILL SEQ		02	60	16
02	60	08	20	TPRO/TR				
04	21	67	21	SEC22		01	66	06
			22					
05	19	81	23	UP		02	60	02
			24					
02	60	12	25	$\overline{\text{LAMP CH}}$		05	21	09
						05	21	11
02	60	07	26	TSCAN		05	24	18
05	21	79	27	SOLα		02	60	21
05	21	71	28	CPEα-S		02	60	23
05	21	61	29	NCC-S		02	60	20
			30					
			31	SEC23		01	66	07
			32					
05	19	85	33	DOWN		02	60	03
			34					
02	60	47	35	TEST GND		05	23	40
02	60	09	36	TSPU		05	24	21
05	21	79	37	SOLϵ		02	60	22
05	21	73	38	CPEϵ-S		02	60	24
05	21	63	39	POWER		02	60	17

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	62	
TYPE		GROUP	SHEET	
	TERMINAL BLOCK 0562	12	152	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
			00					
			01					
			02					
			03					
			04					
			05					
01	67	24	06	$\overline{\text{CSP}}$		05	23	35
01	67	20	07	$\overline{\text{CSA}}$		05	23	36
			08					
01	67	10	09	MPRESα		05	24	10
			10					
			11					
			12					
			13					
			14					
			15					
			16					
01	67	21	17	$\overline{\text{CSB}}$		05	23	37
			18					
01	67	11	19	MPRESε		05	24	13
			20					
			21					
			22					
			23					
			24					
			25					
			26					
01	67	22	27	$\overline{\text{CSC}}$		05	23	38
			28					
01	67	12	29	MSCAN		05	24	16
			30					
			31					
			32					
			33					
			34					
			35					
			36					
01	67	23	37	$\overline{\text{CSD}}$		05	23	39
			38					
01	67	13	39	MSPU		05	24	19

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	63	
TYPE	TERMINAL BLOCK 0563	GROUP 12	SHEET 153	
		PART 1		

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	04	58	00	$\overline{\text{AVRε A6}}$			08	64	00
05	05	41	01	AVRε A6			08	64	01
08	64	02	02	COARSE			04	24	37
			03						
			04						
			05						
			06						
			07						
			08						
			09						
05	04	09	10	$\overline{\text{AVRε B6}}$			08	64	10
05	05	42	11	AVRε B6			08	64	11
08	64	12	12	MEDIUMε			04	24	38
			13						
			14						
			15						
			16						
			17						
			18						
			19						
05	04	56	20	$\overline{\text{AVRε C6}}$			08	64	20
05	05	43	21	AVRε C6			08	64	21
08	64	22	22	DIR1ε			04	24	41
			23						
			24						
			25						
			26						
			27						
			28						
			29						
05	05	07	30	$\overline{\text{AVRε D6}}$			08	64	30
05	05	44	31	AVRε D6			08	64	31
08	64	32	32	DIR2ε			04	24	42
			33						
			34						
			35						
			36						
			37						
			38						
			39						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	64	
TYPE	TERMINAL BLOCK 0564	GROUP	SHEET	
		12	154	
		PART 1		

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	04	65	00	$\overline{\text{AVRE A5}}$			08	65	00
05	05	37	01	AVRE A5			08	65	01
05	04	73	02	$\overline{\text{AVRE A4}}$			08	65	02
05	05	33	03	AVRE A4			08	65	03
05	04	80	04	$\overline{\text{AVRE A3}}$			08	65	04
05	05	29	05	AVRE A3			08	65	05
05	04	88	06	$\overline{\text{AVRE A2}}$			08	65	06
05	04	25	07	AVRE A2			08	65	07
05	04	94	08	$\overline{\text{AVRE A1}}$			08	65	08
05	04	44	09	AVRE A1			08	65	09
05	04	16	10	$\overline{\text{AVRE B5}}$			08	65	10
05	04	38	11	AVRE B5			08	65	11
05	04	24	12	$\overline{\text{AVRE B4}}$			08	65	12
05	05	34	13	AVRE B4			08	65	13
05	04	31	14	$\overline{\text{AVRE B3}}$			08	65	14
05	05	30	15	AVRE B3			08	65	15
05	04	39	16	$\overline{\text{AVRE B2}}$			08	65	16
05	05	26	17	AVRE B2			08	65	17
05	04	45	18	$\overline{\text{AVRE B1}}$			08	65	18
05	04	95	19	AVRE B1			08	65	19
05	04	13	20	$\overline{\text{AVRE C5}}$			08	65	20
05	05	39	21	AVRE C5			08	65	21
05	04	71	22	$\overline{\text{AVRE C4}}$			08	65	22
05	05	35	23	AVRE C4			08	65	23
05	04	78	24	$\overline{\text{AVRE C3}}$			08	65	24
05	05	31	25	AVRE C3			08	65	25
05	04	36	26	$\overline{\text{AVRE C2}}$			08	65	26
05	05	27	27	AVRE C2			08	65	27
05	04	43	28	$\overline{\text{AVRE C1}}$			08	65	28
05	04	42	29	AVRE C1			08	65	29
05	04	14	30	$\overline{\text{AVRE D5}}$			08	65	30
05	05	40	31	AVRE D5			08	65	31
05	04	22	32	$\overline{\text{AVRE D4}}$			08	65	32
05	05	36	33	AVRE D4			08	65	33
05	04	29	34	$\overline{\text{AVRE D3}}$			08	65	34
05	05	32	35	AVRE D3			08	65	35
05	04	37	36	$\overline{\text{AVRE D2}}$			08	65	36
05	05	28	37	AVRE D2			08	65	37
05	04	41	38	$\overline{\text{AVRE D1}}$			08	65	38
05	04	92	39	AVRE D1			08	65	39

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		05	65	
TYPE		GROUP	SHEET	
	TERMINAL BLOCK 0565	12	155	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			4		$\overline{\text{MIR:1}}$	8	06	06	04
							06	10	27
			6			9			
06	04	37	7	CP2	$\overline{\text{MIR:6}}$	10	06	06	10
							06	10	30
			11		dcp2	12	06	02	17
							06	05	24
							06	06	16
			13			17			
			14		$\overline{\text{MIRS:1}}$	21	06	10	15
			15		$\overline{\text{MIRS:5}}$	22			
			16		$\overline{\text{MIRS:4}}$	23	06	01	08
			19		$\overline{\text{MIRS:2}}$	24	06	01	19
							06	10	17
			20		$\overline{\text{MIRS:6}}$	25			
			26			27			
			28			29			
			32			30			
06	15	44	33	GR		31			
06	04	33	34	$\overline{\text{CP}}$	MIRS:7	35	06	08	85
							06	13	70
			40		MIRS:3	36	06	10	08
							06	13	79
			42	GND	$\overline{\text{MIR:3}}$	37	06	06	07
							06	14	01
			45		MIRS:3	38	06	11	17
							06	11	29
							06	11	38
							06	11	41
							06	13	64
			51			39			
			53		MIR:7	41	06	10	13
							06	13	68
			55		$\overline{\text{MIR:7}}$	43	06	06	11
							06	14	08
			65		SH	44	06	08	57
			66			52			
			69			54			
			78		MIR:2	56	06	11	09
							06	11	13
							06	11	19
							06	13	27
							06	11	16
			79		$\overline{\text{MIR:2}}$	57	06	06	06
			80		MIR:1	58	04	02	06
							06	10	04
							06	13	81

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	07	+5V : PIN 5 GND : PIN
TYPE		GROUP	SHEET	
JC246	COMPUTING RANGE CONTROL AND SHIFT, CRC SHEET 1	12	162	
		PART 1		

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			81			59			
			82			60			
			83		MIR:6	61	06	11	22
							06	11	25
							06	13	11
							06	11	31
							06	11	28
06	08	84	86	START MIR	$\overline{\text{MIR:5}}$	62	06	06	09
							06	10	29
06	08	88	87	MIR+1		63			
			88		MIR:5	64	06	13	10
06	08	37	89	PERM MIRS	$\overline{\text{MIR:4}}$	67	06	06	08
							06	10	28
							06	18	16
			91		MIR:4	68	05	02	06
							06	10	10
							06	13	61
			92		MIRS:0	70	06	13	15
					$\overline{\text{MIRS:0}}$	71			
					MIRS:5	72	06	11	44
							06	13	69
					MIRS:1	73	06	13	66
						74			
					MIRS:4	75	06	10	21
							06	11	32
							06	13	18
					MIRS:2	76	06	13	12
					MIRS:6	77	06	11	20
							06	13	19
							06	08	66
						84			
					MIR:0	85	06	02	08
							06	08	80
							06	13	30
					$\overline{\text{MIR:0}}$	90	06	16	14
							06	18	17
							06	18	02
						93			
						94			
					$\overline{\text{MIRS:3}}$	95	06	01	13
							06	10	19

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	07	+5V : PIN 5 GND : PIN 3, 42
TYPE		GROUP	SHEET	
JC246	COMPUTING RANGE CONTROL AND SHIFT, CRC SHEET 2	12	162	PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
					SUMD	12	06	09	82
							06	16	06
06	09	44	6	AC A1	SUMC	13	06	09	32
							06	16	04
06	09	42	8	AC C1		14			
06	08	45	16	MIAD:3		15			
06	08	40	18	$\overline{\text{MIAD:1}}$	CA-S	17	06	13	65
06	08	33	19	$\overline{\text{MIAD:7}}$	AC→BOUT	20	06	06	19
							06	18	20
			21		$\overline{\text{ADD}}$	24	06	14	31
06	08	91	22	MIAD:1	$\overline{\text{SH-CP}}$	26	06	06	14
							06	09	63
							06	16	12
06	10	45	23	SIG AD	$\overline{\text{CLEAR DEL}}$	27	06	17	18
							06	17	19
06	04	29	25	CP	AD→ADDER	31	06	08	54
			28		$\overline{\text{MIAD:6}}$	32	06	06	18
							06	08	69
			29		$\overline{\text{MIAD:7}}$	33	06	08	19
05	01	38	30	$\overline{\text{MIDC}\varepsilon\text{:3}}$	PERM MIRS	37	06	07	89
			34			38			
			35		$\overline{\text{MIAD:0}}$	39	06	16	20
							06	18	06
06	04	29	36	CP	$\overline{\text{MIAD:1}}$	40	06	08	18
06	14	43	51	ADNDC	$\overline{\text{MIAD:2}}$	41	06	08	68
06	14	42	52	ADNDD	$\overline{\text{MIAD:5}}$	42			
06	14	44	53	ADNDB	$\overline{\text{MIAD:4}}$	44	06	06	17
06	08	31	54	AD→ADDER	MIAD:3	45	06	08	16
							06	13	72
06	14	45	55	ADNDA		59			
06	09	95	56	AC B1		60			
06	07	09	57	SH	REG→BIN	61	06	06	02
							06	10	31
06	09	92	58	AC DI	SUMB	62	06	09	33
							06	16	02
06	07	77	66	MIRS:6	SUMA	63	06	09	34
							06	16	01
06	08	41	68	$\overline{\text{MIAD:2}}$		64			
06	08	32	69	$\overline{\text{MIAD:6}}$	$\overline{\text{CA-S}}$	67			
06	18	40	70	SIG BIN	ADD	73	06	14	30
							06	13	13
06	18	42	71	BIN→AC	$\overline{\text{SIG AC-S}}$	75	06	06	15
							06	13	16
			72		SIG AC-S	76	04	09	14
							05	09	14
06	08	91	74	MIAD:1	MIAD:6	82	06	13	04

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	08	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC245	ADDER AND CONTROL, ADD SHEET 1	12	163	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	17	29	77	CLEAR DEL	MIAD:7	83	06	02	09
							06	03	04
							06	13	60
			78		START MIR	84	06	07	86
06	18	43	79	$\overline{\text{ATOP}}$	MIR:1	88	06	07	87
06	07	85	80	MIR 0	MIAD:0	90	06	13	23
			81		MIAD:1	91	06	08	22
							06	08	74
							06	13	74
06	07	35	85	MIRS:7	MIAD:2	92	06	13	20
06	15	44	86	GR	MIAD:5	93	06	13	53
			89		MIAD:4	94	06	13	52
					$\overline{\text{MIAD:3}}$	95	06	06	16

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	08	+5V PIN 5 GND PIN 3
TYPE		GROUP	SHEET	
JC245	ADDER AND CONTROL , ADD SHEET 2	12	163	
		PART 1		

DISPLAY ADAPTER	NVRε	AVRε	NVRα	AVRα	Pin	Signal	Signal	Pin	AVRα	NVRα	RVRα	RVRα	AVRε	NVRε	RVRε	RVRε	DISPLAY ADAPTER
04 13 77	05 06 08	05 04 91	04 06 08	04 04 91	53	BIN A1	BOUT A1	91	04 04 53	04 06 01	04 07 91	04 07 53	05 04 53	05 06 01	05 07 91	05 07 53	04 13 75
04 13 76	05 06 59	05 04 40	04 06 59	04 04 40	54	BIN B1	BOUT B1	40	04 04 54	04 06 02	04 07 40	04 07 54	05 04 54	05 06 02	05 07 40	05 07 54	04 13 26
04 13 01	05 06 55	05 04 90	04 06 55	04 04 90	67	BIN C1	BOUT C1	90	04 04 67	04 06 17	04 07 90	04 07 67	05 04 67	05 06 17	05 07 90	05 07 67	04 13 83
04 13 63	05 06 57	05 04 93	04 06 57	04 04 93	76	BIN D1	BOUT D1	93	04 04 76	04 06 77	04 07 93	04 07 76	05 04 76	05 06 77	05 07 93	05 07 76	04 13 51
04 13 34	05 06 15	05 04 38	04 06 15	04 04 38	4	BIN A2	BOUT A2	38	04 04 04	04 06 51	04 07 38	04 07 04	05 04 04	05 06 51	05 07 38	05 07 04	04 13 08
04 13 33	05 06 66	05 04 89	04 06 66	04 04 89	6	BIN B2	BOUT B2	89	04 04 06	04 06 52	04 07 89	04 07 06	05 04 06	05 06 52	05 07 89	05 07 06	04 13 59
04 13 32	05 06 12	05 04 35	04 06 12	04 04 35	21	BIN C2	BOUT C2	35	04 04 21	04 06 28	04 07 35	04 07 21	05 04 21	05 06 28	05 07 35	05 07 21	04 13 55
04 13 82	05 06 64	05 04 87	04 06 64	04 04 87	83	BIN D2	BOUT D2	87	04 04 83	04 06 26	04 07 87	04 07 83	05 04 83	05 06 26	05 07 87	05 07 83	04 13 57
04 13 18	05 06 23	05 04 30	04 06 23	04 04 30	11	BIN A3	BOUT A3	30	04 04 11	04 06 10	04 07 30	04 07 11	05 04 11	05 06 10	05 07 30	05 07 11	04 13 15
04 13 69	05 06 74	05 04 81	04 06 74	04 04 81	61	BIN B3	BOUT B3	81	04 04 61	04 06 60	04 07 81	04 07 61	05 04 61	05 06 60	05 07 81	05 07 61	04 13 66
04 13 19	05 06 20	05 04 27	04 06 20	04 04 27	84	BIN C3	BOUT C3	27	04 04 84	04 06 86	04 07 27	04 07 84	05 04 84	05 06 16	05 07 27	05 07 84	04 13 12
04 13 70	05 06 72	05 04 79	04 06 72	04 04 79	85	BIN D3	BOUT D3	79	04 04 85	04 06 75	04 07 79	04 07 85	05 04 85	05 06 75	05 07 79	05 07 85	04 13 64
04 13 52	05 06 30	05 04 23	04 06 30	04 04 23	10	BIN A4	BOUT A4	23	04 04 10	04 06 11	04 07 23	04 07 10	05 04 10	05 06 11	05 07 23	05 07 10	04 13 23
04 13 53	05 06 81	05 04 74	04 06 81	04 04 74	60	BIN B4	BOUT B4	74	04 04 60	04 06 61	04 07 74	04 07 60	05 04 60	05 06 61	05 07 74	05 07 60	04 13 74
04 13 04	05 06 27	05 04 20	04 06 27	04 04 20	86	BIN C4	BOUT C4	20	04 04 86	04 06 84	04 07 20	04 07 86	05 04 86	05 06 84	05 07 20	05 07 86	04 13 20
04 13 60	05 06 79	05 04 72	04 06 79	04 04 72	75	BIN D4	BOUT D4	72	04 04 75	04 06 85	04 07 72	04 07 75	05 04 75	05 06 85	05 07 72	05 07 75	04 13 72
04 13 61	05 06 38	05 04 15	04 06 38	04 04 15	51	BIN A5	BOUT A5	15	04 04 51	04 06 04	04 07 15	04 07 51	05 04 51	05 06 04	05 07 15	05 07 51	04 13 30
04 13 10	05 06 89	05 04 66	04 06 89	04 04 66	52	BIN B5	BOUT B5	66	04 04 52	04 06 06	04 07 66	04 07 52	05 04 52	05 06 06	05 07 66	05 07 52	04 13 81
04 13 11	05 06 35	05 04 12	04 06 35	04 04 12	28	BIN C5	BOUT C5	12	04 04 28	04 06 21	04 07 12	04 07 28	05 04 28	05 06 21	05 07 12	05 07 28	04 13 27
04 13 68	05 06 87	05 04 64	04 06 87	04 04 64	26	BIN D5	BOUT D5	64	04 04 26	04 06 83	04 07 64	04 07 26	05 04 26	05 06 83	05 07 64	05 07 26	04 13 79
04 13 62	05 06 91	05 04 08	04 06 91	04 04 08	1	BIN A6	BOUT A6	8	04 04 01	04 06 53	04 07 08	04 07 01	05 04 01	05 06 53	05 07 08	05 07 01	04 13 38
04 13 17	05 06 40	05 04 59	04 06 40	04 04 59	2	BIN B6	BOUT B6	59	04 04 02	04 06 54	04 07 59	04 07 02	05 04 02	05 06 54	05 07 59	05 07 02	04 13 89
04 13 67	05 06 90	05 04 55	04 06 90	04 04 55	17	BIN C6	BOUT C6	55	04 04 17	04 06 67	04 07 55	04 07 17	05 04 17	05 06 67	05 07 55	05 07 17	04 13 35
04 13 73	05 06 93	05 04 57	04 06 93	04 04 57	77	BIN D6	BOUT D6	57	04 04 77	04 06 76	04 07 57	04 07 77	05 04 77	05 06 76	05 07 57	05 07 77	04 13 87
				06 09 18	25	CLEAR AC	AC A1	44	06 08 06	04 13 65							
				06 08 26	63	SH CP	AC A1	94									
				06 10 36	68	BIN→AC	AC B1	95	06 08 56	04 13 16							
				06 18 41	62	AC→BOUT	AC B1	45									
				06 15 44	70	GR	AC C1	42	06 08 08	04 13 13							
				06 09 19	69	CLEAR AC	AC C1	43									
				06 08 63	34	SUMA	AC D1	92	06 08 58	04 13 14							
				06 08 62	33	SUMB	AC D1	41									
				06 08 12	32	SUMC	AC A2	88									
				06 08 13	82	SUMD	AC B2	39									
							AC C2	36									
							AC D2	37									
							AC A3	80									
							AC B3	31									
							AC C3	78									
							AC D3	29									
							AC A4	73									
							AC B4	24									
							AC C4	71									
							AC D4	22									
							AC A5	65									
							AC B5	16									
							AC C5	13									
							AC D5	14									
							AC A6	58									
							AC B6	9									
							AC C6	56									
							AC D6	7									
							CLEAR AC	18	06 09 25								
							CLEAR AC	19	06 06 45	06 09 69							

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	09	+5V : PIN5 GND : PIN3
TYPE		GROUP	SHEET	
JC244	AC REGISTER BUSLINES, BOUT, BIN	12	164	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	07	65	1	$\overline{\text{ROUTE A5}}$					
05	07	16	2	$\overline{\text{ROUTE B5}}$					
05	07	13	4	$\overline{\text{ROUTE C5}}$					
05	07	14	6	$\overline{\text{ROUTE D5}}$					
05	07	73	7	$\overline{\text{ROUTE A4}}$					
05	07	24	8	$\overline{\text{ROUTE B4}}$					
05	07	71	9	$\overline{\text{ROUTE C4}}$					
05	07	22	10	$\overline{\text{ROUTE D4}}$					
					SEC 20	30	04	21	13
					SEC 21	31	04	21	15
					SEC 22	32	04	21	17
					SEC 23	33	04	21	19
					SEC 24	34	04	21	21
					SEC 25	35	04	21	23
					RVRE 90°	36			
					RVRE 80°	37			
					RVRE 70°	38			
					RVRE 60°	39			
					RVRE 50°	40			
					RVRE 40°	41			
					RVRE 30°	42			
					RVRE 20°	43			
					RVRE 10°	44			
					RVRE 0°	45			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	12	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC218	SECANT DECODER, SEC	12	167	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	03	39	1	MIAM:6					
06	08	82	4	MIAD:6					
06	02	45	8	MISC:0					
06	07	72	10	MIR:5					
06	07	11	11	MIR:6					
06	07	13	12	MIRS:2					
06	08	73	13	ADD					
04	09	44	14	SIG BIN					
06	07	61	15	MIRS:0					
06	08	75	16	SIG AC-S					
06	15	32	17	MIED:5					
06	07	18	18	MIRS:4					
06	07	24	19	MIRS:6					
06	08	92	20	MIAD:2					
06	08	90	23	MIAD:0					
06	03	44	26	MIAM:1					
06	07	55	27	MIR:2					
06	15	42	28	MIED:0					
06	07	22	30	MIR:0					
06	17	30	31	DEL					
06	02	33	32	MISC:6					
06	02	35	33	MISC:5					
06	02	37	34	MISC:4					
06	15	38	35	MIED:2					
06	14	43	42	AD C					
06	14	45	44	AD A					
06	03	42	51	MIAM:3					
06	08	94	52	MIAD:4					
06	08	93	53	MIAD:5					
06	02	41	55	MISC:2					
06	02	39	57	MISC:3					
06	02	43	59	MISC:1					
06	08	83	60	MIAD:7					
06	07	66	61	MIR:4					
06	15	34	62	MIED:4					
06	03	38	63	MIAM:7					
06	07	37	64	MIRS:3					
06	08	17	65	CA-S					
06	07	10	66	MIRS:1					
06	15	30	67	MIED:6					
06	07	41	68	MIR:7					
06	07	16	69	MIRS:5					
06	07	74	70	MIRS:7					
06	08	45	72	MIAD:3					
06	15	28	73	MIED:7					
06	08	91	74	MIAD:1					

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	13	+5V : PIN 5 GND : PIN 3
TYPE	SIGNAL DISPLAY ADAPTER SHEET 1	GROUP	SHEET	
		12	168	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
06	03	45	75	MIAM:0				
06	03	40	76	MIAM:5				
06	03	41	77	MIAM:4				
06	07	27	79	MIR:3				
			80					
06	07	56	81	MIR:1				
06	02	31	82	MISC:7				
06	18	44	83	MIAM:2				
06	15	36	87	MIED:3				
06	15	40	89	MIED:1				
06	14	42	92	AD D				
06	14	44	95	AD B				

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	13	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
	SIGNAL DISPLAY ADAPTER	12	168	
	SHEET 2			PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	02	18	1	ECYC	ERRORε	22	05	09	23
06	10	34	2	dDEL	ERRORα	23	04	09	23
04	02	21	4	VCα Ovl	$\overline{\text{ERROR 1}}$	24	04	21	75
05	02	21	6	VCε Ovl	$\overline{\text{ERROR 1}}$	25	04	21	75
05	22	13	7	$\overline{\text{TESTMES}}$	MSEε-S	26	05	21	25
06	04	20	8	$\overline{\text{NPO}}$	MSEα-S	27	05	21	23
06	04	35	9	CP	MIED:7	28	04	09	26
							05	09	26
							06	13	73
06	16	30	10	$\overline{\text{RESER}}$	$\overline{\text{MIED:7}}$	29	06	18	18
04	24	17	11	COARSEα	MIED:6	30	06	13	67
04	24	18	12	FINEα	$\overline{\text{MIED:6}}$	31			
04	02	20	13	LIM HIGHα	MIED:5	32	06	13	17
04	24	15	14	COARSEα	$\overline{\text{MIED:5}}$	33			
04	24	16	15	FINEε	MIED:4	34	06	13	62
							06	11	43
05	02	20	16	LIM HIGHε	$\overline{\text{MIED:4}}$	35			
06	04	16	17	CP3	MIED:3	36	06	13	87
			18		$\overline{\text{MIED:3}}$	37	04	01	19
							05	01	19
04	10	35	19	$\overline{\text{CPEα-S}}$	MIED:2	38	06	13	35
							06	11	40
			20		$\overline{\text{MIED:2}}$	39			
05	10	35	21	$\overline{\text{CPEε-S}}$	MIED:1	40	06	13	89
					EDV	41	06	10	07
					MIED:0	42	06	13	38
					$\overline{\text{GR}}$	43	04	10	20
							05	10	20
							06	02	01
							06	03	13
							06	16	19
							06	17	24
					GR	44	04	01	02
							04	04	70
							04	06	70
							04	07	70
							05	01	02
							05	04	70
							05	06	70
							05	07	70
							06	08	86
							06	09	70
							06	07	33
					$\overline{\text{CVSP}}$	45	06	02	20

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	15	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC218	MEASUREMENT ERROR DETECTING, MED	12	170	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	08	63	1	SUMA					
06	08	62	2	SUMB					
06	08	13	4	SUMC	$\overline{\text{ERCOM}}$	28			
06	08	12	6	SUMD	$\overline{\text{ERCOM}}$	29	04	09	24
							05	09	24
06	14	45	7	ADNDA	$\overline{\text{RESER}}$	30	04	10	18
							05	10	18
							06	15	10
06	14	44	8	ADNDB	$\overline{\text{NCL}}$	31	05	21	55
06	14	43	9	ADNDC	$\overline{\text{NCL}}$	32			
06	14	42	10	ADND	ERROR 1	33	04	21	75
04	10	28	11	$\overline{\text{ERRES}}$	ILL SEQ	34	05	21	53
06	08	26	12	$\overline{\text{SH-CP}}$	CP	35			
05	01	44	13	$\overline{\text{MIDC}\epsilon\text{:0}}$	TP	36			
06	07	90	14	$\overline{\text{MIR:0}}$	TP	37			
04	01	44	15	$\overline{\text{MIDC}\alpha\text{:0}}$	TP	38			
06	07	12	16	dcp2	REDIG	39	05	21	51
06	04	33	17	$\overline{\text{CP}}$	REDIG AD	40			
06	04	37	18	CP2	$\overline{\text{SUMD}}$	41	04	08	82
							05	08	82
06	15	43	19	$\overline{\text{GR}}$	$\overline{\text{SUMC}}$	42	04	08	32
							05	08	32
06	08	39	20	$\overline{\text{MIAD:0}}$	$\overline{\text{SUMB}}$	43	04	08	33
							05	08	33
			21		$\overline{\text{SUMA}}$	44	04	08	34
							05	08	34
			22		REDIG SUM	45			
			23						
			24						
			25						
			26						
			27						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	16	+5V : PIN 5 GND : PIN 3
TYPE		GROUP	SHEET	
JC218	PROCESSOR ERROR DETECTING, PED	12	171	
		PART 1		

LOCATION			PIN	SIGNAL		PIN	LOCATION		
06	07	90	2			25	06	18	26
06	03	26	4			26	06	18	27
06	08	39	6			27	06	18	28
06	02	23	7			28	06	18	08
06	18	28	8			29	TEST	WIRES	
06	18	08	9			30	TEST	WIRES	
			10			31			
			11			32			
05	22	17	12	$\overline{\text{PRES}\alpha}$	PRESα	33	06	17	06
							04	10	07
							05	24	40
05	22	18	13	$\overline{\text{PRES}\epsilon}$	PRESϵ	34	06	17	09
							05	10	07
							05	24	39
05	22	15	14	$\overline{\text{SCAN}}$	SCAN	35	06	02	02
							05	24	38
			15	$\overline{\text{PROG TR}}$	PROG TR	36			
06	07	67	16	$\overline{\text{MIR:4}}$	MIR:4	37	04	09	12
06	07	90	17	$\overline{\text{MIR:0}}$	MIR:0	38	05	09	12
06	15	29	18	$\overline{\text{MIED:7}}$	MIED:7	39	06	02	45
04	09	44	19	SIG BIN	$\overline{\text{SIG BIN}}$	40	06	08	70
			20	AC→BOUT	$\overline{\text{AC→BOUT}}$	41	06	09	62
							04	09	13
							05	09	13
06	10	36	21	$\overline{\text{BIN→AC}}$	BIN→AC	42	06	08	71
06	03	29	22	ATOP	$\overline{\text{ATOP}}$	43	06	08	79
06	03	43	23	$\overline{\text{MIAM:2}}$	MIAM:2	44	06	13	83
06	17	30	24	DEL	$\overline{\text{DEL}}$	45	06	10	32
							06	02	10

INTERCONNECTION TABLE

RACK	LOC'N
06	18

NOTES

+5V : PIN 5
GND : PIN 3

TYPE	
JC218	INVERTER, INV

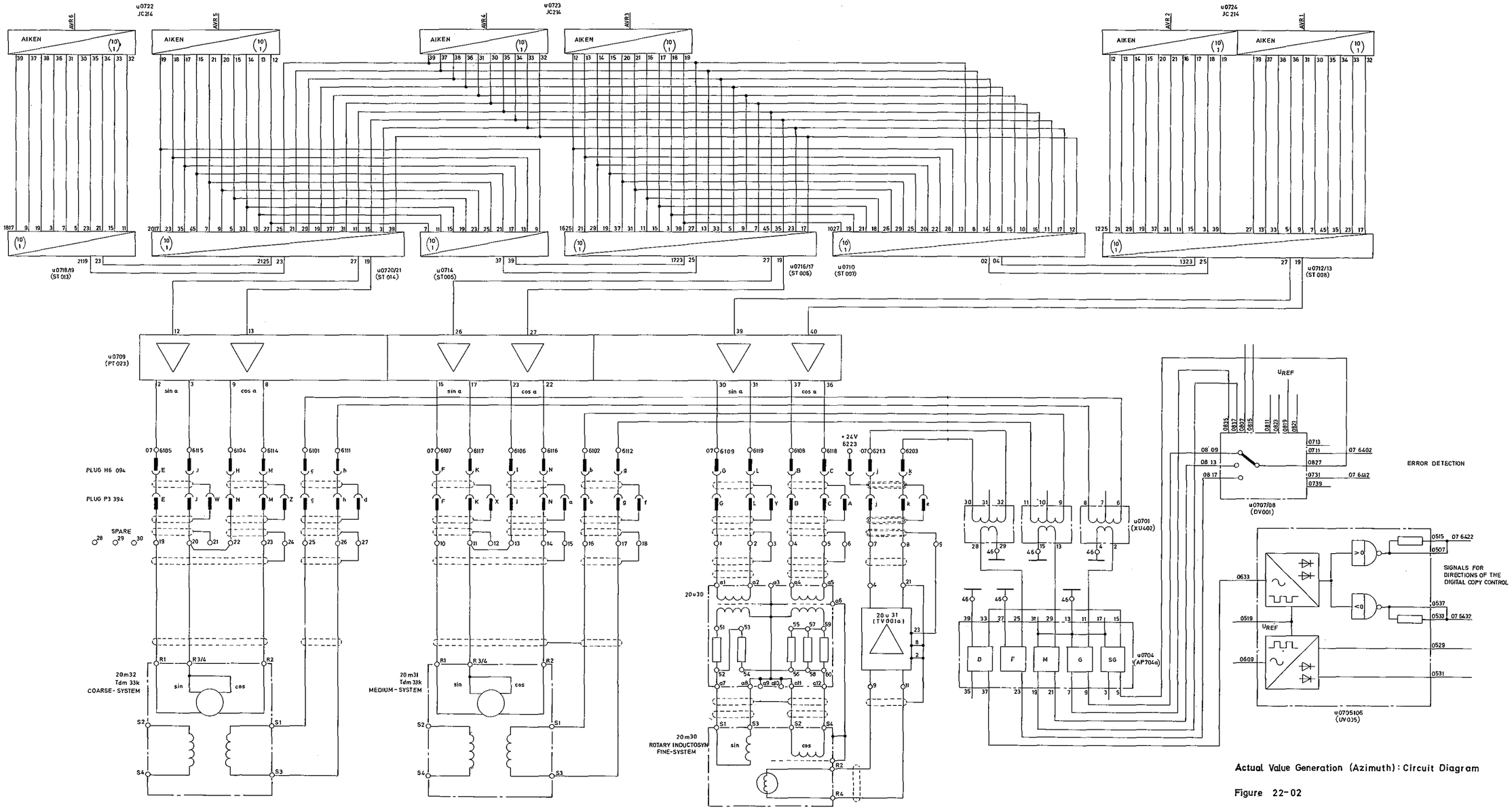
GROUP	SHEET
12	173

PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	04	65	1	$\overline{\text{AVR}\varepsilon\ \text{A5}}$	AVRε 0°	56	04	11	12
							04	11	15
05	04	16	4	$\overline{\text{AVR}\varepsilon\ \text{B5}}$	AVRε 10°	58	04	11	18
							04	11	21
05	05	39	32	AVRε C5	AVRε 20°	61	04	11	24
							04	11	27
05	05	40	34	AVRε D5	AVRε 30°	63	04	11	30
							04	11	33
					AVRε 40°	24	04	11	36
							04	11	39
					AVRε 50°	27	04	11	42
							04	11	45
					AVRε 60°	65	05	11	12
							05	11	15
					AVRε 70°	66	05	11	18
							05	11	21
					AVRε 80°	68	05	11	24
					AVRε 90°	70			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		06	19	+5V : PIN 5 GND : PIN 3
TYPE	DECIMAL DECODER FOR WEIGHT CORRECTION, DEC	GROUP	SHEET	
JC242		12	174	
		PART 1		

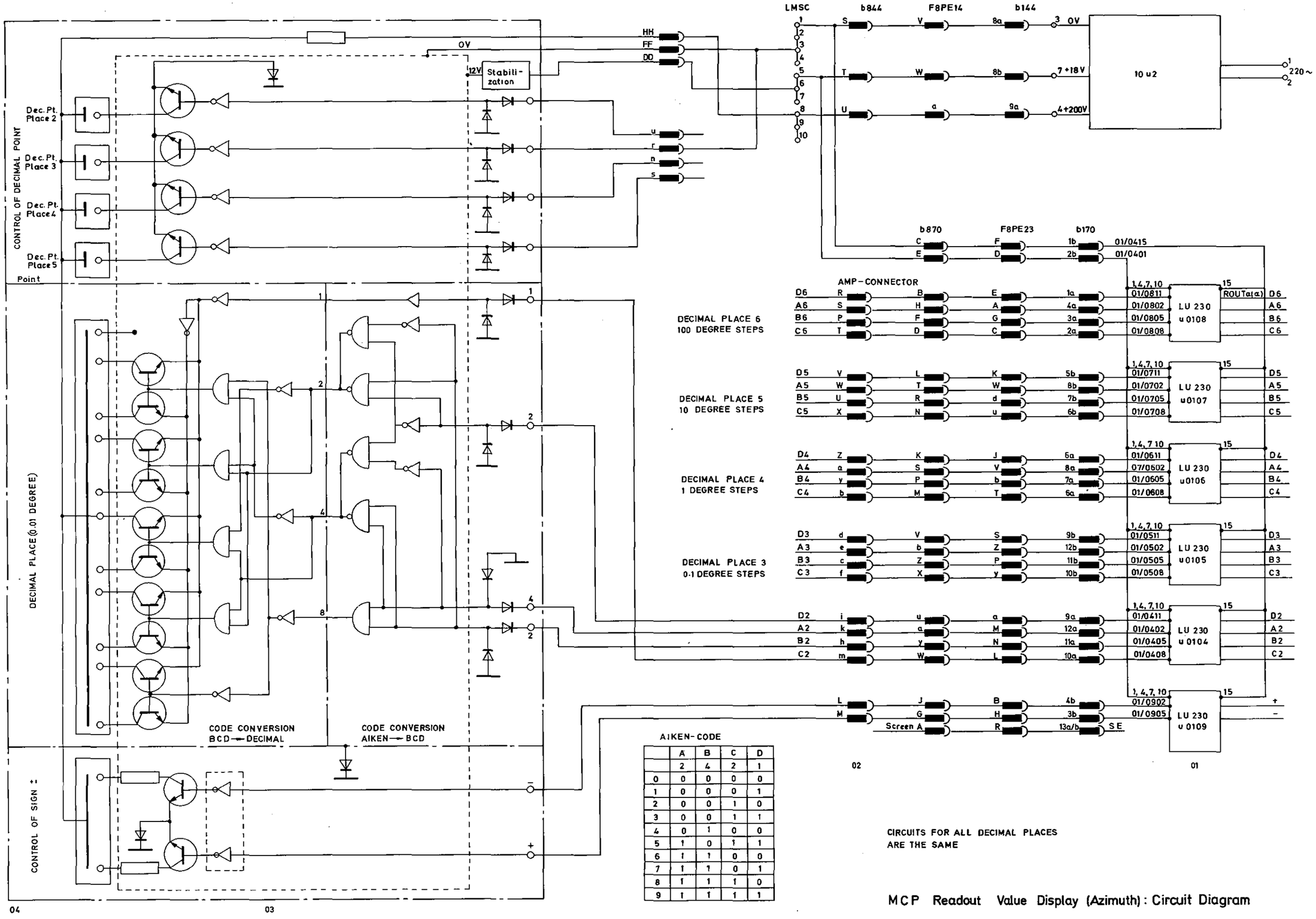


Actual Value Generation (Azimuth): Circuit Diagram

Figure 22-02

Part 1

Iss. 1



AIKEN-CODE

	A	B	C	D
	2	4	2	1
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

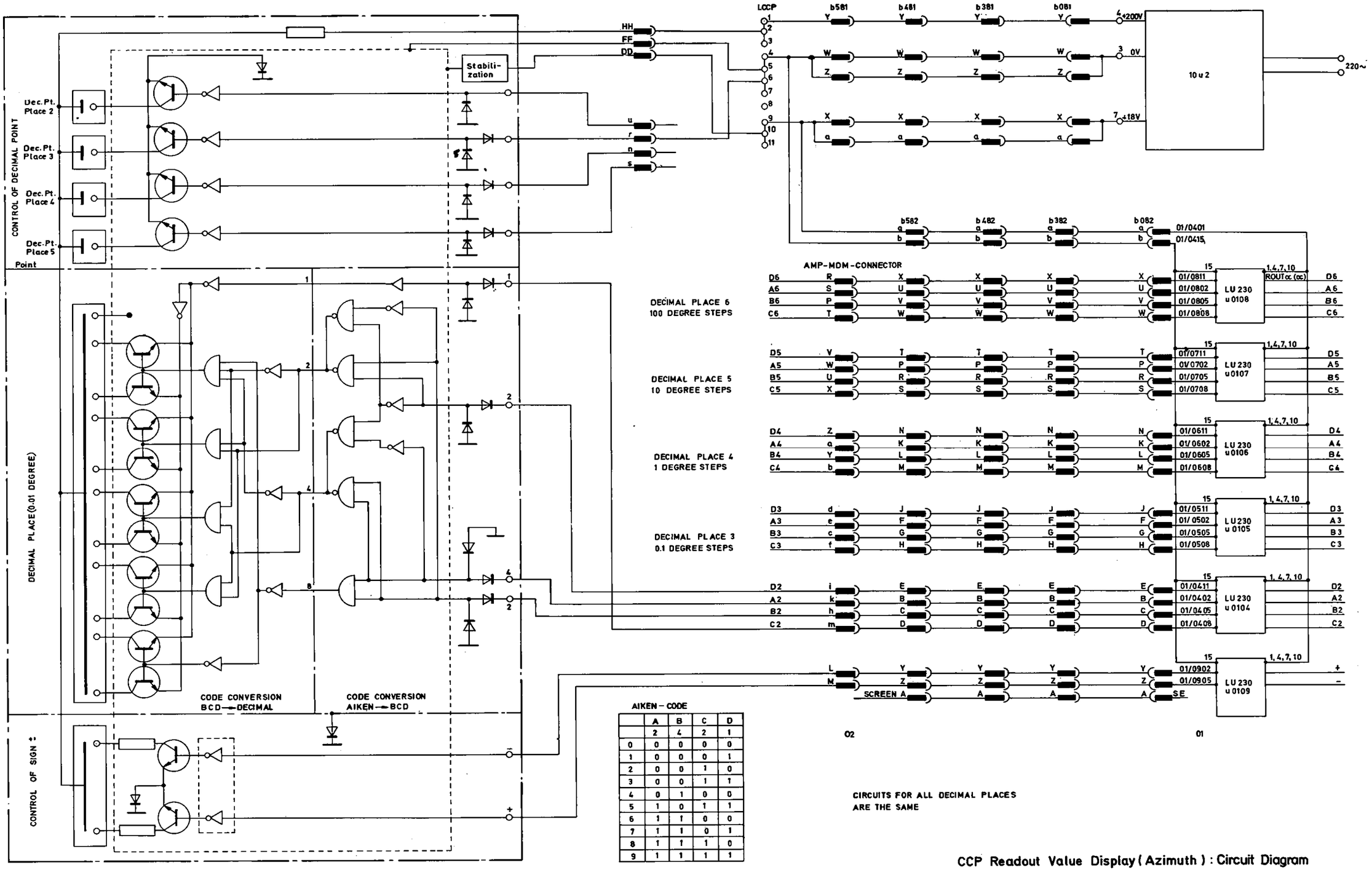
CIRCUITS FOR ALL DECIMAL PLACES ARE THE SAME

MCP Readout Value Display (Azimuth): Circuit Diagram

Figure 22–03

Part 1

Iss.1



AIKEN – CODE

	A	B	C	D
	2	4	2	1
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

CIRCUITS FOR ALL DECIMAL PLACES ARE THE SAME

CCP Readout Value Display (Azimuth) : Circuit Diagram

Figure 22 -04

Part 1

Iss. 1

LOCATION			P	SIGNAL		SIGNAL	P	LOCATION		
07	61	01	6	ERVOLTCα		ERVOLT1Cα	2	07	04	11
			7							
07	61	11	8	ERVOLTCα		GND	4	>	01	46
07	61	02	9	ERVOLTMα		ERVOLT1Mα	13	07	04	29
			10							
07	61	12	11	ERVOLTMα			15	>	01	46
08	61	01	19	ERVOLTCε		ERVOLT1Cε	21	08	04	11
			18							
08	61	11	17	ERVOLTCε		GNDε	22	07	01	46
08	61	02	25	ERVOLTMε		ERVOLT1Mε	23	08	04	29
			26							
08	61	12	27	ERVOLTMε		GND	24	>	01	46
07	62	03	30	ERVOLTFα		ERVOLT1Fα	28	07	04	25
			31							
07	62	13	32	ERVOLTFα		GND	29	>	01	46
08	62	03	33	ERVOLTFε		ERVOLT1Fε	37	08	04	25
			34							
08	62	13	35	ERVOLTFε		GND	38	>	01	46
07	01	46	41	GND						

INTERCONNECTION TABLE

RACK	LOC'N
07	01 02

NOTES

GND : PIN 32,35
> See Figure 0205

TYPE	
XU 402	ERROR VOLTAGE INPUT, C, M, α, ε

GROUP	SHEET
22	08

PART 1

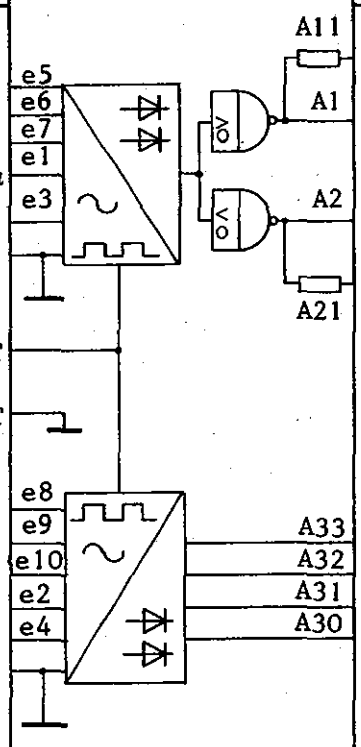
Iss. 1

LOCATION			P	SIGNAL		SIGNAL	P	LOCATION		
07	08	27	05	ERVOLT1α	D		15	07	04	23
			17				3			
07	01	02	11	ERVOLT1Cα	C	ERVOLT3Cα	9	07	08	09
>	03	46	13	GND		ERVOLT2Cα	7	07	08	35
07	01	13	29	ERVOLT1Mα	M	ERVOLT3Mα	21	07	08	13
						ERVOLT2Mα	19	07	08	37
07	03	07	25	ERVOLT1Fα	F	ERVOLT3Fα	23	07	08	17
>	03	46	27	GND						
		15	33		D	ERVOLT2α	37	07	06	33
07	03	46	39	GND			35			
07	04	00	45	SGND						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	04	> See Figure 0205
TYPE		GROUP	SHEET	
AP 704	ERROR VOLTAGE ADJUSTMENT α	22	09	
				PART 1

Iss. 1

LOCATION			P	SIGNAL
07	06	29	27	
			29	
>	04	37	33	ERVOLT2α
07	06	37	35	
			37	
08	01	33	19*	REF VOLT
08	01	37	21*	REF VOLT
07	06	05	03	
			05	
			09	
07	06	13	11	
			13	
07	05	48	45	+24V
08	01	07	05	-6V
08	01	29	19	83V
07	05	46	41	GND
07	05	00	39	SGND



SIGNAL	P	LOCATION		
	05			
	07	07	64	22
	37	07	64	32
	33			
	25	07	05	27
	27			
	29			
	31			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	05* 06	
TYPE		GROUP	SHEET	
UV 005	DISCRIMINATOR α	22	10	
		PART 1		

Iss. 1

LOCATION			P	SIGNAL		SIGNAL	P	LOCATION		
07	04	07	*35	ERVOLT2Cα		COARSEα	13 11	07	64	02
08	01	33	23	REF VOLT						
			25							
07	04	37	*37	ERVOLT2Mα						
			* 7							
			*15							
			*11							
			*23							
08	01	37	*19	REF VOLT		FINEα	31 37	07	64	12
			*21							
						ERVOLT1α	27*	07	04	05
07	04	09	* 9	ERVOLT3Cα						
07	04	21	*13	ERVOLT3Mα						
07	04	23	*17	ERVOLT3Fα						
>	07	48	45	+24V						
08	01	07	* 5	-6V						
08	01	29	*31	83V						
>	07	46	*41	GND						
>	07	00	39	SGND						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	07 08*	> See Figure 0205
TYPE	CMF SWITCH α	GROUP	SHEET	
DV 001		22	11	
		PART 1		

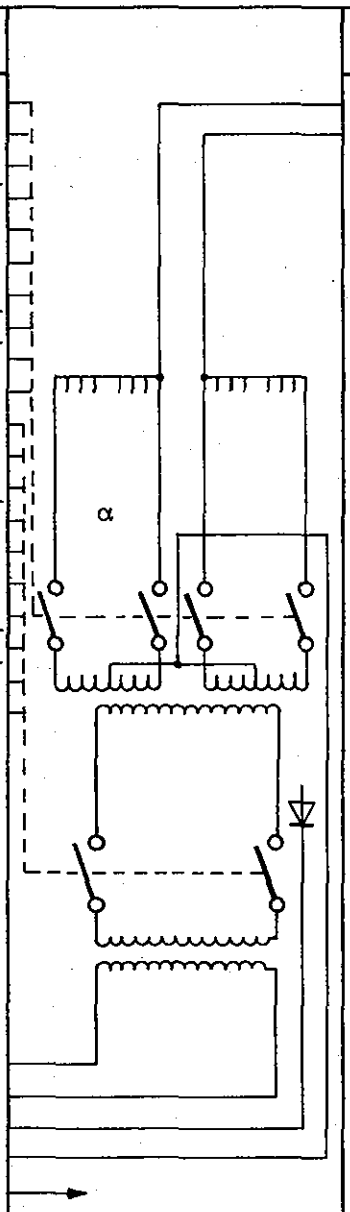
Iss. 1

LOCATION			P	SIGNAL		SIGNAL	P	LOCATION		
07	21	19	13	SINVOLTCα		SINCα	2	07	61	05
					12V 8V 4V	SINCα	3	07	61	15
							5			
							7			
						COSCα	8	07	61	14
07	21	27	12	COSVOLTCα		COSCα	9	07	61	04
07	17	19	27	SINVOLTMα		SINMα	16	07	61	07
						SINMα	17	07	61	17
							19			
							21			
						COSMα	22	07	61	16
07	17	27	26	COSVOLTMα		COSMα	23	07	61	06
07	13	19	40	SINVOLTFα		SINFα	30	07	61	09
						SINFα	31	07	61	19
							33			
							35			
						COSFα	36	07	61	18
07	13	27	39	COSVOLTFα		COSFα	37	07	61	08
07	09	46	41	GND						
07	09	48	45	+24V						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	09	
TYPE		GROUP	SHEET	
PT 023	SIN/COS VOLTAGES OUTPUT α	22	12	
		PART 1		

Iss. 1

LOCATION			P	SIGNAL
07	23	12	27	ZERO α 3
07	23	13	19	ONE α 3
07	23	14	21	TWO α 3
07	23	15	18	THREE α 3
07	23	20	26	FOUR α 3
07	23	21	29	FIVE α 3
07	23	16	25	SIX α 3
07	23	17	20	SEVEN α 3
07	23	18	22	EIGHT α 3
07	23	19	28	NINE α 3
07	23	39	13	ZERO α 4
07	23	37	8	ONE α 4
07	23	38	14	TWO α 4
07	23	36	9	THREE α 4
07	23	31	15	FOUR α 4
07	23	30	10	FIVE α 4
07	23	35	16	SIX α 4
07	23	34	11	SEVEN α 4
07	23	33	17	EIGHT α 4
07	23	32	12	NINE α 4
08	01	21	7	10kHz
08	01	19	6	10kHz
>	11	48	31	+24V
>	11	46	41	GND
>	11	48	45	24V

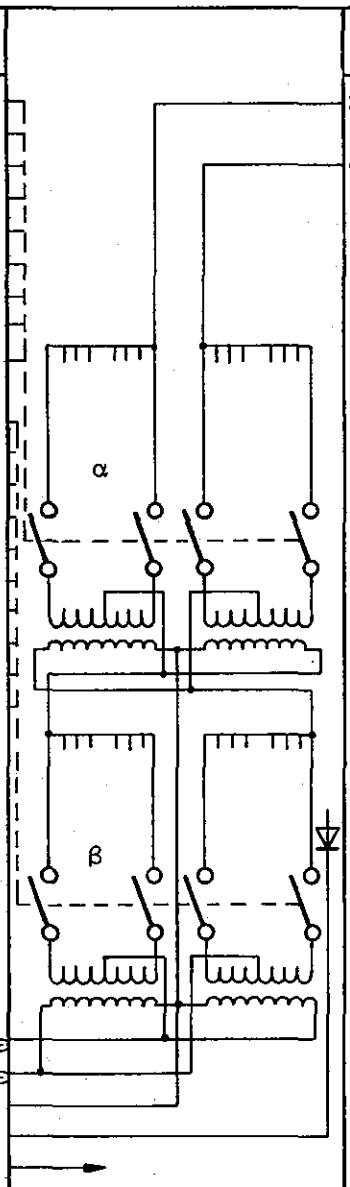


SIGNAL	P	LOCATION		
SINVOLTFα	2	07	13	25
COSVOLTFα	4	07	13	23

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	10 11	> See Figure 0205
TYPE		GROUP	SHEET	
ST 007	SIN/COS GENERATOR α FINE UNIT	22	13	
		PART 1		

Iss. 1

LOCATION			P	SIGNAL
07	24	12	25	ZEROα 1
07	24	13	21	ONEα 1
07	24	14	29	TWOα 1
07	24	15	19	THREEα 1
07	24	20	37	FOURα 1
07	24	21	31	FIVEα 1
07	24	16	11	SIXα 1
07	24	17	15	SEVENα 1
07	24	18	3	EIGHTα 1
07	24	19	39	NINEα 1
07	24	39	27	ZEROα 2
07	24	37	13	ONEα 2
07	24	38	33	TWOα 2
07	24	36	5	THREEα 2
07	24	31	9	FOURα 2
07	24	30	7	FIVEα 2
07	24	35	45	SIXα 2
07	24	34	35	SEVENα 2
07	24	33	23	EIGHTα 2
07	24	32	17	NINEα 2
07	10	02	25*	SINVOLTFε
07	10	04	23*	COSVOLTFε
>	13	46	41*	GND
>	13	48	21*	+24V
>	13	48	45*	+24V



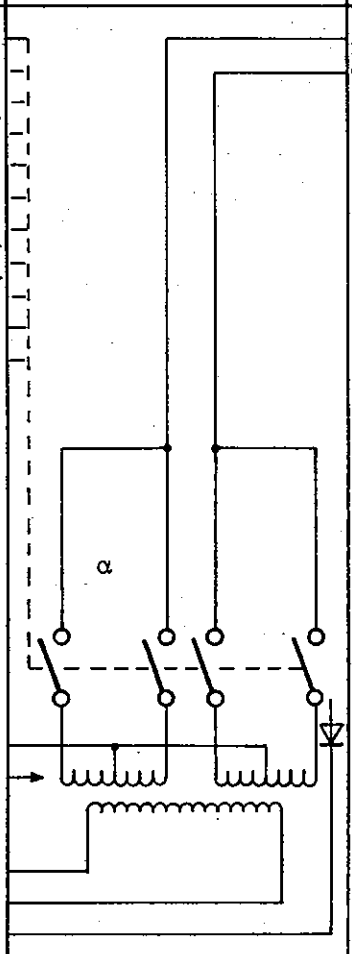
SIGNAL	P	LOCATION		
SINVOLTFα	19*	07	09	40
		07	64	07
COSVOLTFα	27*	07	09	39
		07	64	17

INTERCONNECTION TABLE	RACK	LOC'N	NOTES
	07	12 13*	> See Figure 0205

TYPE		GROUP	SHEET
ST 008	SIN/COS GENERATOR α FINE UNIT	22	14
		PART 1	

Iss. 1

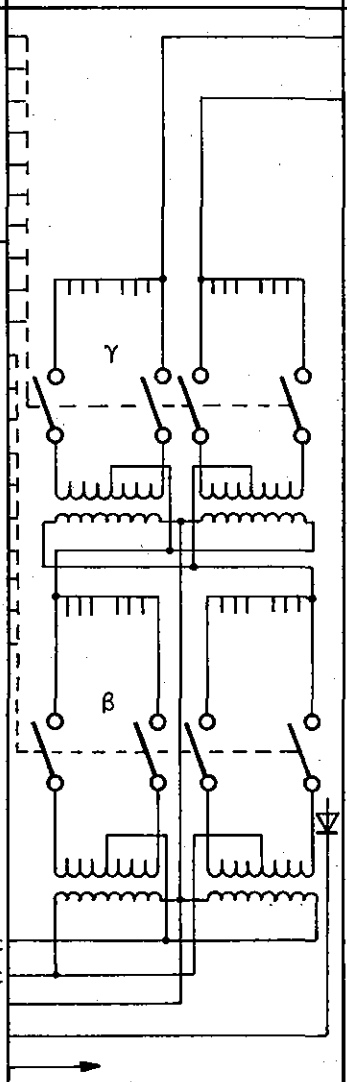
LOCATION			P	SIGNAL		SIGNAL	P	LOCATION		
07	22	12	7	ZEROα 5		SINVOLTMα	37	07	17	25
07	22	13	11	ONEα 5		COSVOLTMα	39	07	17	23
07	22	14	15	TWOα 5						
07	22	15	19	THREEα 5						
07	22	20	23	FOURα 5						
07	22	21	25	FIVEα 5						
07	22	16	21	SIXα 5						
07	22	17	17	SEVENα 5						
07	22	18	13	EIGHTα 5						
07	22	19	9	NINEα 5						
>	15	46	41	GND						
>	15	48	45	+24V						
08	01	21	33	10kHz						
08	01	19	35	10kHz						
>	15	48	31	+24V						



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	14 15	> See Figure 0205
TYPE		GROUP	SHEET	
ST 005	SIN/COS GENERATOR α MEDIUM UNIT	22	15	
		PART 1		

Iss. 1

LOCATION			P	SIGNAL
07	23	12	25	ZEROα 3
07	23	13	21	ONEα 3
07	23	14	29	TWOα 3
07	23	15	19	THREEα 3
07	23	20	37	FOURα 3
07	23	21	31	FIVEα 3
07	23	16	11	SIXα 3
07	23	17	15	SEVENα 3
07	23	18	3	EIGHTα 3
07	23	19	39	NINEα 3
07	23	39	27	ZEROα 4
07	23	37	13	ONEα 4
07	23	38	33	TWOα 4
07	23	36	5	THREEα 4
07	23	31	9	FOURα 4
07	23	30	7	FIVEα 4
07	23	35	45	SIXα 4
07	23	34	35	SEVENα 4
07	23	33	23	EIGHTα 4
07	23	32	17	NINEα 4
07	14	37	25*	SINVOLTMε
07	14	39	23*	COSVOLTMε
>	17	46	41*	GND
>	17	48	21*	+24V
>	17	48	45*	+24V

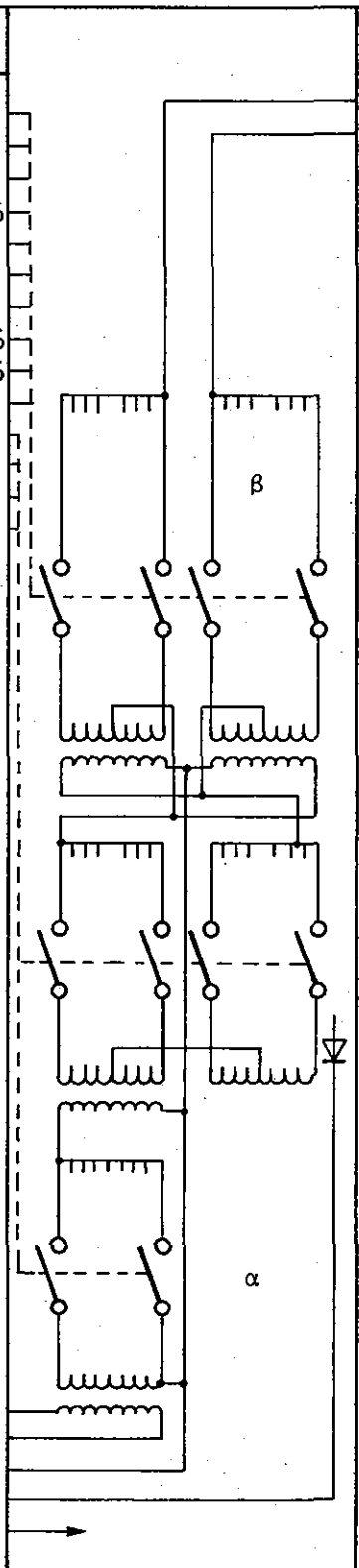


SIGNAL	P	LOCATION		
SINVOLTMα	19*	07	09	27
		07	64	08
COSVOLTMα	27*	07	09	26
		07	64	18

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	16 17*	>See Figure 0205
TYPE	SIN/COS GENERATOR α MEDIUM UNIT	GROUP	SHEET	
STO06		22	16	
				PART 1

Iss. 1

LOCATION			P	SIGNAL
07	22	39	17	ZERO α 6
07	22	37	9	ONE α 6
07	22	38	19	TWO α 6
07	22	36	3	THREE α 6
07	22	31	7	FOUR α 6
07	22	30	5	FIVE α 6
07	22	35	23	SIX α 6
07	22	34	21	SEVEN α 6
07	22	33	15	EIGHT α 6
07	22	32	11	NINE α 6
		46	35	GND
		48	31	+24V
		48	29	+24V
		48	33	+24V
08	01	21	13	10kHz
08	01	19	25	10kHz
>	19	46	41*	GND
>	19	48	27	+24V
>	19	48	45*	+24V



SIGNAL	P	LOCATION	
SINVOLTCα	21*	21	25
COSVOLTCα	23*	21	23

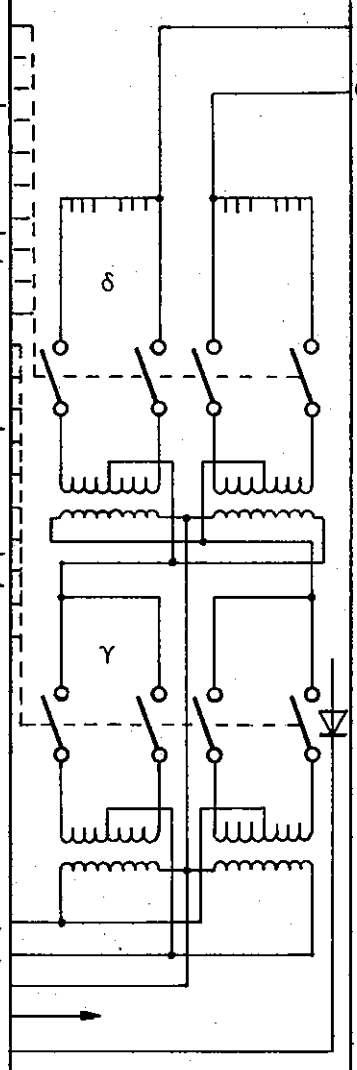
INTERCONNECTION TABLE	RACK	LOC'N	NOTES
	07	18 19*	> See Figure 0205

TYPE		GROUP	SHEET
ST 013	SIN/COS GENERATOR α COARSE UNIT	22	17

PART 1

Iss. 1

LOCATION			P	SIGNAL
07	23	39	25	ZEROα4
07	23	37	21	ONEα 4
07	23	38	29	TWOα 4
07	23	36	19	THREEα 4
07	23	31	37	FOURα 4
07	23	30	31	FIVEα 4
07	23	35	11	SIXα 4
07	23	34	15	SEVENα 4
07	23	33	3	EIGHTα 4
07	23	32	39	NINEα 4
07	22	12	27	ZEROα 5
07	22	13	13	ONEα 5
07	22	14	33	TWOα 5
07	22	15	5	THREEα 5
07	22	20	9	FOURα 5
07	22	21	7	FIVEα 5
07	22	16	45	SIXα 5
07	22	17	35	SEVENα 5
07	22	18	23	EIGHTα 5
07	22	19	17	NINEα 5
07	19	21	25*	SINVOLTCα
07	19	23	23*	COSVOLTCα
07	21	46	41*	GND
07	21	48	45*	+24V
07	21	48	21*	+24V



SIGNAL	P	LOCATION		
SINVOLTCα	19*	07	09	13
		07	64	09
COSVOLTCα	27*	07	09	12
		07	64	19

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	20 21*	
TYPE		GROUP	SHEET	
ST 014	SIN/COS GENERATOR α COARSE UNIT	22	18	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			1			7			
07	65	10	2	$\overline{\text{AVR}\alpha\ \text{B5}}$	$\overline{\text{ZERO}\alpha\ 5}$	12	07	14	07
							07	20	27
07	65	30	4	$\overline{\text{AVR}\alpha\ \text{D5}}$	$\overline{\text{ONE}\alpha\ 5}$	13	07	14	11
							07	20	13
07	65	11	6	AVRα B5	$\overline{\text{TWO}\alpha\ 5}$	14	07	14	15
							07	20	33
07	65	31	8	AVRα D5	$\overline{\text{THREE}\alpha\ 5}$	15	07	14	19
							07	20	05
07	65	20	10	$\overline{\text{AVR}\alpha\ \text{C5}}$	$\overline{\text{SIX}\alpha\ 5}$	16	07	14	21
							07	20	45
07	65	21	11	AVRα C5	$\overline{\text{SEVEN}\alpha\ 5}$	17	07	14	17
							07	20	35
			22		$\overline{\text{EIGHT}\alpha\ 5}$	18	07	14	13
							07	20	23
			23		$\overline{\text{NINE}\alpha\ 5}$	19	07	14	09
							07	20	17
07	65	00	25	$\overline{\text{AVR}\alpha\ \text{A5}}$	$\overline{\text{FOUR}\alpha\ 5}$	20	07	14	23
							07	20	09
07	65	01	26	AVRα A5	$\overline{\text{FIVE}\alpha\ 5}$	21	07	14	25
							07	20	07
			27	GND		24			
07	64	01	28	AVRα A6	$\overline{\text{FIVE}\alpha\ 6}$	30	07	18	05
07	64	00	29	$\overline{\text{AVR}\alpha\ \text{A6}}$	$\overline{\text{FOUR}\alpha\ 6}$	31	07	18	07
07	64	10	40	$\overline{\text{AVR}\alpha\ \text{B6}}$	$\overline{\text{NINE}\alpha\ 6}$	32	07	18	11
07	64	20	41	$\overline{\text{AVR}\alpha\ \text{C6}}$	$\overline{\text{EIGHT}\alpha\ 6}$	33	07	18	15
07	64	30	42	$\overline{\text{AVR}\alpha\ \text{D6}}$	$\overline{\text{SEVEN}\alpha\ 6}$	34	07	18	21
07	64	11	43	AVRα B6	$\overline{\text{SIX}\alpha\ 6}$	35	07	18	23
07	64	21	44	AVRα C6	$\overline{\text{THREE}\alpha\ 6}$	36	07	18	03
07	64	31	45	AVRα D6	$\overline{\text{ONE}\alpha\ 6}$	37	07	18	09
					$\overline{\text{TWO}\alpha\ 6}$	38	07	18	19
					$\overline{\text{ZERO}\alpha\ 6}$	39	07	18	17

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	22	+5V : PIN 5 GND : PIN 3, 27 +24V: PIN 9
TYPE JC214	AIKEN → DECIMAL DECODER α	GROUP 22	SHEET 19	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			1			7			
07	65	14	2	AVRα B3	ZEROα 3	12	07	10	27
							07	16	25
07	65	30	4	AVRα D3	ONEα 3	13	07	10	19
							07	16	21
07	65	15	6	AVRα B3	TWOα 3	14	07	10	21
							07	16	29
07	65	31	8	AVRα D3	THREEα 3	15	07	10	18
							07	16	19
07	65	24	10	AVRα C3	SIXα 3	16	07	10	25
							07	16	11
07	65	25	11	AVRα C3	SEVENα 3	17	07	10	20
							07	16	15
			22		EIGHTα 3	18	07	10	22
							07	16	03
			23		NINEα 3	19	07	10	28
							07	16	39
07	65	04	25	AVRα A3	FOURα 3	20	07	10	26
							07	16	37
07	65	05	26	AVRα A3	FIVEα 3	21	07	10	29
							07	16	31
			27			24			
07	65	03	28	AVRα A4	FIVEα 4	30	07	10	10
							07	16	07
							07	20	31
07	65	02	29	AVRα A4	FOURα 4	31	07	10	15
							07	16	09
							07	20	37
07	65	12	40	AVRα B4	NINEα 4	32	07	10	12
							07	16	17
							07	20	39
07	65	22	41	AVRα C4	EIGHTα 4	33	07	10	17
							07	16	23
							07	20	03
07	65	32	42	AVRα D4	SEVENα 4	34	07	10	11
							07	16	35
							07	20	15
07	65	13	43	AVRα B4	SIXα 4	35	07	10	16
							07	16	45
							07	20	11
07	65	23	44	AVRα C4	THREEα 4	36	07	10	09
							07	16	05
							07	20	19
07	65	33	45	AVRα D4	ONEα 4	37	07	10	08
							07	16	13
							07	20	21

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	23	+5V : PIN 5 GND : PIN 3,27 +24V: PIN 9
TYPE		GROUP	SHEET	
JC214	AIKEN → DECIMAL DECODER α	22	20	
	SHEET 1			PART 1

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				$\overline{\text{TWO}}\alpha$ 4	38	07	10	14
						07	16	33
						07	20	29
				$\overline{\text{ZERO}}\alpha$ 4	39	07	10	13
						07	16	27
						07	20	25

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	23	+5V : PIN 5 GND : PIN 3, 27 +24V: PIN 9
TYPE		GROUP	SHEET	
JC214	AIKEN → DECIMAL DECODER α SHEET 2	22	20	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			1			7			
07	65	18	2	AVRα B1	ZEROα 1	12	07	12	25
07	65	38	4	AVRα D1	ONEα 1	13	07	12	21
07	65	19	6	AVRα B1	TWOα 1	14	07	12	29
07	65	39	8	AVRα D1	THREEα 1	15	07	12	19
07	65	28	10	AVRα C1	SIXα 1	16	07	12	11
07	65	29	11	AVRα C1	SEVENα 1	17	07	12	15
			22		EIGHTα 1	18	07	12	03
			23		NINEα 1	19	07	12	39
07	65	08	25	AVRα A1	FOURα 1	20	07	12	37
07	65	09	26	AVRα A1	FIVEα 1	21	07	12	31
			27			24			
07	65	07	28	AVRα A2	FIVEα 2	30	07	12	07
07	65	06	29	AVRα A2	FOURα 2	31	07	12	09
07	65	16	40	AVRα B2	NINEα 2	32	07	12	17
07	65	26	41	AVRα C2	EIGHTα 2	33	07	12	23
07	65	36	42	AVRα D2	SEVENα 2	34	07	12	35
07	65	17	43	AVRα B2	SIXα 2	35	07	12	45
07	65	27	44	AVRα C2	THREEα 2	36	07	12	05
07	65	37	45	AVRα D2	ONEα 2	37	07	12	13
					TWOα 2	38	07	12	33
					ZEROα 2	39	07	12	27

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	24	+5V : PIN 5 GND : PIN 3,27 +24V: PIN 9
TYPE		GROUP	SHEET	
JC214	AIKEN → DECIMAL DECODER α	22	21	
		PART 1		

Iss. 1

		9	8	7	6	5	4	3	2	1	0
61	0	SINVOLTFα	COSVOLTFα	SINVOLTMα	COSVOLTMα	SINVOLTCα	COSVOLTCα		ERVOLTMα	ERVOLTCα	
	1	SINVOLTFα	COSVOLTFα	SINVOLTMα	COSVOLTMα	SINVOLTCα	COSVOLTCα		ERVOLTMα	ERVOLTCα	
	2	SCREEN	SCREEN	SCREEN	SCREEN	SCREEN	SCREEN		SCREEN	SCREEN	
	3										
62	0							ERVOLTFα			
	1							(GND) ERVOLTFα			
	2							(+24V) SCREEN			
	3										
63	0										
	1										
	2										
	3										
64	0	SINVOLTCα	SINVOLTMα	SINVOLTFα					COARSEα	AVRα A6	$\overline{\text{AVRα A6}}$
	1	COSVOLTCα	COSVOLTMα	COSVOLTFα					MEDIUMα	AVRα B6	$\overline{\text{AVRα B6}}$
	2								DIR1α	AVRα C6	$\overline{\text{AVRα C6}}$
	3								DIR2α	AVRα D6	$\overline{\text{AVRα D6}}$
65	0	AVRα A1	$\overline{\text{AVRα A1}}$	AVRα A2	$\overline{\text{AVRα A2}}$	AVRα A3	$\overline{\text{AVRα A3}}$	AVRα A4	$\overline{\text{AVRα A4}}$	AVRα A5	$\overline{\text{AVRα A5}}$
	1	AVRα B1	$\overline{\text{AVRα B1}}$	AVRα B2	$\overline{\text{AVRα B2}}$	AVRα B3	$\overline{\text{AVRα B3}}$	AVRα B4	$\overline{\text{AVRα B4}}$	AVRα B5	$\overline{\text{AVRα B5}}$
	2	AVRα C1	$\overline{\text{AVRα C1}}$	AVRα C2	$\overline{\text{AVRα C2}}$	AVRα C3	$\overline{\text{AVRα C3}}$	AVRα C4	$\overline{\text{AVRα C4}}$	AVRα C5	$\overline{\text{AVRα C5}}$
	3	AVRα D1	$\overline{\text{AVRα D1}}$	AVRα D2	$\overline{\text{AVRα D2}}$	AVRα D3	$\overline{\text{AVRα D3}}$	AVRα D4	$\overline{\text{AVRα D4}}$	AVRα D5	$\overline{\text{AVRα D5}}$

INTERCONNECTION TABLE		RACK 07	LOC'N	NOTES
TYPE	SUB-RACK TERMINAL BLOCK	GROUP 22	SHEET 22	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			00						
b94		c	01	ERVOLTCα			07	01	06
b94		b	02	ERVOLTMα			07	01	09
			03						
07	09	09	04	COSVOLTCα			b094		H
07	09	02	05	SINVOLTCα			b094		E
07	09	23	06	COSVOLTMα			b094		I
07	09	16	07	SINVOLTMα			b094		F
07	09	37	08	COSVOLTFα			b094		B
07	09	30	09	SINVOLTFα			b094		G
			10						
b094		h	11	ERVOLTCα			07	01	08
b094		g	12	ERVOLTMα			07	01	11
			13						
07	09	08	14	COSVOLTCα			b094		M
07	09	03	15	SINVOLTCα			b094		J
07	09	22	16	COSVOLTMα			b094		N
07	09	17	17	SINVOLTMα			b094		K
07	09	36	18	COSVOLTFα			b094		C
07	09	31	19	SINVOLTFα			b094		L
			20						
b094		d	21	SCREEN					
b094		f	22	SCREEN					
			23						
			24	SCREEN			b094		Z
			25	SCREEN			b094		W
			26	SCREEN			b094		a
			27	SCREEN			b094		X
			28	SCREEN			b094		A
			29	SCREEN			b094		Y
			30						
			31						
			32						
			33						
			34						
			35						
			36						
			37						
			38						
			39						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	61	
TYPE	TERMINAL BLOCK 0761	GROUP 22	SHEET 23	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			00						
			01						
			02						
b94		k	03	ERVOLTFα			07	01	30
							07	62	46
			04						
			05						
			06						
			07						
			08						
			09						
			10						
			11						
			12						
b94		j	13	ERVOLTFα			07	01	32
			14						
			15						
			16						
			17						
			18						
			19						
			20						
			21						
			22						
07	62	48	23	+24V	SCREEN		b94		e
			24						
			25						
			26						
			27						
			28						
			29						
			30						
			31						
			32						
			33						
			34						
			35						
			36						
			37						
			38						
			39						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	62	GND : PIN 03
TYPE		GROUP	SHEET	
	TERMINAL BLOCK 0762	22	24	
		PART 1		

Iss. 1

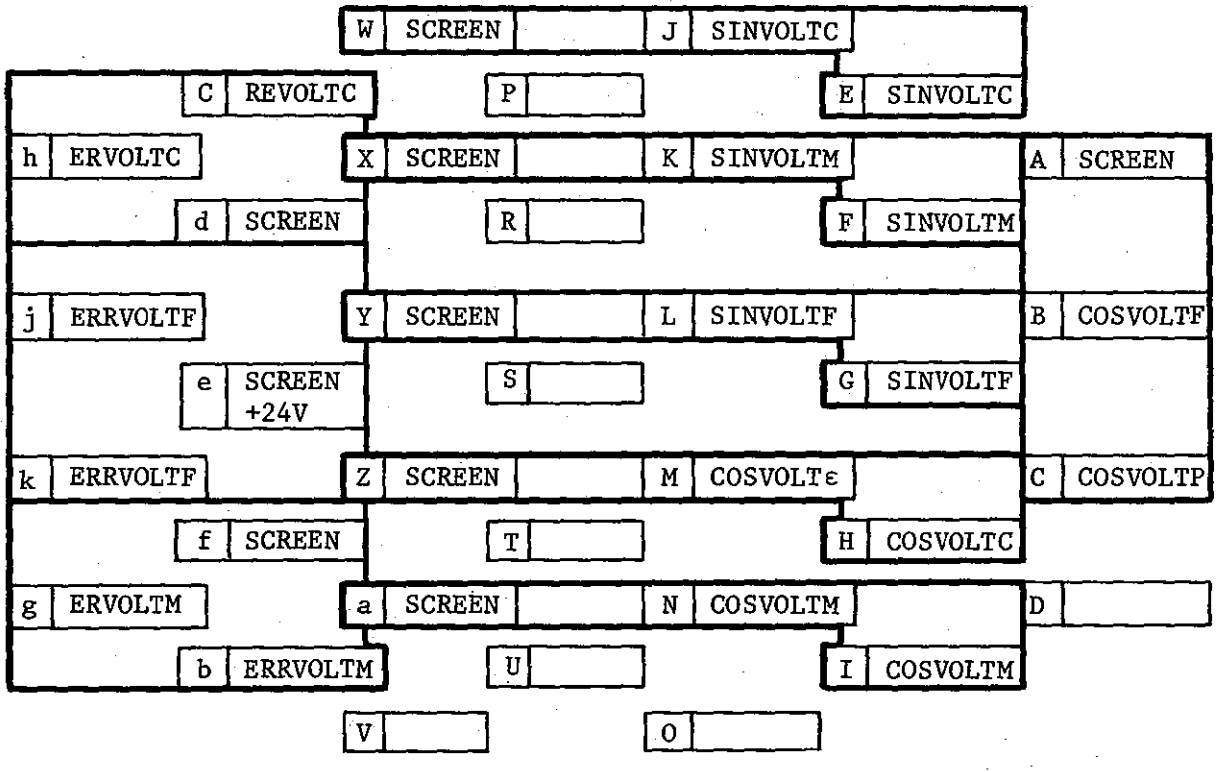
LOCATION			PIN	SIGNAL	PIN	LOCATION		
04	64	00	00	$\overline{\text{AVR}\alpha\ \text{A6}}$		07	22	29
64	64	01	01	AVRα A6		07	22	28
07	07	11	02	COARSEα		04	64	02
			03					
			04					
			05					
			06					
07	13	19	07	SIN VOLTFα		02	60	31
07	17	19	08	SIN VOLTHα		02	60	33
07	21	19	09	SIN VOLTCα		02	60	35
04	64	10	10	$\overline{\text{AVR}\alpha\ \text{B6}}$		07	22	40
04	64	11	11	AVRα B6		07	22	43
07	07	31	12	MEDIUMα		04	64	12
			13					
			14					
			15					
			16					
07	13	27	17	COS VOLTFα		02	60	32
07	17	27	18	COS VOLTMα		02	60	34
07	21	27	19	COS VOLTCα		02	60	36
04	64	20	20	$\overline{\text{AVR}\alpha\ \text{C6}}$		07	22	41
04	64	21	21	AVRα C6		07	22	44
07	05	15	22	DIR1α		04	64	22
			23					
			24					
			25					
			26					
			27					
			28					
			29					
04	64	30	30	$\overline{\text{AVR}\alpha\ \text{D6}}$		07	22	42
04	64	31	31	AVRα D6		07	22	45
07	05	33	32	DIR2α		04	64	32
			33					
			34					
			35					
			36					
			37					
			38					
			39					

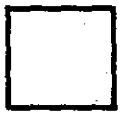
INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	64	
TYPE	TERMINAL BLOCK 0764	GROUP	SHEET	
		22	25	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
04	64	00	00	$\overline{\text{AVR}\alpha\ \text{A5}}$			07	22	25
04	64	01	01	AVRα A5			07	22	26
04	64	02	02	$\overline{\text{AVR}\alpha\ \text{A4}}$			07	23	29
04	64	03	03	AVRα A4			07	23	28
04	64	04	04	$\overline{\text{AVR}\alpha\ \text{A3}}$			07	23	25
04	64	05	05	AVRα A3			07	23	26
04	64	06	06	$\overline{\text{AVR}\alpha\ \text{A2}}$			07	24	29
04	64	07	07	AVRα A2			07	24	28
04	64	08	08	$\overline{\text{AVR}\alpha\ \text{A1}}$			07	24	25
04	64	09	09	AVRα A1			07	24	26
04	64	10	10	$\overline{\text{AVR}\alpha\ \text{B5}}$			07	22	02
04	64	11	11	AVRα B5			07	22	06
04	64	12	12	$\overline{\text{AVR}\alpha\ \text{B4}}$			07	23	40
04	64	13	13	AVRα B4			07	23	43
04	64	14	14	$\overline{\text{AVR}\alpha\ \text{B3}}$			07	23	02
04	64	15	15	AVRα B3			07	23	06
04	64	16	16	$\overline{\text{AVR}\alpha\ \text{B2}}$			07	24	40
04	64	17	17	AVRα B2			07	24	43
04	64	18	18	AVRα B1			07	24	02
04	64	19	19	$\overline{\text{AVR}\alpha\ \text{B1}}$			07	24	06
04	64	20	20	$\overline{\text{AVR}\alpha\ \text{C5}}$			07	22	10
04	64	21	21	AVRα C5			07	22	11
04	64	22	22	$\overline{\text{AVR}\alpha\ \text{C4}}$			07	23	41
04	64	23	23	AVRα C4			07	23	44
04	64	24	24	$\overline{\text{AVR}\alpha\ \text{C3}}$			07	23	10
04	64	25	25	AVRα C3			07	23	11
04	64	26	26	$\overline{\text{AVR}\alpha\ \text{C2}}$			07	24	41
04	64	27	27	AVRα C2			07	24	44
04	64	28	28	$\overline{\text{AVR}\alpha\ \text{C1}}$			07	24	10
04	64	29	29	AVRα C1			07	24	11
04	64	30	30	$\overline{\text{AVR}\alpha\ \text{D5}}$			07	22	04
04	64	31	31	AVRα D5			07	22	08
04	64	32	32	$\overline{\text{AVR}\alpha\ \text{D4}}$			07	23	42
04	64	33	33	AVRα D4			07	23	45
04	64	34	34	$\overline{\text{AVR}\alpha\ \text{D3}}$			07	23	04
04	64	35	35	AVRα D3			07	23	08
04	64	36	36	$\overline{\text{AVR}\alpha\ \text{D2}}$			07	24	42
04	64	37	37	AVRα D2			07	24	45
04	64	38	38	$\overline{\text{AVR}\alpha\ \text{D1}}$			07	24	04
04	64	39	39	AVRα D1			07	24	08

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		07	65	
TYPE	TERMINAL BLOCK 0765	GROUP	SHEET	
		22	26	
		PART 1		





= 1 CABLE

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
TYPE	MEASUREMENT SYSTEM CONNECTOR PLUG, b94	GROUP 22	SHEET 27	
		PART 1		

C1...C20 : 0.1uF - 160 V
R1...R20 : 1.2 k - 1W
R21...R60 : 1.5 k - ¼W
D1...D20 : OA 200
Tr1...Tr20 : BCY56

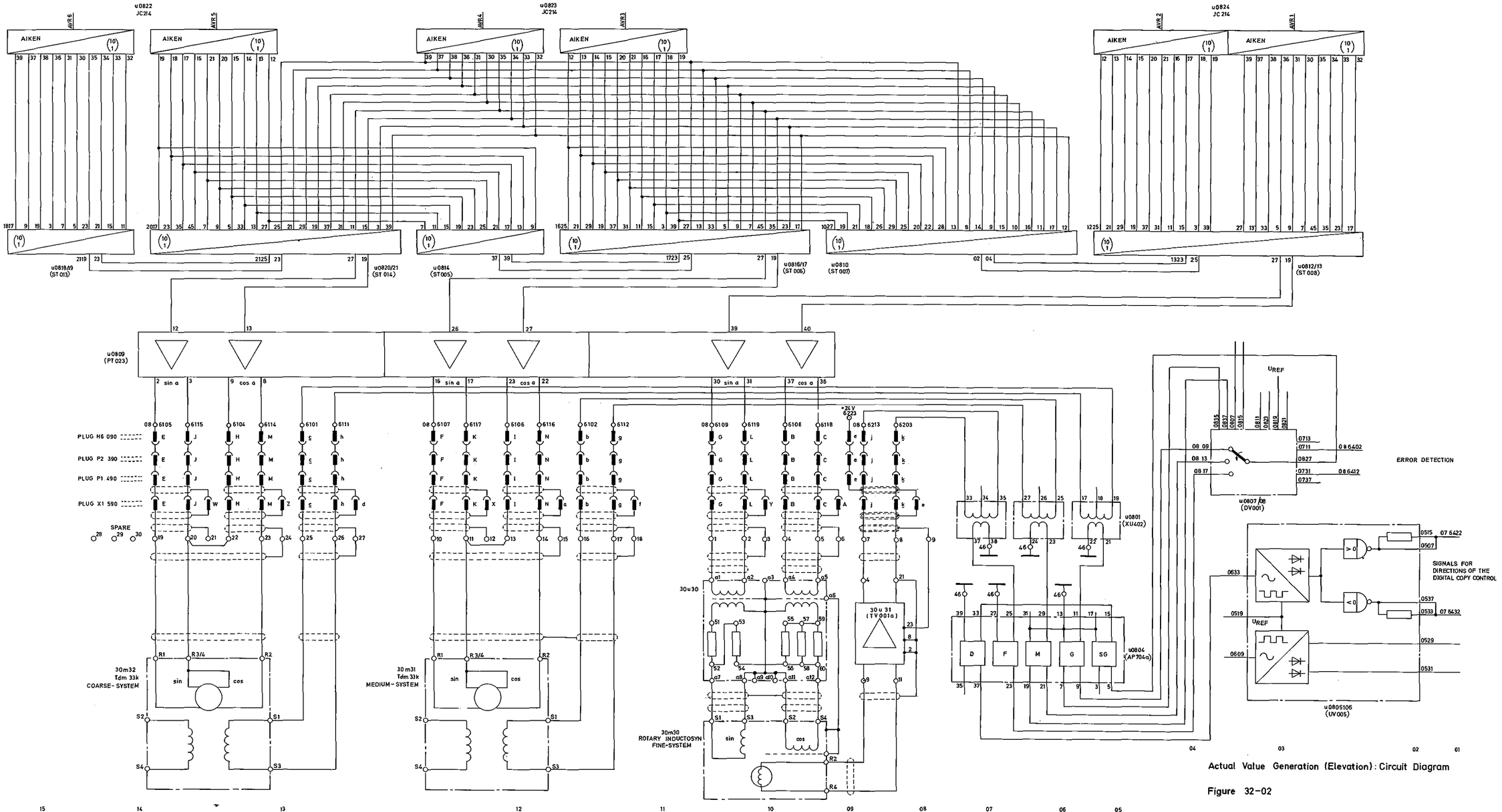
1/4 DTµL 946 &
1/3 DTµL 962 &
1/2 DTµL 930 &

Aiken to Decimal Decoder JC 214a-E: Circuit Diagram

Figure 22-28 Location See Figure 02-06

Part 1

Iss.1

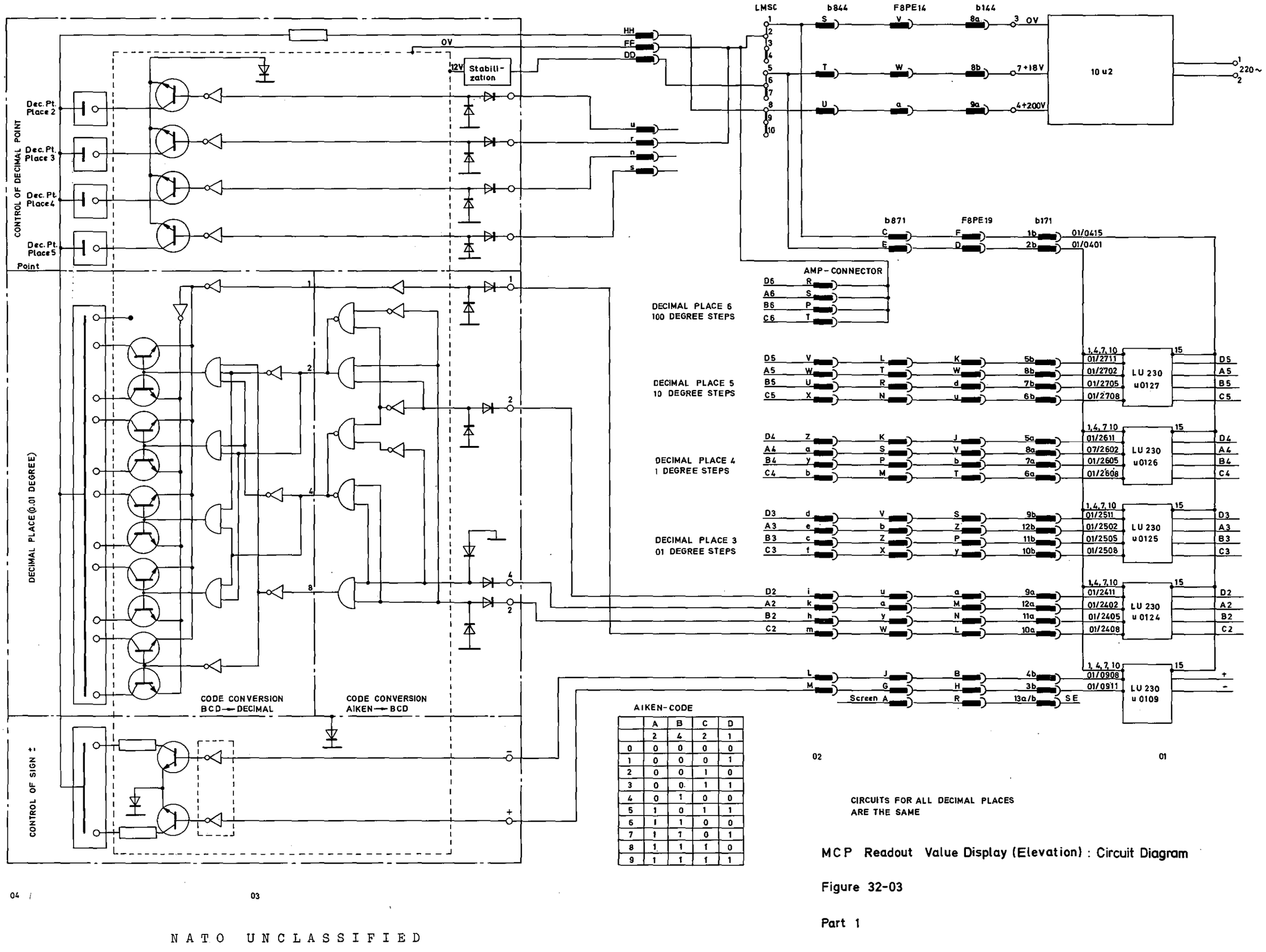


Actual Value Generation (Elevation): Circuit Diagram

Figure 32-02

Part 1

Iss.1



	A	B	C	D
	2	4	2	1
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

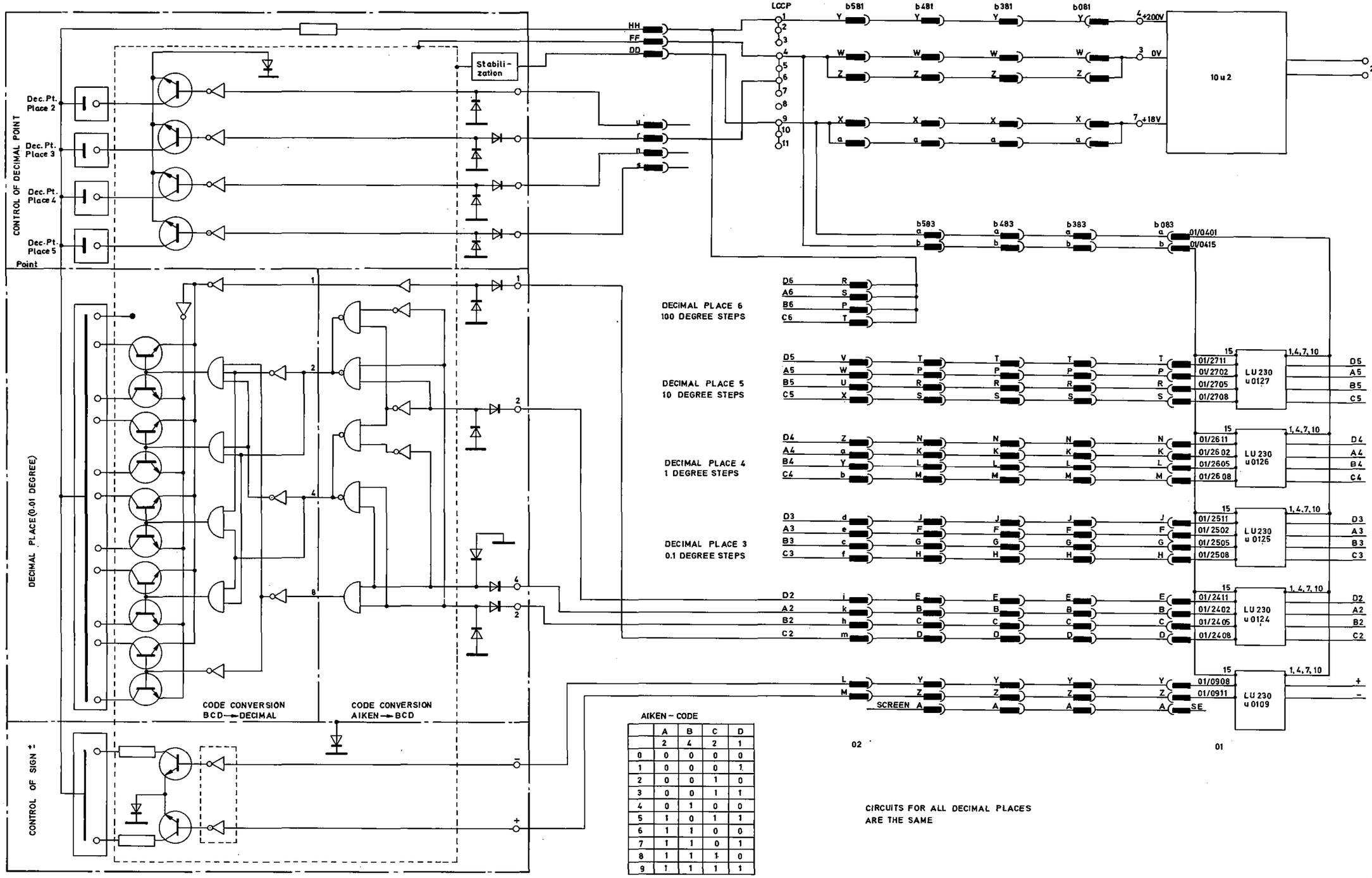
CIRCUITS FOR ALL DECIMAL PLACES ARE THE SAME

MCP Readout Value Display (Elevation) : Circuit Diagram

Figure 32-03

Part 1

Iss. 1



AIKEN – CODE

	A	B	C	D
	2	4	2	1
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	1	0	1	1
6	1	1	0	0
7	1	1	0	1
8	1	1	1	0
9	1	1	1	1

CIRCUITS FOR ALL DECIMAL PLACES ARE THE SAME

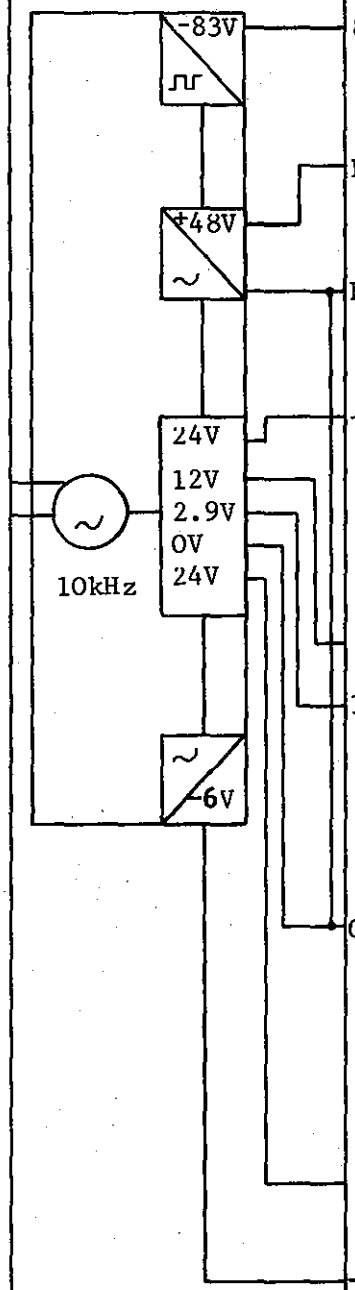
CCP Readout Value Display (Elevation) : Circuit Diagram

Figure 32–04

Part 1

Iss. 1

LOCATION			P	SIGNAL
>	01	49	1	-24V
>	01	46	41	GND
>	01	46	39	GND



SIGNAL	P	LOCATION		
83V	29	07	06	19
		07	08	31
		08	06	19
		08	08	31
REFVOLT	33	07	05	19
		07	07	23
		08	05	19
		08	07	23
REFVOLT	37	08	05	21
		08	07	25
		07	05	21
		07	07	25
+24VE	15	08	64	06
	11			
10kHz	21	07	10	07
		07	14	33
		07	18	13
		08	10	07
		08	14	33
		08	18	13
GND	19	08	01	46
		07	10	06
		07	14	35
		07	18	25
		08	10	06
		08	14	35
		08	18	25
	23			
-6V	7	07	05	05
		07	07	05
		08	05	05
		08	07	05
		08	64	16

INTERCONNECTION TABLE

RACK	LOC'N	NOTES
08	01 02 03	> See Figure 0205

TYPE		GROUP	SHEET
UV 006	10kHz OSCILLATOR	32	08

PART 1

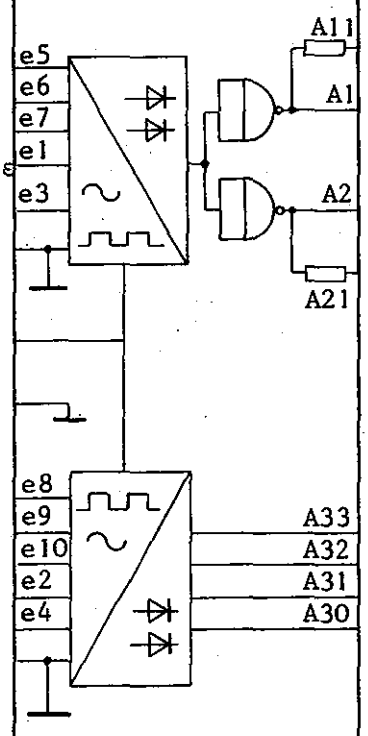
Iss. 1

LOCATION			P	SIGNAL		SIGNAL	P	LOCATION		
08	08	27	05	ERVOLT1ε	D		15	08	04	33
			17				3			
07	01	21	11	ERVOLT1Cε	C	ERVOLT3Cε	9	08	08	09
>	03	46	13	GND		ERVOLT2Cε	7	08	08	35
07	01	23	29	ERVOLT1Mε	M	ERVOLT3Mε	21	08	08	13
			31			ERVOLT2Mε	19	08	08	37
01	03	37	25	ERVOLT1Fε	F	ERVOLT3Fε	23	08	08	17
>	03	46	27	GND						
08	04	05	33		D	ERVOLT2ε	37	08	06	33
08	04	46	39	GND			35			
08	04	00	45	SGND						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	04	
TYPE		GROUP	SHEET	
AP 704	ERROR VOLTAGE ADJUSTMENT ε	32	09	
				PART 1

Iss. 1

LOCATION			P	SIGNAL
08	06	29	27*	
			29*	
08	04	37	33*	ERVOLT2ε
08	06	37	35*	
			37*	
08	01	33	19	REF VOLT
08	01	37	21	REF VOLT
08	06	05	03*	
			05*	
			09*	
08	06	13	11*	
			13*	
08	05	48	45	+24V
08	01	07	05	-6V
08	01	29	19	-83V
08	05	46	41	GND
08	05	00	39	SGND

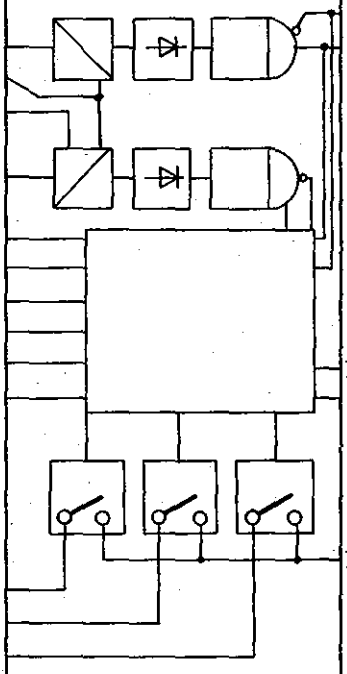


SIGNAL	P	LOCATION		
	15			
DIR1ε	07	08	64	22
DIR2ε	37	08	64	32
	33			
	25	08	05	27
	27			
	29			
	31			

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	05 06*	
TYPE		GROUP	SHEET	
UV 005	DISCRIMINATOR ε	32	10	
		PART 1		

Iss. 1

LOCATION			P	SIGNAL	SIGNAL	P	LOCATION		
08	04	07	35*	ERVOLT2Cε	COARSEα	13 11	08	64	02
08	01	33	23	REF VOLT					
			25						
08	04	19	37*	ERVOLT2Mε					
			7*						
			15*						
			11*						
			23*						
08	01	37	19*	REF VOLT	MEDIUMα	31	08	64	12
			21*						
					ERVOLT1α	27*	08	04	05
08	04	09	9*	ERVOLT3Cε					
08	04	21	13*	ERVOLT3Mε					
08	04	23	17*	ERVOLT3Fε					
>	07	48	45	+24V					
08	01	07	5	-6V					
08	01	29	31*	83V					
>	07	46	41*	GND					
>	07	00	39	SGND					



INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	07 08*	> See Figure 0205
TYPE		GROUP	SHEET	
DV 001	CMF SWITCH ε	32	11	
		PART 1		

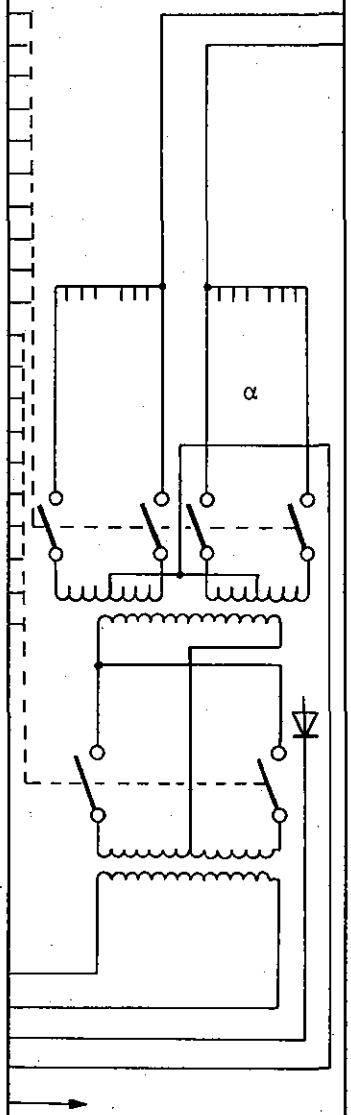
Iss. 1

LOCATION			P	SIGNAL		SIGNAL	P	LOCATION		
08	21	19	13	SINVOLTCε		SINCε	2	08	61	05
						SINCε	3	08	61	15
							5			
							7			
						COSCε	8	08	61	14
08	21	27	12	COSVOLTCε		COSCε	9	08	61	04
08	17	19	27	SINVOLTMε		SINMε	16	08	61	07
						SINMε	17	08	61	17
							19			
							21			
						COSMε	22	08	61	16
08	17	27	26	COSVOLTMε		COSMε	23	08	61	06
08	13	19	40	SINVOLTFε		SINFε	30	08	61	09
						SINFε	31	08	61	19
							33			
							35			
						COSFε	36	08	61	18
08	13	27	39	COSVOLTFε		COSFε	37	08	61	08
08	09	46	41	GND						
08	09	48	45	+24V						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	09	
TYPE		GROUP	SHEET	
PT 023	SIN/COS VOLTAGES OUTPUT ε	32	12	
		PART 1		

Iss. 1

LOCATION			P	SIGNAL
08	23	12	27	$\overline{\text{ZERO}\epsilon 3}$
08	23	13	19	$\overline{\text{ONE}\epsilon 3}$
08	23	14	21	$\overline{\text{TWO}\epsilon}$
08	23	15	18	$\overline{\text{THREE}\epsilon 3}$
08	23	20	26	$\overline{\text{FOUR}\epsilon 3}$
08	23	21	29	$\overline{\text{FIVE}\epsilon 3}$
08	23	16	25	$\overline{\text{SIX}\epsilon 3}$
08	23	17	20	$\overline{\text{SEVEN}\epsilon 3}$
08	23	18	22	$\overline{\text{EIGHT}\epsilon 3}$
08	23	19	28	$\overline{\text{NINE}\epsilon 3}$
08	23	39	13	$\overline{\text{ZERO}\epsilon 4}$
08	23	37	8	$\overline{\text{ONE}\epsilon 4}$
08	23	38	14	$\overline{\text{TWO}\epsilon 4}$
08	23	36	9	$\overline{\text{THREE}\epsilon 4}$
08	23	31	15	$\overline{\text{FOUR}\epsilon 4}$
08	23	30	10	$\overline{\text{FIVE}\epsilon 4}$
08	23	35	16	$\overline{\text{SIX}\epsilon 4}$
08	23	34	11	$\overline{\text{SEVEN}\epsilon 4}$
08	23	33	17	$\overline{\text{EIGHT}\epsilon 4}$
08	23	32	12	$\overline{\text{NINE}\epsilon 4}$
08	01	21	7	10kHz
08	01	19	6	10kHz
>	11	48	31	+24V
>	11	46	41	GND
>	11	48	45	24V

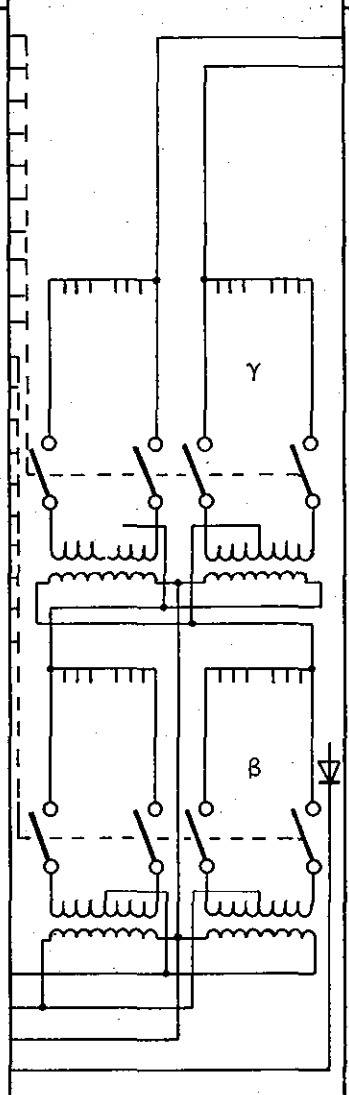


SIGNAL	P	LOCATION		
SINVOLTFϵ	2	08	13	25
COSVOLTFϵ	4	08	13	23

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	10 11	
TYPE		GROUP	SHEET	
ST 007	SIN/COS GENERATOR ϵ FINE UNIT	32	13	
		PART 1		

Iss. 1

LOCATION			P	SIGNAL
08	24	12	25	ZEROε 1
08	24	13	21	ONEε 1
08	24	14	29	TWOε 1
08	24	15	19	THREEε 1
08	24	20	37	FOURε 1
08	24	21	31	FIVEε 1
08	24	16	11	SIXε 1
08	24	17	15	SEVENε 1
08	24	18	3	EIGHTε 1
08	24	19	39	NINEε 1
08	24	39	27	ZEROε 2
08	24	37	13	ONEε 2
08	24	38	33	TWOε 2
08	24	36	5	THREEε 2
08	24	31	9	FOURε 2
08	24	30	7	FIVEε 2
08	24	35	45	SIXε 2
08	24	34	35	SEVENε 2
08	24	33	23	EIGHTε 2
08	24	32	17	NINEε 2
08	10	02	25*	SINVOLTFε
08	10	04	23*	COSVOLTε
>	13	46	41*	GND
>	13	48	21*	+24V
>	13	48	45*	+24V



SIGNAL	P	LOCATION		
SINVOLTFε	19*	07	09	40
		07	64	07
COSVOLTFε	27*	07	09	39
		07	64	17

INTERCONNECTION TABLE

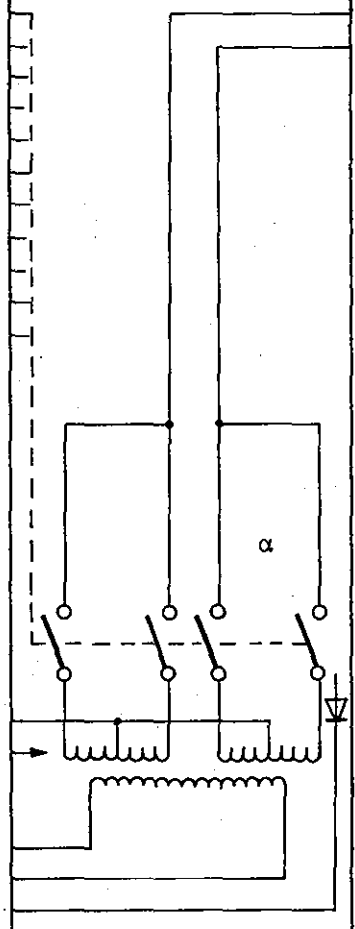
RACK	LOC'N	NOTES
08	12 13*	> See Figure 0205

TYPE		GROUP	SHEET
ST 008	SIN/COS GENERATOR ε FINE UNIT	32	14

PART 1

Iss. 1

LOCATION			P	SIGNAL
08	22	12	7	$\overline{\text{ZERO}\epsilon 5}$
08	22	13	11	$\overline{\text{ONE}\epsilon 5}$
08	22	14	15	$\overline{\text{TWO}\epsilon 5}$
08	22	15	19	$\overline{\text{THREE}\epsilon 5}$
08	22	20	23	$\overline{\text{FOUR}\epsilon 5}$
08	22	21	25	$\overline{\text{FIVE}\epsilon 5}$
08	22	16	21	$\overline{\text{SIX}\epsilon 5}$
08	22	17	17	$\overline{\text{SEVEN}\epsilon 5}$
08	22	18	13	$\overline{\text{EIGHT}\epsilon 5}$
08	22	19	9	$\overline{\text{NINE}\epsilon 5}$
>	15	46	41	GND
>	15	48	45	+24V
08	01	21	33	10KHz
08	01	19	35	10KHz
>	15	48	31	+24V

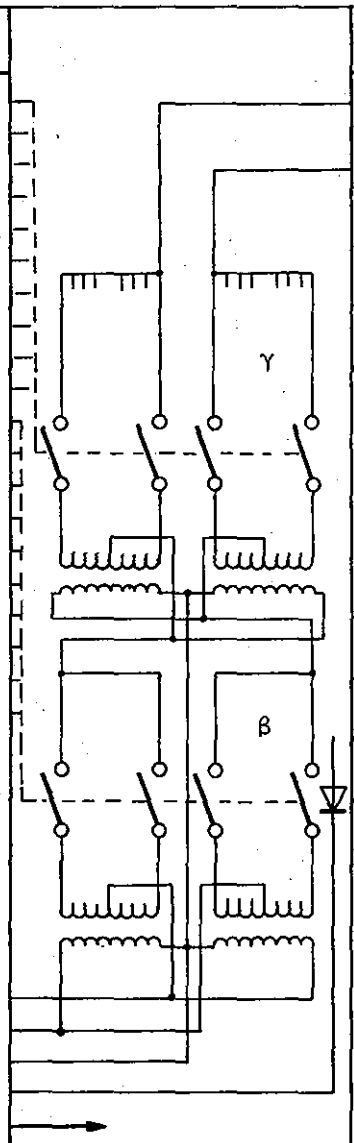


SIGNAL	P	LOCATION		
SINVOLTMε	37	08	17	25
COSVOLTMε	39	08	17	23

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	14 15	
TYPE		GROUP	SHEET	
ST 005	SIN/COS GENERATOR ε MEDIUM UNIT	32	15	
		PART 1		

Iss. 1

LOCATION			P	SIGNAL
08	23	12	25	ZERO ε3
08	23	13	21	ONE ε3
08	23	14	29	TWO ε3
08	23	15	19	THREE ε3
08	23	20	37	FOUR ε3
08	23	21	31	FIVE ε3
08	23	16	11	SIX ε3
08	23	17	15	SEVEN ε3
08	23	18	3	EIGHT ε3
08	23	19	39	NINE ε3
08	23	39	27	ZERO ε4
08	23	37	13	ONE ε4
08	23	38	33	TWO ε4
08	23	36	5	THREE ε4
08	23	31	9	FOUR ε4
08	23	30	7	FIVE ε4
08	23	35	45	SIX ε4
08	23	34	35	SEVEN ε4
08	23	33	23	EIGHT ε4
08	23	32	17	NINE ε4
08	14	37	25*	SINVOLTMε
08	14	39	23*	COSVOLTMε
>	17	46	41*	GND
>	17	48	21*	+24V
>	17	48	45*	+24V

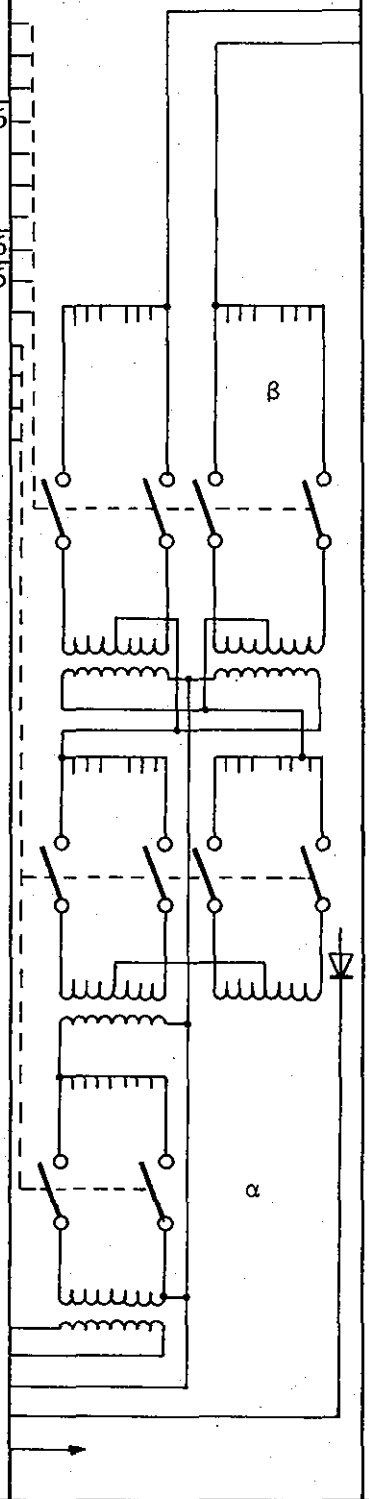


SIGNAL	P	LOCATION		
SINVOLTMε	19*	08	03	27
		08	64	08
COSVOLTMε	27*	08	09	26
		08	64	78

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	16 17*	> See Figure 0205
TYPE		GROUP	SHEET	
ST 006	SIN/COS GENERATOR ε MEDIUM UNIT	32	16	PART 1

Iss. 1

LOCATION			P	SIGNAL
08	22	39	17	ZERO ε6
08	22	37	9	ONE ε6
08	22	38	19	TWO ε6
08	22	36	3	THREE ε6
08	22	31	7	FOUR ε6
08	22	30	5	FIVE ε6
08	22	35	23	SIX ε6
08	22	34	21	SEVEN ε6
08	22	33	15	EIGHT ε6
08	22	34	11	NINE ε6
>	18	46	35	GND
		48	31	+24V
		48	29	+24V
		48	33	+24V
08	01	21	43	10kHz
08	01	19	25	10kHz
>	19	46	41*	GND
>	19	48	27	+24V
>	19	48	45*	+24V

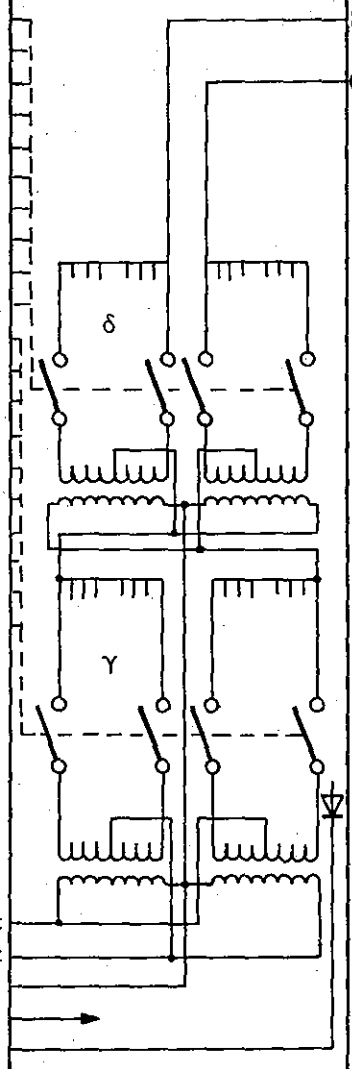


SIGNAL	P	LOCATION		
SINVOLTCε	21*	08	21	25
COSVOLTCε	23*	08	21	23

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	18 19*	> See Figure 0205
TYPE		GROUP	SHEET	
ST 013	SIN/COS GENERATOR ε COARSE UNIT	32	17	PART 1

Iss. 1

LOCATION			P	SIGNAL
08	23	39	25	ZERO ε4
08	23	37	21	ONE ε4
08	23	38	29	TWO ε4
08	23	36	19	THREE ε4
08	23	31	37	FOUR ε4
08	23	30	31	FIVE ε4
08	23	35	11	SIX ε4
08	23	34	15	SEVEN ε4
08	23	33	3	EIGHT ε4
08	23	32	39	NINE ε4
08	23	12	27	ZERO ε5
08	23	13	13	ONE ε5
08	23	14	33	TWO ε5
08	23	15	5	THREE ε5
08	23	20	9	FOUR ε5
08	23	21	7	FIVE ε5
08	23	16	45	SIX ε5
08	23	17	35	SEVEN ε5
08	23	18	23	EIGHT ε5
08	23	19	17	NINE ε5
08	19	21	25*	SINVOLTCε
08	19	23	23*	COSVOLTCε
08	21	46	41*	GND
08	21	48	45*	+24V
08	21	48	21*	+24V



SIGNAL	P	LOCATION		
SINVOLTCε	19*	08	09	13
		08	64	09
COSVOLTCε	27*	08	09	12
		08	64	19

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	20 21*	
TYPE		GROUP	SHEET	
ST 014	SIN/COS GENERATOR ε COARSE UNIT	32	18	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			1			7			
08	65	10	2	AVRε B5	ZEROε 5	12	08	14	07
							08	20	27
08	65	30	4	AVRε D5	ONEε 5	13	08	14	11
							08	20	13
08	65	11	6	AVRε B5	TWOε 5	14	08	14	15
							08	20	33
08	65	31	8	AVRε D5	THREEε 5	15	08	14	19
							08	20	05
08	65	20	10	AVRε C5	SIXε 5	16	08	14	21
							08	20	45
08	65	21	11	AVRε C5	SEVENε 5	17	08	14	17
							08	20	35
			22		EIGHTε 5	18	08	14	13
							08	20	23
			23		NINEε 5	19	08	14	09
							08	20	17
08	65	00	25	AVRε A5	FOURε 5	20	08	14	23
							08	20	09
08	65	01	26	AVRε A5	FIVEε 5	21	08	14	25
							08	20	07
			27			24			
08	64	01	28	AVRε A6	FIVEε 6	30	08	18	05
08	64	00	29	AVRε A6	FOURε 6	31	08	18	07
08	64	10	40	AVRε B6	NINEε 6	32	08	18	11
08	64	20	41	AVRε C6	EIGHTε 6	33	08	18	15
08	64	30	42	AVRε D6	SEVENε 6	34	08	18	21
08	64	11	43	AVRε B6	SIXε 6	35	08	18	23
08	64	21	44	AVRε C6	THREEε 6	36	08	18	03
08	64	31	45	AVRε D6	ONEε 6	37	08	18	09
					TWOε 6	38	08	18	19
					ZEROε 6	39	08	18	17

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	22	+5V : PIN 5 GND : PIN 3, 27 +24V: PIN 9
TYPE		GROUP	SHEET	
JC214	AIKEN → DECIMAL DECODER ε	32	19	
				PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			1			7			
08	65	14	2	$\overline{\text{AVRε B3}}$	$\overline{\text{ZEROε 3}}$	12	08	10	27
							08	16	25
08	65	30	4	$\overline{\text{AVRε D3}}$	$\overline{\text{ONEε 3}}$	13	08	10	19
							08	16	21
08	65	15	6	AVRε B3	$\overline{\text{TWOε 3}}$	14	08	10	21
							08	16	29
08	65	31	8	AVRε D3	$\overline{\text{THREEε 3}}$	15	08	10	18
							08	16	19
08	65	24	10	$\overline{\text{AVRε C3}}$	$\overline{\text{SIXε 3}}$	16	08	10	25
							08	16	11
08	65	25	11	AVRε C3	$\overline{\text{SEVENε 3}}$	17	08	10	20
							08	16	15
			22		$\overline{\text{EIGHTε 3}}$	18	08	10	22
							08	16	03
			23		$\overline{\text{NINEε 3}}$	19	08	10	28
							08	16	39
08	65	04	25	$\overline{\text{AVRε A3}}$	$\overline{\text{FOURε 3}}$	20	08	10	26
							08	16	37
08	65	05	26	AVRε A3	$\overline{\text{FIVEε 3}}$	21	08	10	29
							08	16	31
			27			24			
08	65	03	28	AVRε A4	$\overline{\text{FIVEε 4}}$	30	08	10	10
							08	16	07
							08	20	31
08	65	02	29	$\overline{\text{AVRε A4}}$	$\overline{\text{FOURε4}}$	31	08	10	15
							08	16	09
							08	20	37
08	65	12	40	$\overline{\text{AVRε B4}}$	$\overline{\text{NINEε 4}}$	32	08	10	12
							08	16	17
							08	20	39
08	65	22	41	$\overline{\text{AVRε C4}}$	$\overline{\text{EIGHTε 4}}$	33	08	10	17
							08	16	23
							08	20	03
08	65	32	42	$\overline{\text{AVRε D4}}$	$\overline{\text{SEVENε 4}}$	34	08	10	11
							08	16	35
							08	20	15
08	65	13	43	AVRε B4	$\overline{\text{SIXε 4}}$	35	08	10	16
							08	16	45
							08	20	11
08	65	23	44	AVRε C4	$\overline{\text{THREEε 4}}$	36	08	10	09
							08	16	05
							08	20	19
08	65	33	45	AVRε D4	$\overline{\text{ONEε 4}}$	37	08	10	08
							08	16	13
							08	20	21

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	23	+5V : PIN 5 GND : PIN 3, 27 +24V: PIN 9
TYPE	AIKEN → DECIMAL DECODER ε	GROUP	SHEET	
JC214	SHEET 1	32	20	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL	PIN	LOCATION		
				$\overline{\text{TWO}\varepsilon\ 4}$	38	08	10	17
						08	16	33
						08	20	29
				$\overline{\text{ZERO}\varepsilon\ 4}$	39	08	10	13
						08	16	27
						08	20	25

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	23	+5V : PIN 5 GND : PIN 3, 27 +24V: PIN 9
TYPE		GROUP	SHEET	
JC214	AIKEN → DECIMAL DECODER ε SHEET 2	32	20	PART 1

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			1			7			
08	65	18	2	AVRε B1	ZEROε 1	12	08	12	25
08	65	38	4	AVRε D1	ONEε 1	13	08	12	21
08	65	19	6	AVRε B1	TWOε 1	14	08	12	29
08	65	39	8	AVRε D1	THREEε 1	15	08	12	19
08	65	28	10	AVRε C1	SIXε 1	16	08	12	11
08	65	29	11	AVRε C1	SEVENε 1	17	08	12	15
			22		EIGHTε 1	18	08	12	03
			23		NINEε 1	19	08	12	39
08	65	08	25	AVRε A1	FOURε 1	20	08	12	37
08	65	09	26	AVRε A1	FIVEε 1	21	08	12	31
			27			24			
08	65	06	28	AVRε A2	FIVEε 2	30	08	12	07
08	65	07	29	AVRε A2	FOURε 2	31	08	12	09
08	65	16	40	AVRε B2	NINEε 2	32	08	12	17
08	65	26	41	AVRε C2	EIGHTε 2	33	08	12	23
08	65	36	42	AVRε D2	SEVENε 2	34	08	12	35
08	65	17	43	AVRε B2	SIXε 2	35	08	12	45
08	65	27	44	AVRε C2	THREEε 2	36	08	12	05
08	65	37	45	AVRε D2	ONEε 2	37	08	12	13
					TWOε 2	38	08	12	33
					ZEROε 2	39	08	12	27

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	24	+5V : PIN 5 GND : PIN 3, 27 +24V: PIN 9
TYPE		GROUP	SHEET	
JC214	AIKEN → DECIMAL DECODER ε	32	21	
		PART 1		

Iss. 1

		9	8	7	6	5	4	3	2	1	0
61	0	SINVOLTFε	COSVOLTFε	SINVOLTMε	COSVOLTMε	SINVOLTCε	COSVOLTCε		ERVOLTMε	ERVOLTCε	
	1	SINVOLTFε	COSVOLTFε	SINVOLTMε	COSVOLTMε	SINVOLTCε	COSVOLTCε		ERVOLTMε	ERVOLTCε	
	2	SCREENε	SCREENε	SCREENε	SCREENε	SCREENε	SCREENε		SCREENε	SCREENε	
	3										
62	0							ERVOLTFε			
	1							(GND) ERVOLTFε			
	2							(+24V) SCREENε			
	3										
63	0										
	1										
	2										
	3										
64	0	SINVOLTCε	SINVOLTMε	SINVOLTFε	10kHz				COARSEε	AVRε A6	$\overline{\text{AVRε A6}}$
	1	COSVOLTCε	COSVOLTMε	COSVOLTFε	-6V				MEDIUMε	AVRε B6	$\overline{\text{AVRε B6}}$
	2								DIR1ε	AVRε C6	$\overline{\text{AVRε C6}}$
	3								DIR2ε	AVRε D6	$\overline{\text{AVRε D6}}$
65	0	AVRε A1	$\overline{\text{AVRε A1}}$	AVRε A2	$\overline{\text{AVRε A2}}$	AVRε A3	$\overline{\text{AVRε A3}}$	AVRε A4	$\overline{\text{AVRε A4}}$	AVRε A5	$\overline{\text{AVRε A5}}$
	1	AVRε B1	$\overline{\text{AVRε B1}}$	AVRε B2	$\overline{\text{AVRε B2}}$	AVRε B3	$\overline{\text{AVRε B3}}$	AVRε B4	$\overline{\text{AVRε B4}}$	AVRε B5	$\overline{\text{AVRε B5}}$
	2	AVRε C1	$\overline{\text{AVRε C1}}$	AVRε C2	$\overline{\text{AVRε C2}}$	AVRε C3	$\overline{\text{AVRε C3}}$	AVRε C4	$\overline{\text{AVRε C4}}$	AVRε C5	$\overline{\text{AVRε C5}}$
	3	AVRε D1	$\overline{\text{AVRε D1}}$	AVRε D2	$\overline{\text{AVRε D2}}$	AVRε D3	$\overline{\text{AVRε D3}}$	AVRε D4	$\overline{\text{AVRε D4}}$	AVRε D5	$\overline{\text{AVRε D5}}$

INTERCONNECTION TABLE		RACK 08	LOC'N	NOTES
TYPE	SUB-RACK TERMINAL BLOCK	GROUP 32	SHEET 22	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			00						
b90		c	01	ERVOLTCε			07	01	19
b90		b	02	ERVOLTMε			07	01	25
			03						
08	09	09	04	COSVOLTCε			b90		H
08	09	02	05	SINVOLTCε			b90		E
08	09	23	06	COSVOLTMε			b90		I
08	09	16	07	SINVOLTMε			b90		F
08	09	37	08	COSVOLTFε			b90		B
08	09	30	09	SINVOLTFε			b90		G
			10						
b90		h	11	ERVOLTCε			07	01	17
b90		g	12	ERVOLTMε			07	01	27
			13						
08	09	08	14	COSVOLTCε			b90		M
08	09	03	15	SINVOLTCε			b90		J
08	09	22	16	COSVOLTMε			b90		N
08	09	17	17	SINVOLTMε			b90		K
08	09	36	18	COSVOLTFε			b90		C
08	09	31	19	SINVOLTFε			b90		L
			20						
b90		d	21	SCREEN					
b90		f	22	SCREEN					
			23						
			24	SCREEN			b90		Z
			25	SCREEN			b90		W
			26	SCREEN			b90		a
			27	SCREEN			b90		X
			28	SCREEN			b90		A
			29	SCREEN			b90		Y
			30						
			31						
			32						
			33						
			34						
			35						
			36						
			37						
			38						
			39						

INTERCONNECTION TABLE	RACK	LOC'N	NOTES
	08	61	
TYPE	GROUP	SHEET	
	32	23	
TERMINAL BLOCK 0861	PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
			00						
			01						
			02						
b90		k	03	ERVOLTFε			07	01	33
							08	62	46
			04						
			05						
			06						
			07						
			08						
			09						
			10						
			11						
			12						
b90		j	13	ERVOLTFε			07	01	35
			14						
			15						
			16						
			17						
			18						
			19						
			20						
			21						
			22						
08	62	48	23	+24V			b90		e
			24						
			25						
			26						
			27						
			28						
			29						
			30						
			31						
			32						
			33						
			34						
			35						
			36						
			37						
			38						
			39						

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	62	GND : PIN 03
TYPE	TERMINAL BLOCK 0862	GROUP 32	SHEET 24	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	64	00	00	$\overline{\text{AVRε A6}}$			08	22	29
05	64	01	01	AVRε A6			08	22	28
08	07	11	02	COARSEε			05	64	02
			03						
			04						
			05						
08	01	15	06	24VE			02	60	43
08	13	19	07	SIN VOLTFα			02	60	37
08	17	19	08	SIN VOLTMε			02	60	39
08	21	19	09	SIN VOLTCε			02	60	41
05	64	10	10	$\overline{\text{AVRε B6}}$			08	22	40
05	64	11	11	AVRε B6			08	22	43
08	07	31	12	MEDIUMε			05	64	12
			13						
			14						
			15						
08	01	07	16	-6V			02	60	44
08	13	27	17	COS VOLTFε			02	60	38
08	17	27	18	COS VOLTMε			02	60	40
08	21	27	19	COS VOLTCε			02	60	42
05	64	20	20	$\overline{\text{AVRε C6}}$			08	22	41
05	64	21	21	AVRε C6			08	22	44
08	05	15	22	DIR1ε			05	64	22
			23						
			24						
			25						
			26						
			27						
			28						
			29						
05	64	30	30	$\overline{\text{AVRε D6}}$			08	22	42
05	64	31	31	AVRε D6			08	22	45
08	05	33	32	DIR2ε			05	64	32
			33						
			34						
			35						
			36						
			37						
			38						
			39						

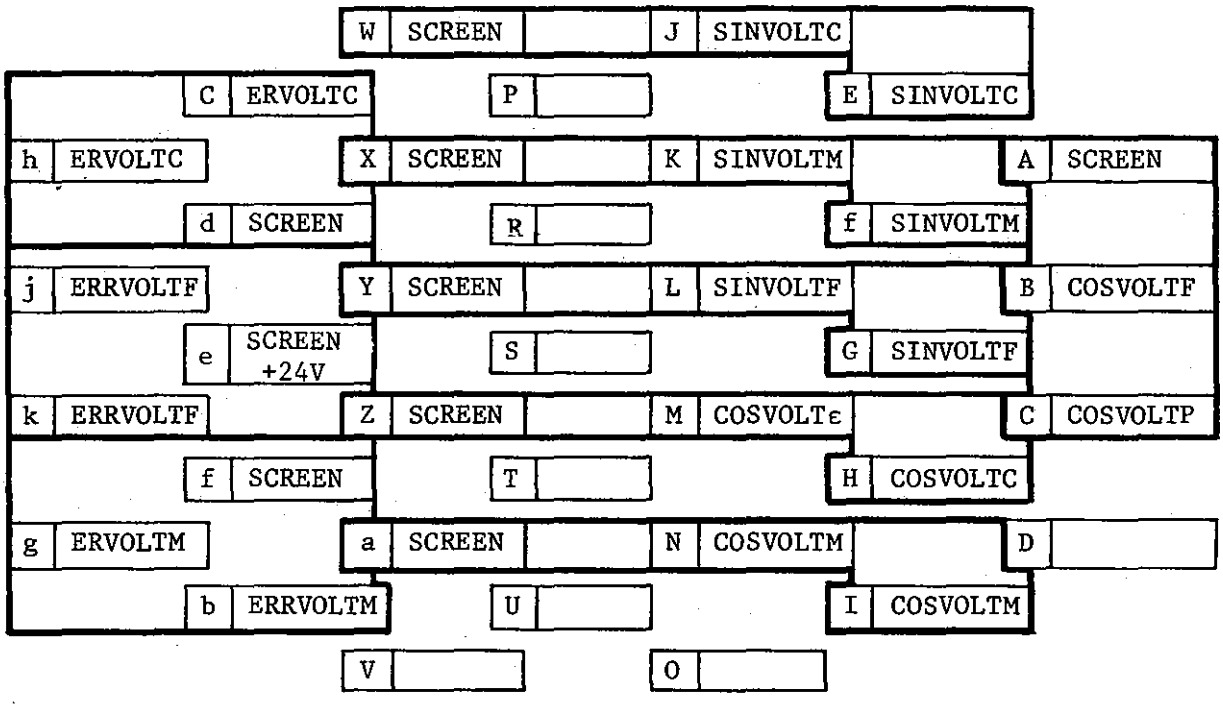
INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	64	
TYPE	TERMINAL BLOCK 0864	GROUP	SHEET	
		32	25	
		PART 1		

Iss. 1

LOCATION			PIN	SIGNAL		PIN	LOCATION		
05	64	00	00	$\overline{\text{AVRε A5}}$			08	22	25
05	64	01	01	AVRε A5			08	22	26
05	64	02	02	$\overline{\text{AVRε A4}}$			08	23	29
05	64	03	03	AVRε A4			08	23	28
05	64	04	04	$\overline{\text{AVRε A3}}$			08	23	25
05	64	05	05	AVRε A3			08	23	26
05	64	06	06	$\overline{\text{AVRε A2}}$			08	24	29
05	64	07	07	AVRε A2			08	24	28
05	64	08	08	$\overline{\text{AVRε A1}}$			08	24	25
05	64	09	09	AVRε A1			08	24	26
05	64	10	10	$\overline{\text{AVRε B5}}$			08	22	02
05	64	11	11	AVRε B5			08	22	06
05	64	12	12	$\overline{\text{AVRε B4}}$			08	23	40
05	64	13	13	AVRε B4			08	23	43
05	64	14	14	$\overline{\text{AVRε B3}}$			08	23	02
05	64	15	15	AVRε B3			08	23	06
05	64	16	16	$\overline{\text{AVRε B2}}$			08	24	40
05	64	17	17	AVRε B2			08	24	43
05	64	18	18	$\overline{\text{AVRε B1}}$			08	24	02
05	64	19	19	AVRε B1			08	24	06
05	64	20	20	$\overline{\text{AVRε C5}}$			08	22	10
05	64	21	21	AVRε C5			08	22	11
05	64	22	22	$\overline{\text{AVRε C4}}$			08	23	41
05	64	23	23	AVRε C4			08	23	44
05	64	24	24	$\overline{\text{AVRε C3}}$			08	23	10
05	64	25	25	AVRε C3			08	23	11
05	64	26	26	$\overline{\text{AVRε C2}}$			08	24	41
05	64	27	27	AVRε C2			08	24	44
05	64	28	28	$\overline{\text{AVRε C1}}$			08	24	10
05	64	29	29	AVRε C1			08	24	11
05	64	30	30	$\overline{\text{AVRε D5}}$			08	22	04
05	64	31	31	AVRε D5			08	22	08
05	64	32	32	$\overline{\text{AVRε D4}}$			08	23	42
05	64	33	33	AVRε D4			08	23	45
05	64	34	34	$\overline{\text{AVRε D3}}$			08	23	04
05	64	35	35	AVRε D3			08	23	08
05	64	36	36	$\overline{\text{AVRε D2}}$			08	24	42
05	64	37	37	AVRε D2			08	24	45
05	64	38	38	$\overline{\text{AVRε D1}}$			08	24	04
05	64	39	39	AVRε D1			08	24	08

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
		08	65	
TYPE	TERMINAL BLOCK 0865	GROUP	SHEET	
		32	26	
		PART 1		

Iss. 1



= 1 CABLE

INTERCONNECTION TABLE		RACK	LOC'N	NOTES
TYPE	MEASUREMENT SYSTEM CONNECTOR PLUG b90	GROUP 32	SHEET 27	
		PART 1		